

An RFID Distance Bounding Protocol

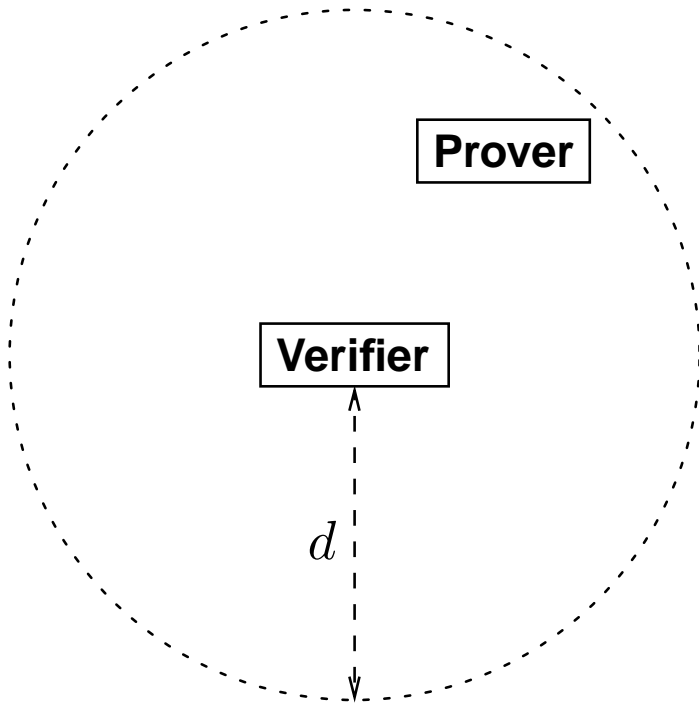
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May 22, 2006



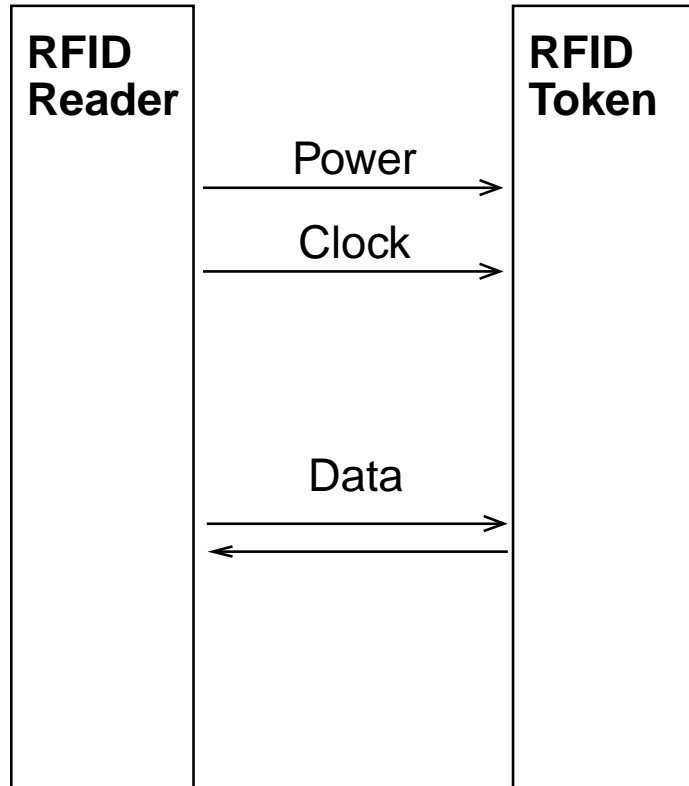
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Distance bounding



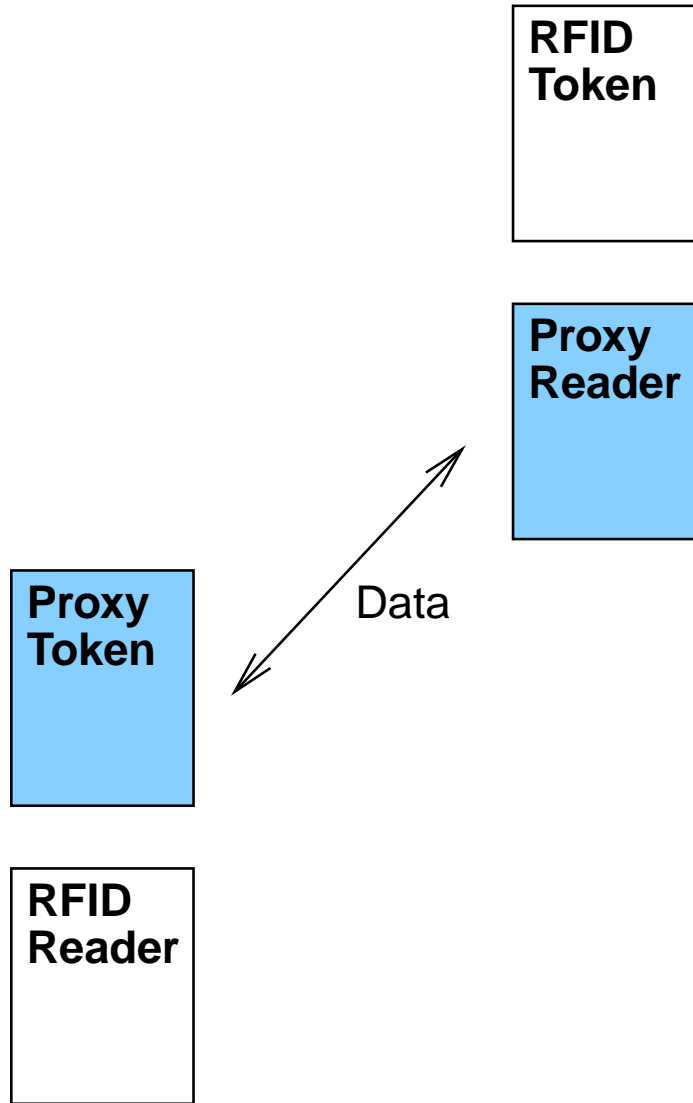
- Places an upper bound on physical distance
- Does not provide absolute location
- Operates on physical characteristics of the communication medium.
- Supplements existing security mechanisms

RFID devices



- Various applications
- Passive devices with low resources
- Limited range
- Used to link an item or person to a location

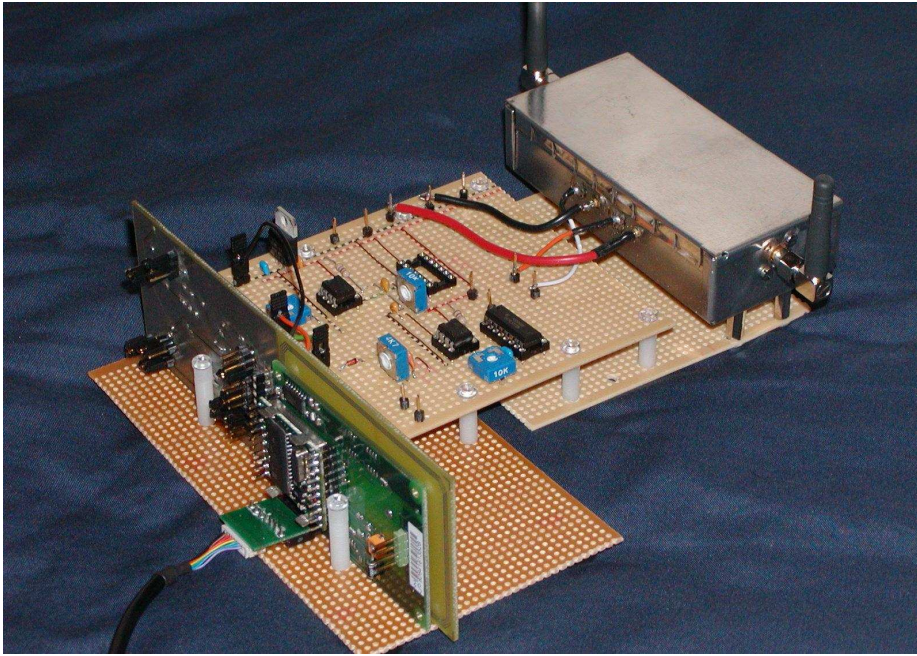
Relay attack



- Simple, well known attack
- Circumvents application layer security protocols

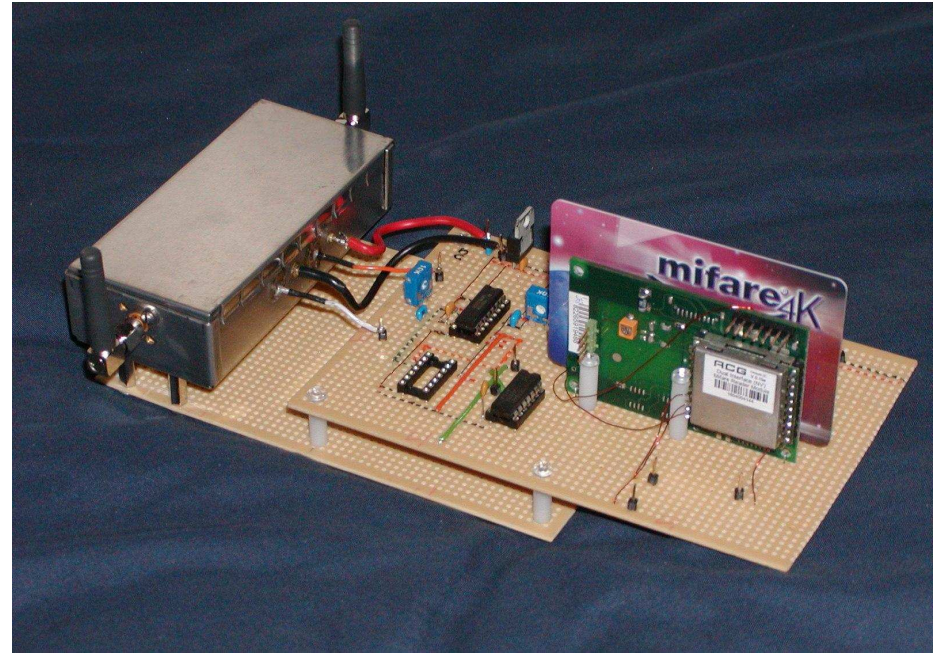
Relay attack demonstration

Proxy Token



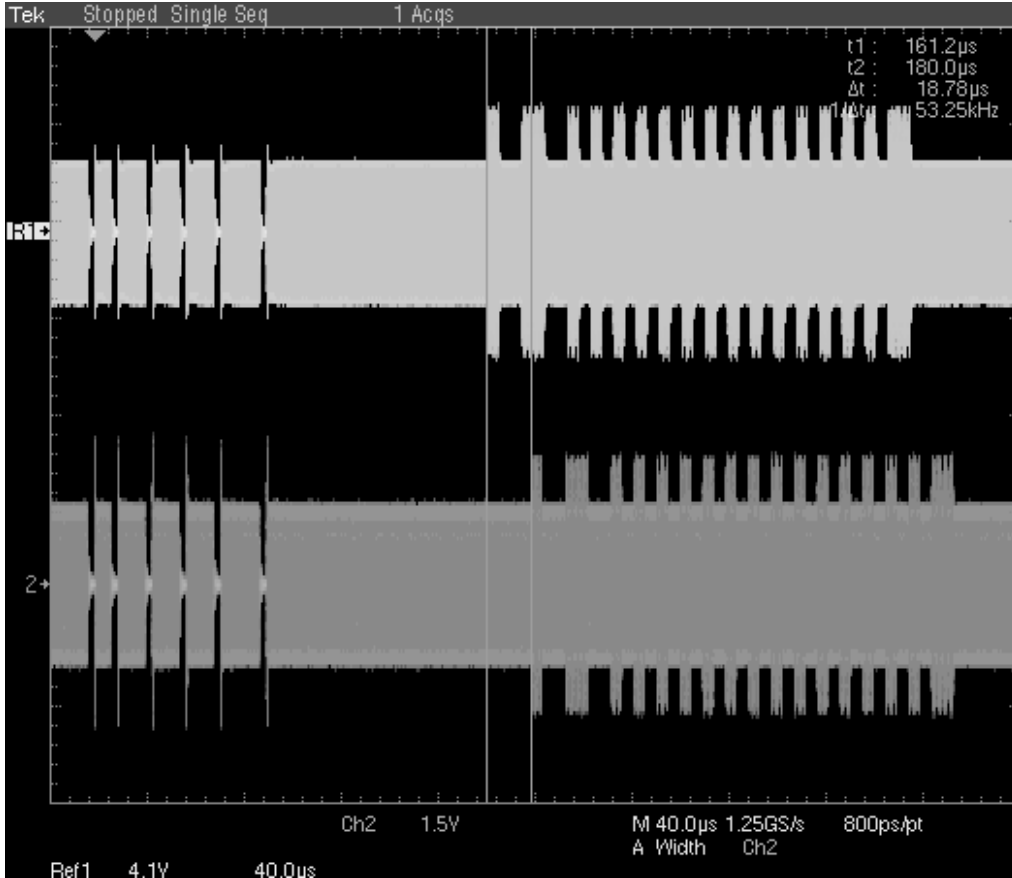
- 14443 A/B test card circuit
- Signal processing with discrete components
- Duplex RF link

Proxy Reader



- Commercial reader module
- Reprogrammed with our firmware
- Price \approx \$ 100

Relay attack detection



Timing difference between an actual token (top) and a Proxy token(bottom) response to a reader's *REQA* command.

- Delay
 - Could be reduced with complex hardware
 - Cannot be less than 3 ns/m
- Physical layer
 - High-resolution timing
- Application layer
 - Timing information lost

Our Protocol

Goals

- Suited to RFID environment
 - Verifier handles demanding processing functions
 - Prover performs simple functions
- Provide same level of security as other distance bounding protocols
 - Should not be worse because it has hardware constraints
- Implementation
 - Suggest practical ideas on how to implement our protocol
 - Protocol should supplement current RFID standards, not suggest wholesale changes

Protocol assumptions

Security target

- Places an upper bound on the distance between Verifier and Prover
- Does not provide non-repudiation of location to a third party
- The Prover does not collude with an attacker

Crypto primitives

- Shared secret key, K
- Shared pseudorandom function, h
- Nonces N_V, N_P are of sufficient length and will not be repeated

Protocol assumptions (2)

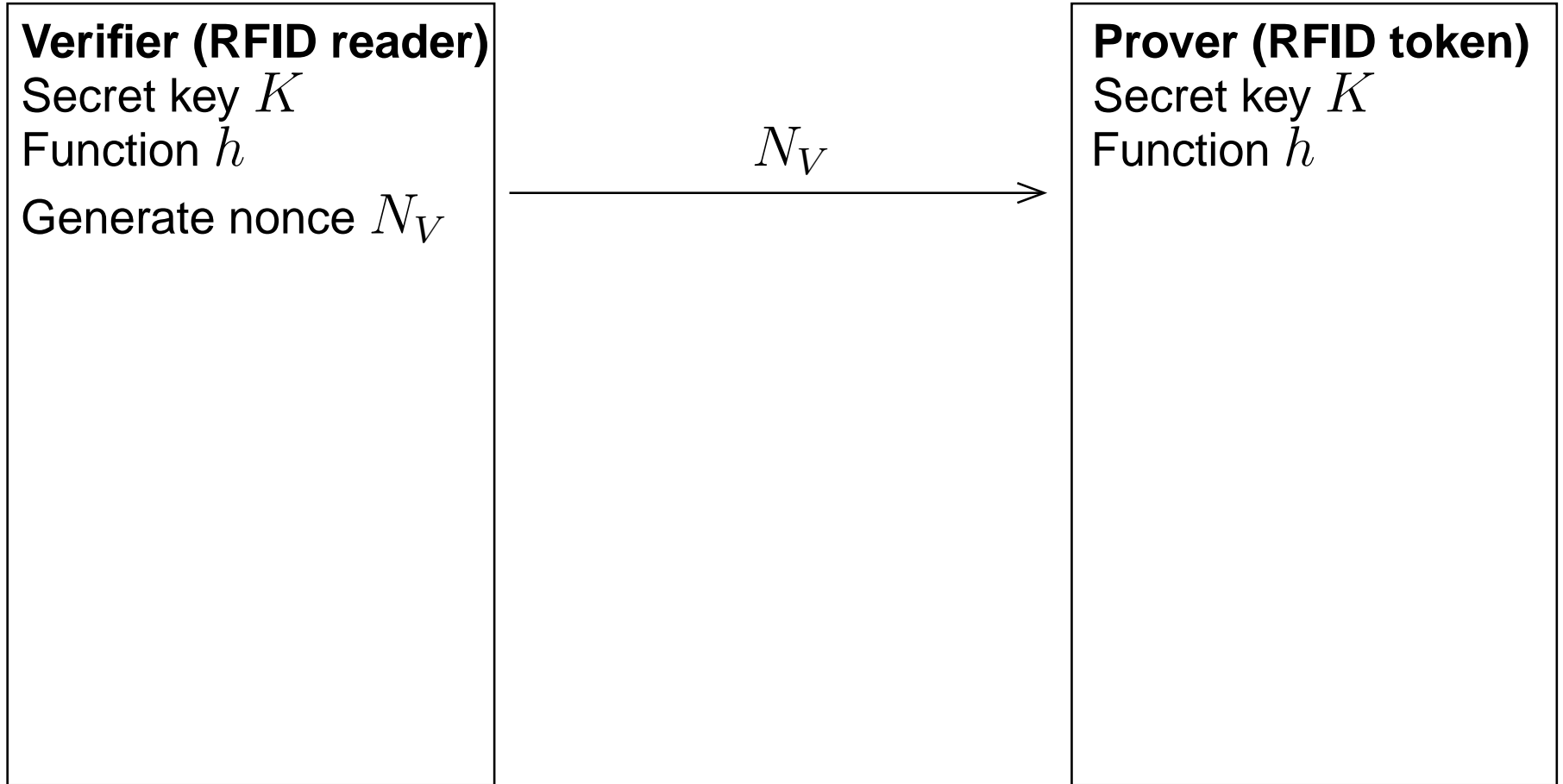
Time base

- Verifier is computationally strong
 - Perform accurate timing operations
- Prover is computationally weak
 - Cannot determine accurate timing information
 - Uses external clock signal (received carrier)
 - Prover can detect large deviations in clock frequency

Communication channels

- Low bandwidth error corrected channel
- High bandwidth rapid bit exchange channel

Protocol description



Protocol description

Verifier (RFID reader)

Secret key K

Function h

Generate nonce N_V

Generate random bits

C_1, \dots, C_n

$\langle C_i \rangle = 01001100$

N_V

Prover (RFID token)

Secret key K

Function h

Calculate

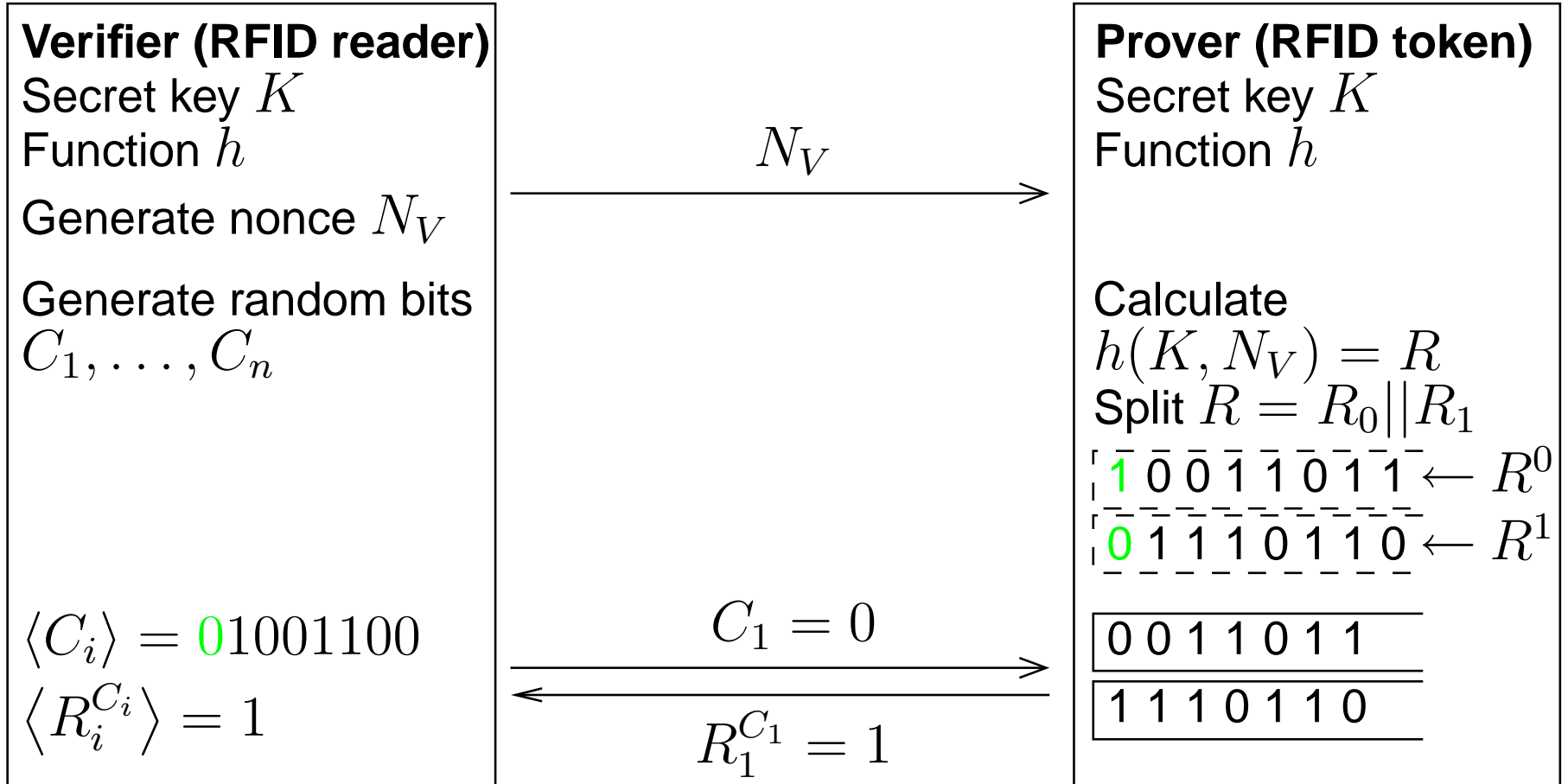
$h(K, N_V) = R$

Split $R = R_0 || R_1$

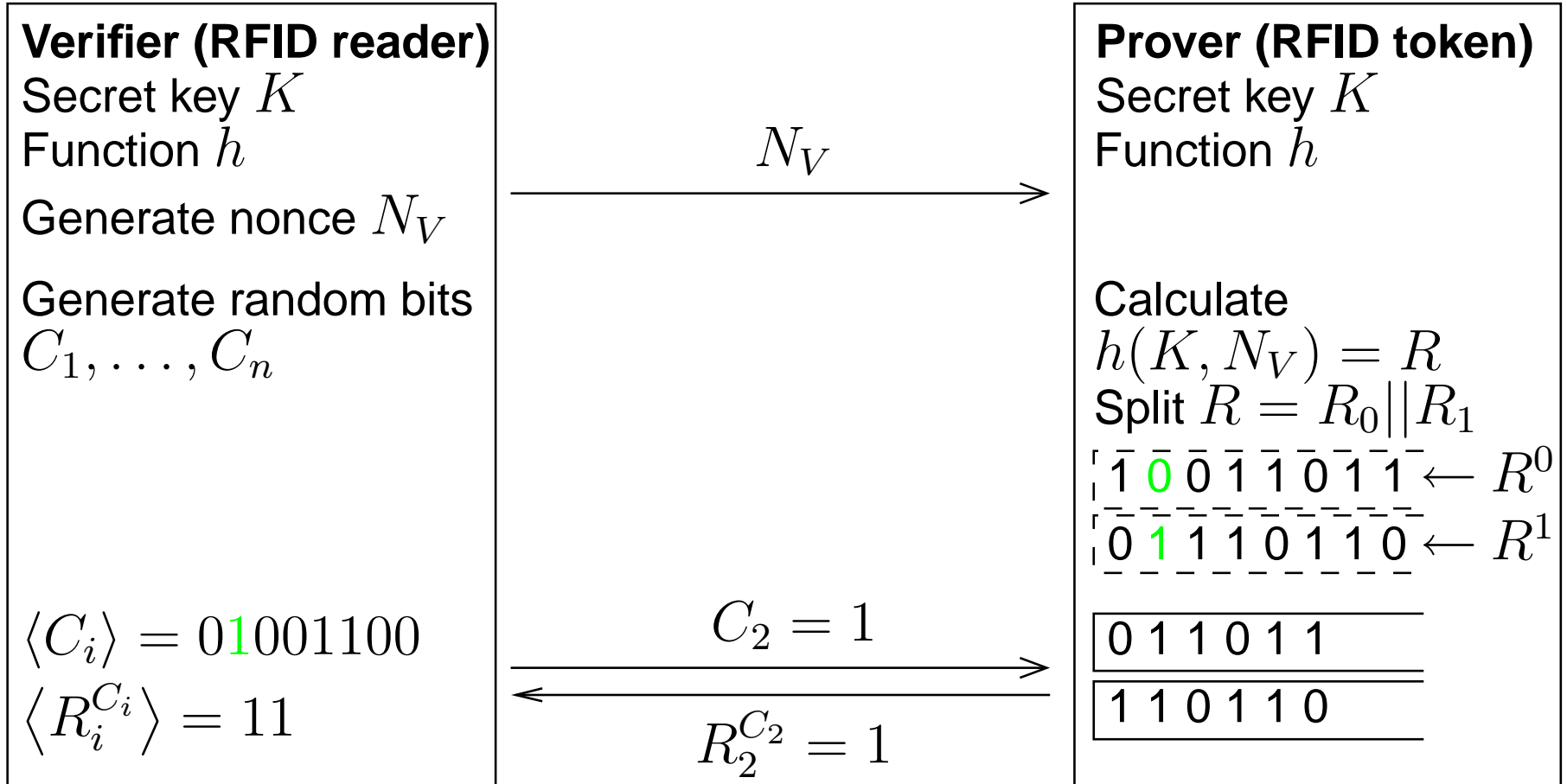
$\overline{10011011} \leftarrow R^0$

$\overline{01110110} \leftarrow R^1$

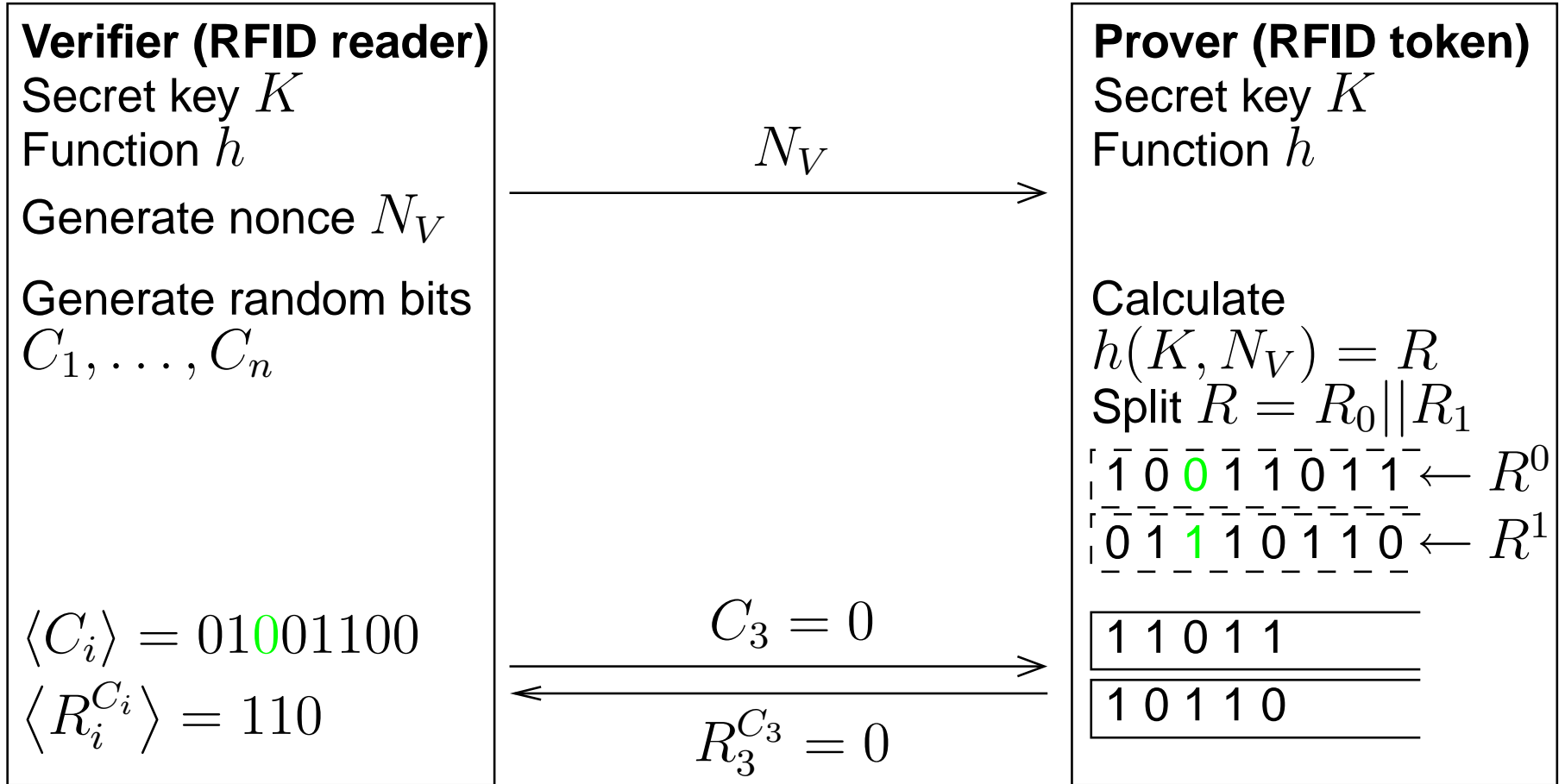
Protocol description



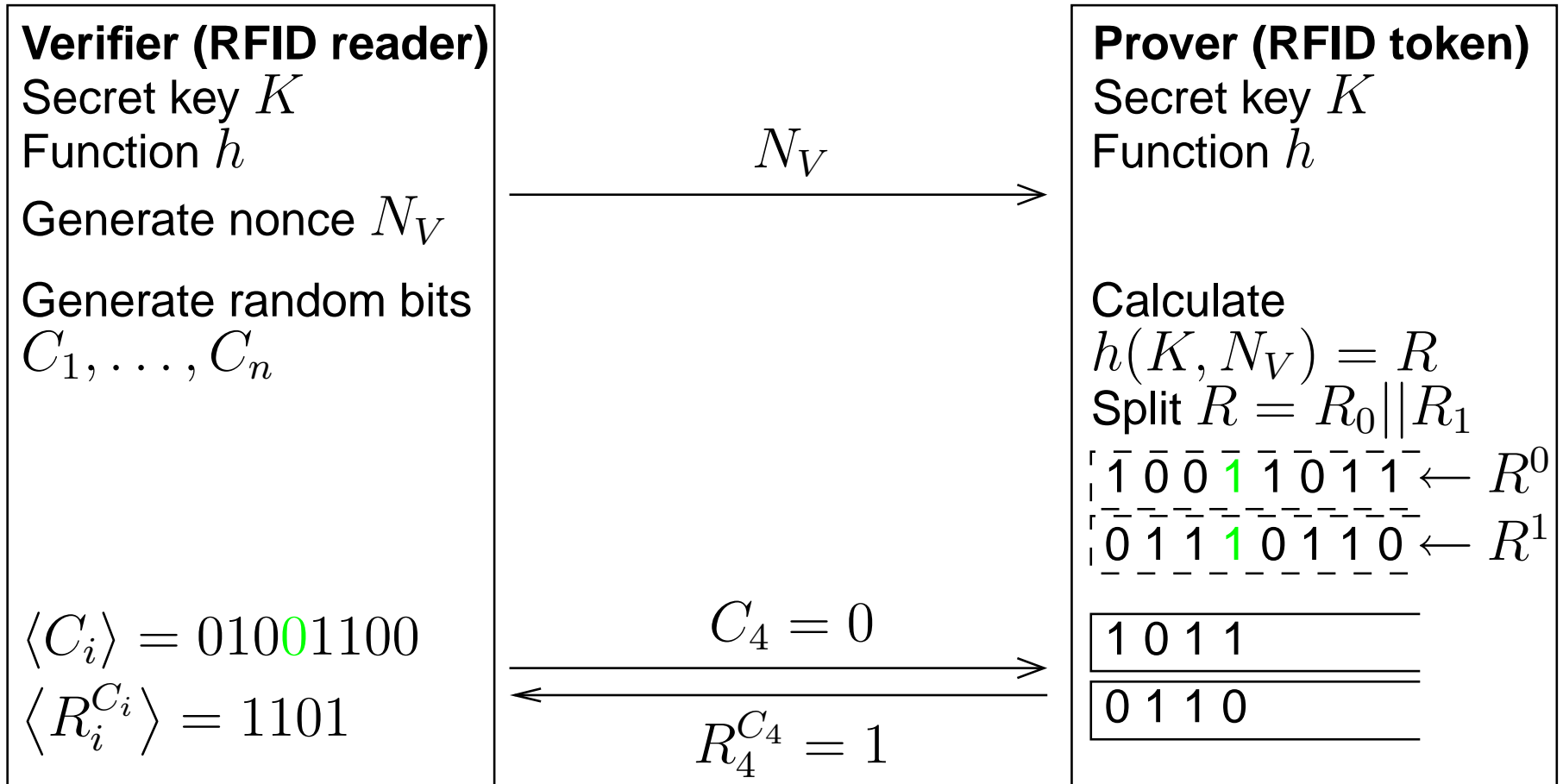
Protocol description



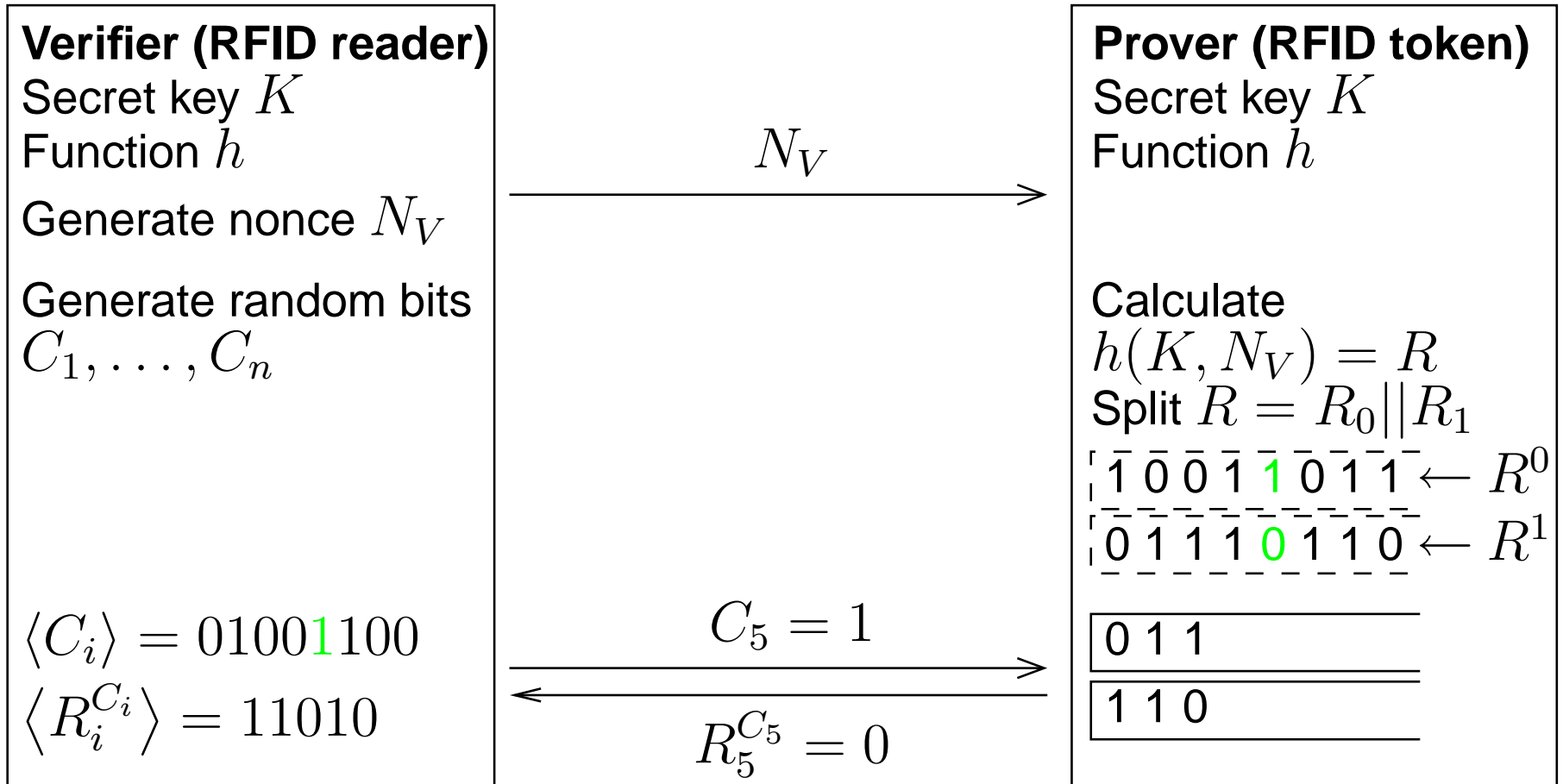
Protocol description



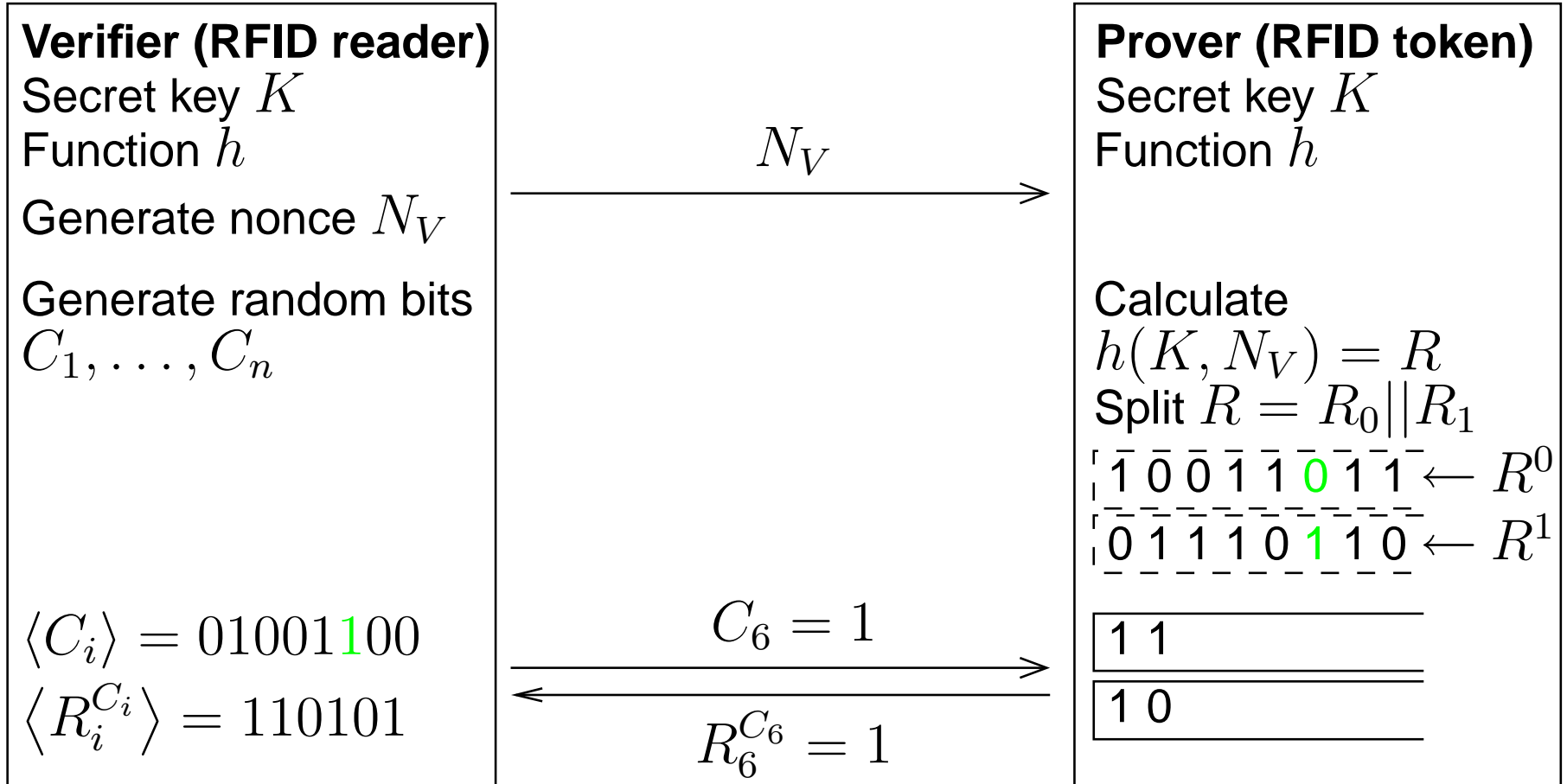
Protocol description



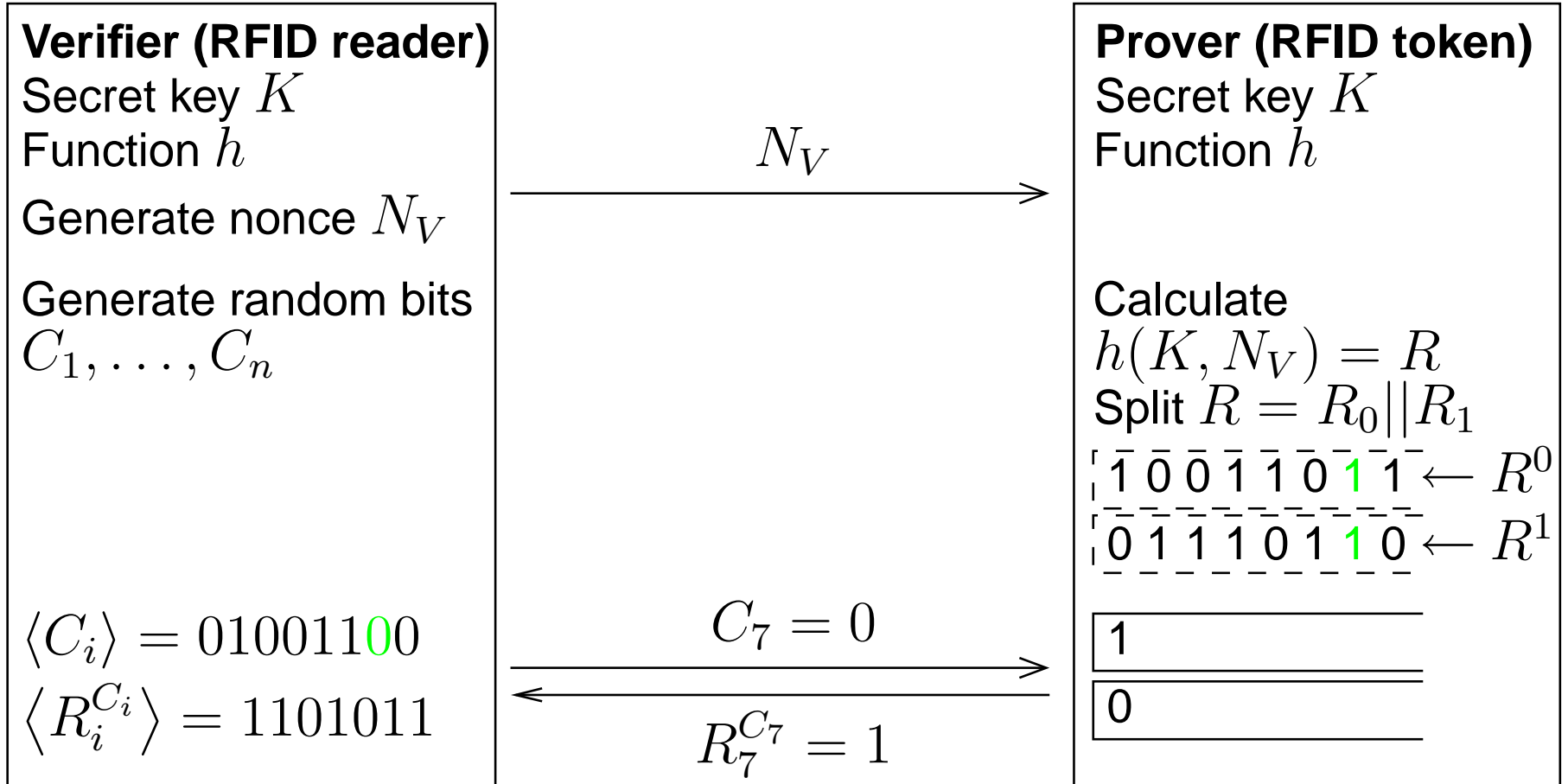
Protocol description



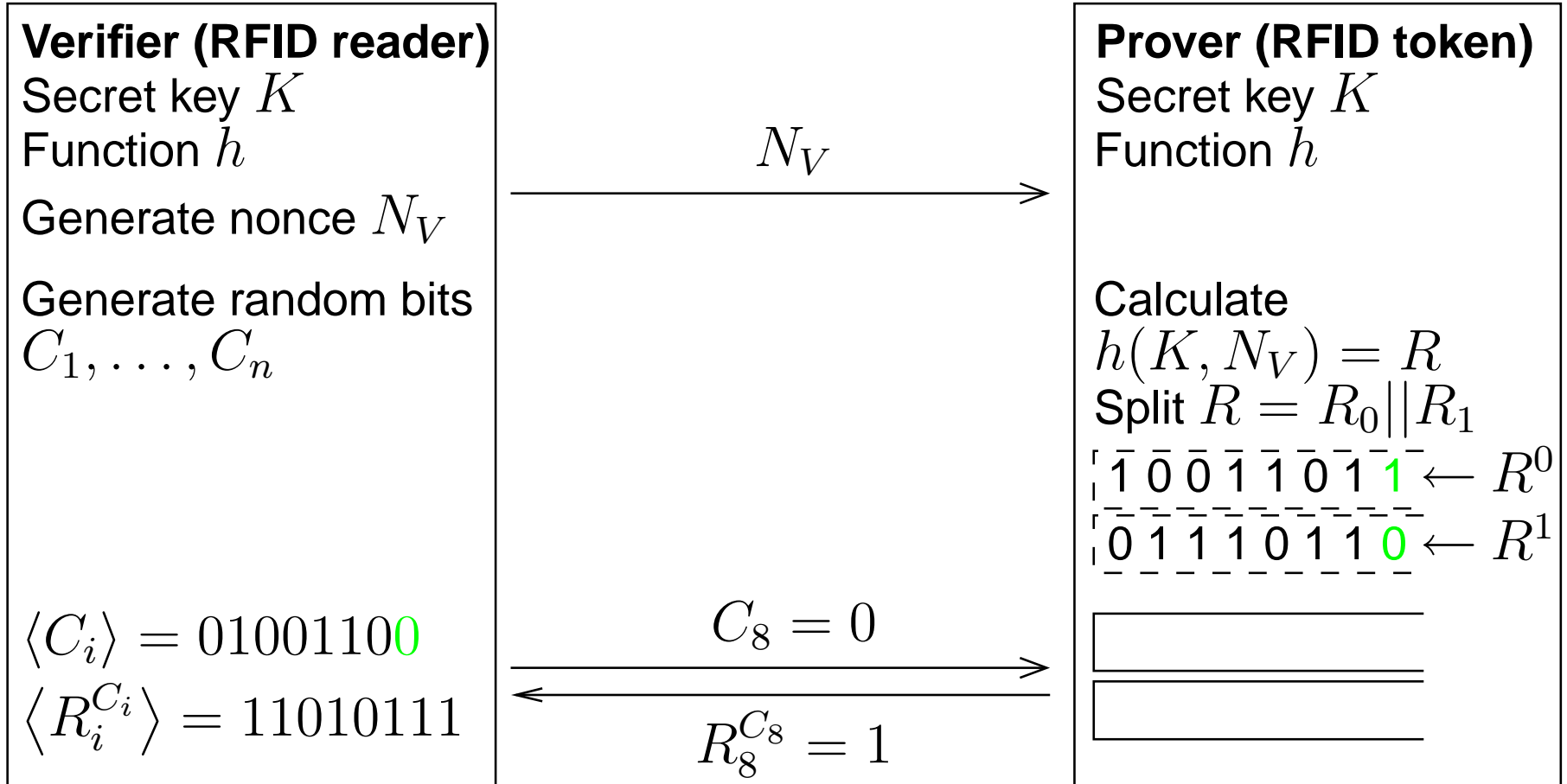
Protocol description



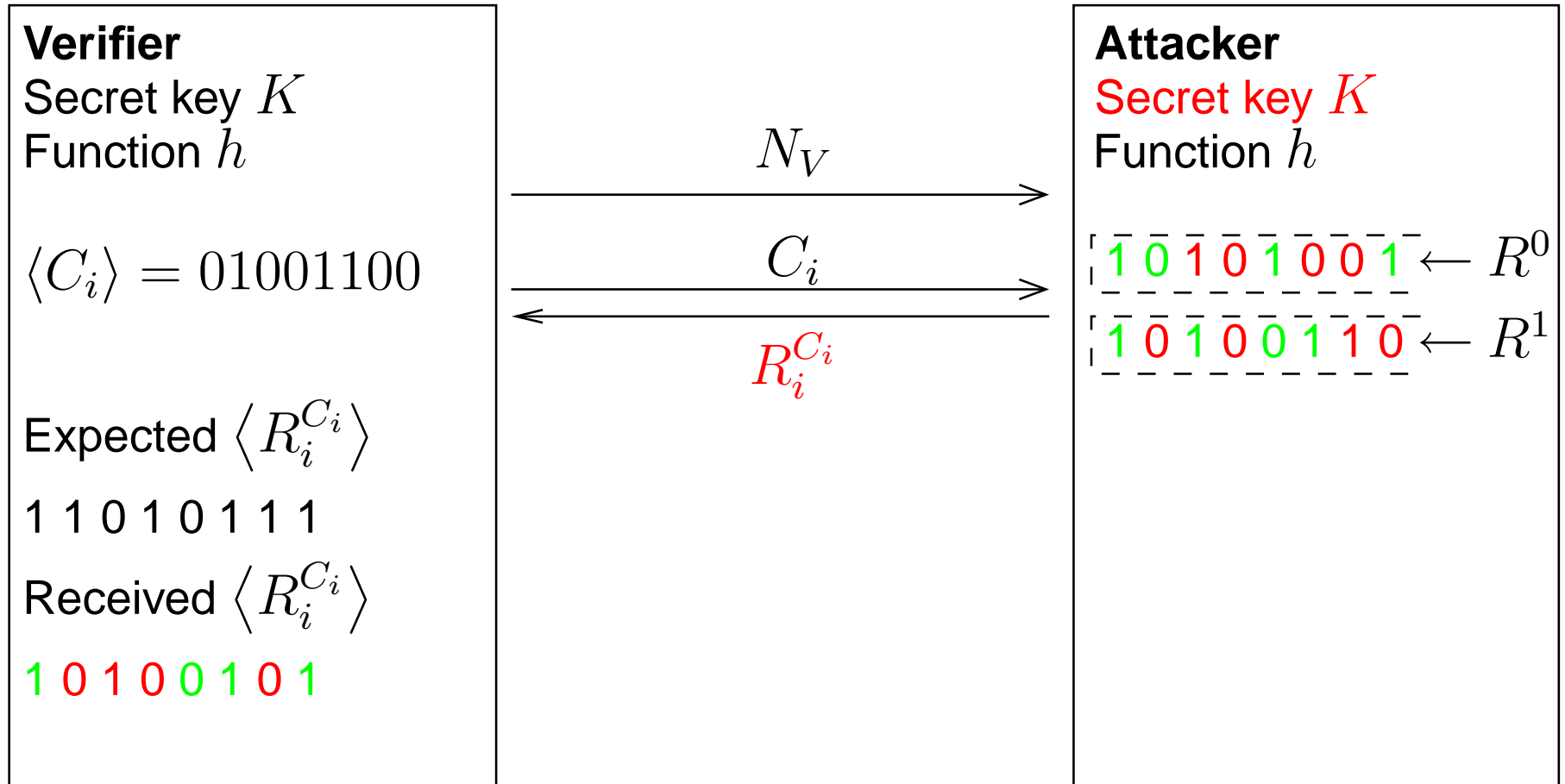
Protocol description



Protocol description



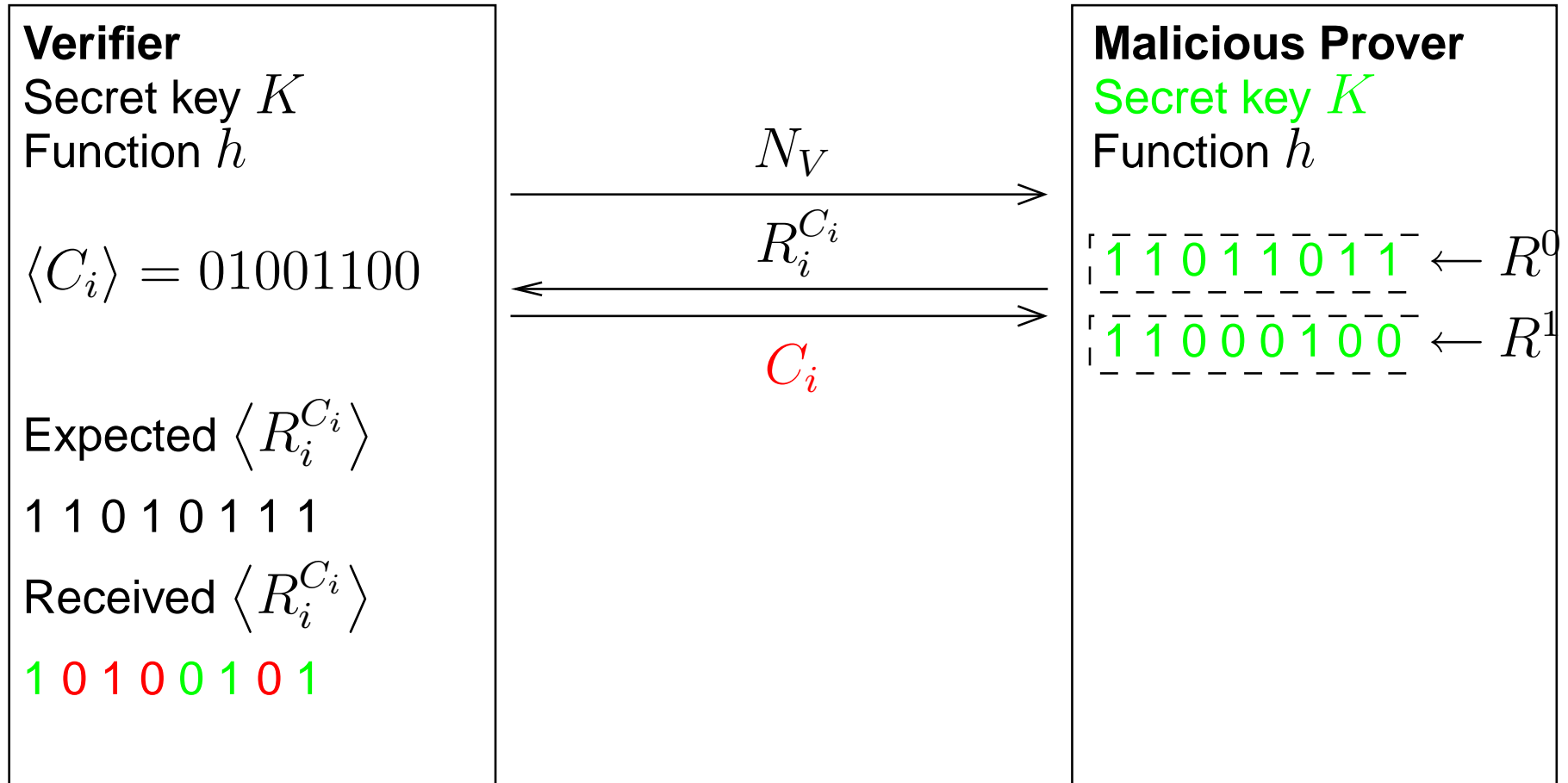
Protocol description



● Verifier \leftrightarrow Attacker

- $\frac{1}{2}$ chance of guessing a response bit correctly

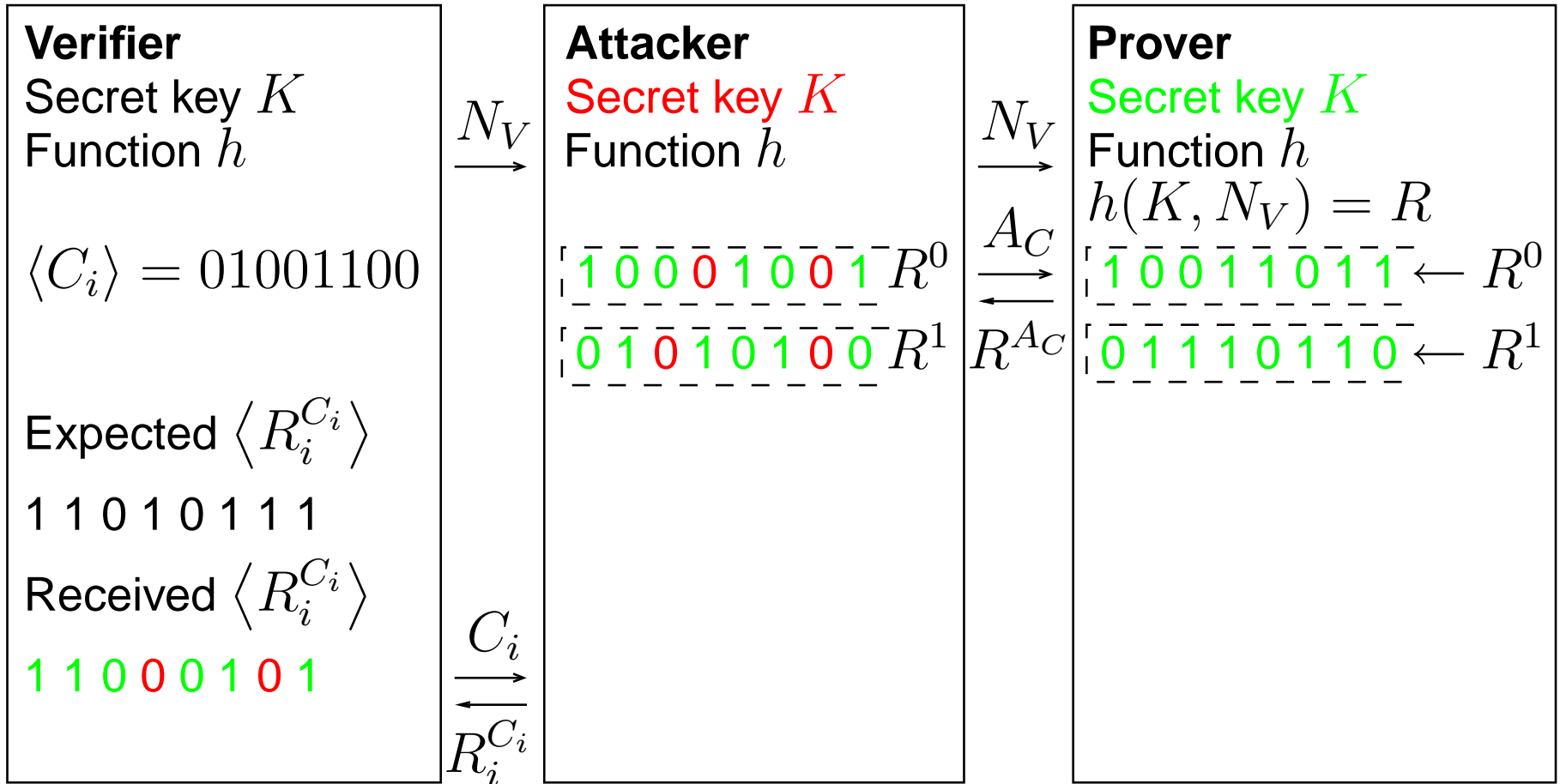
Protocol description



● Verifier \leftrightarrow Malicious Prover

● $\frac{1}{2}$ chance of guessing a response bit correctly

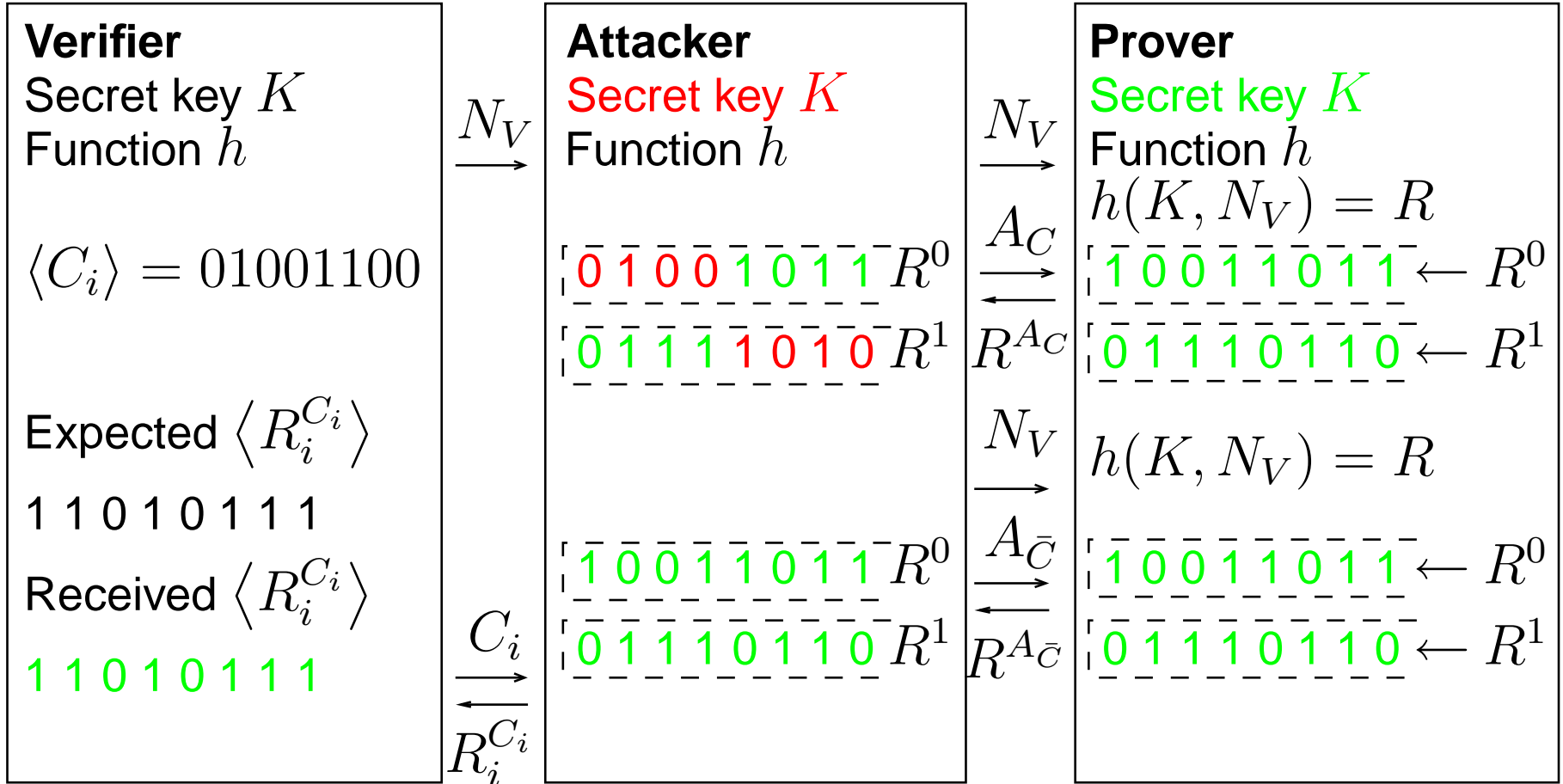
Protocol description



● Verifier \leftrightarrow Attacker \leftrightarrow Prover

● $\frac{3}{4}$ chance of guessing a response bit correctly

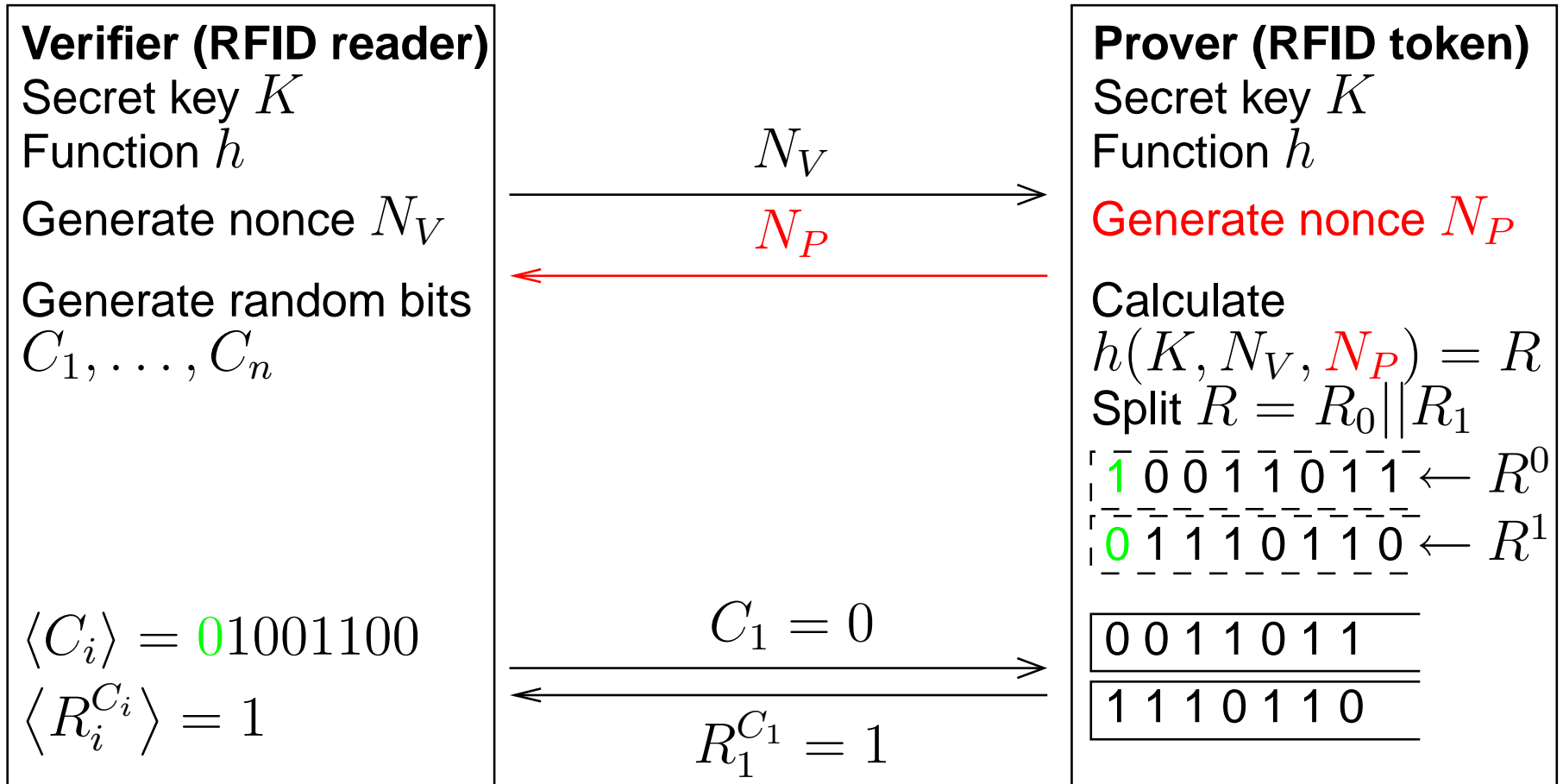
Protocol description



● Overclocking attack

- Prevented in hardware e.g. Bandpass filter

Protocol description



- Overclocking attack
- Alternative to hardware solutions

Noise

Bit errors will probably occur on the rapid exchange channel

- Accept if at least k bits out of n are correct

- False accept:

$$p_{\text{FA}} = \sum_{i=k}^n \binom{n}{i} \cdot \left(\frac{3}{4}\right)^i \cdot \left(\frac{1}{4}\right)^{n-i}$$

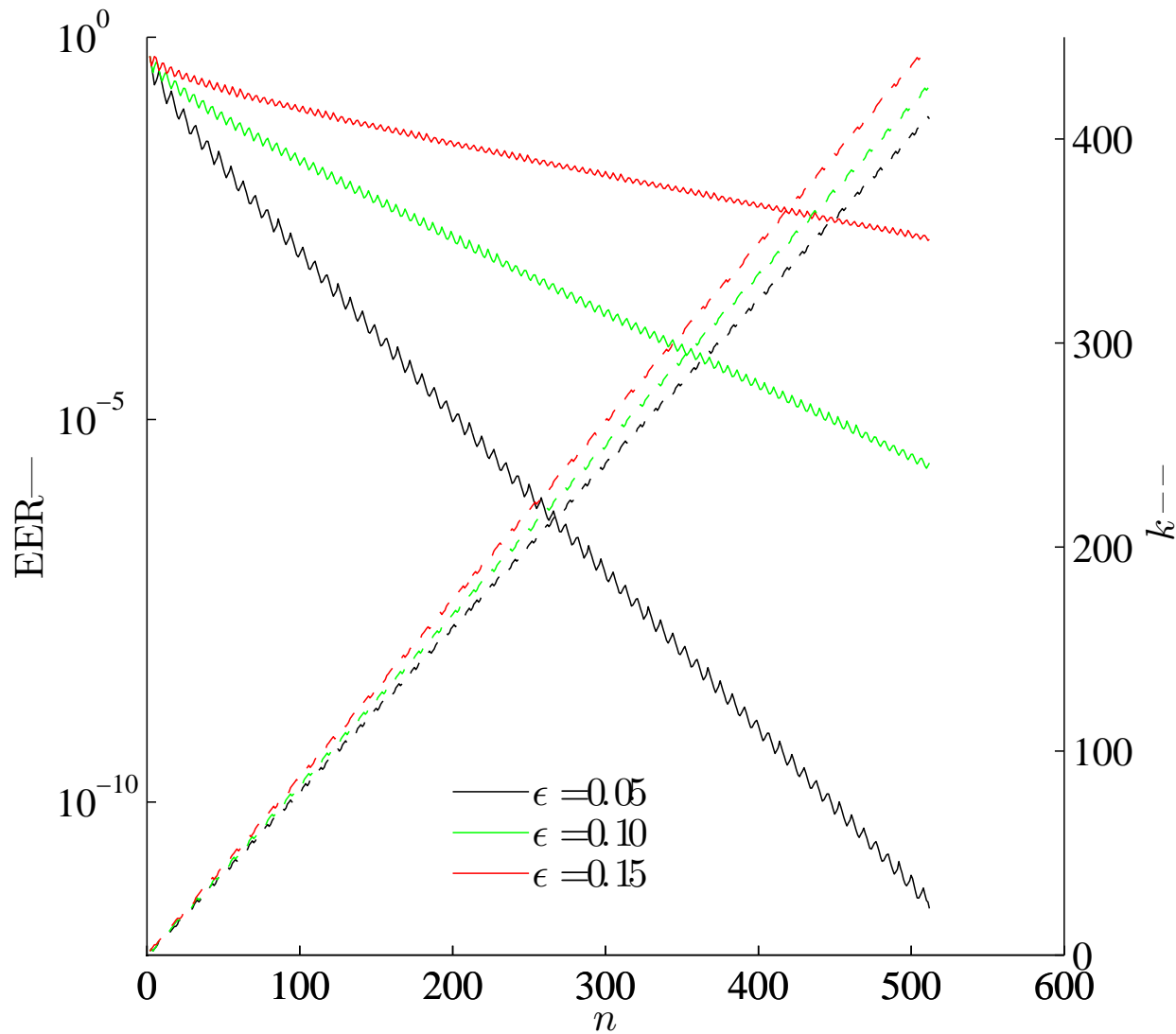
- False reject:

$$p_{\text{FR}} = \sum_{i=0}^{k-1} \binom{n}{i} \cdot (1 - \epsilon)^i \cdot \epsilon^{n-i}$$

where ϵ is the bit-error probability.

Noise (2)

Example of parameter tradeoffs in the presence of noise



Related work

$$t_m = 2 \cdot t_p + t_d$$

$$d = v_p \cdot \frac{t_m - t_d}{2}$$

t_m = round trip time

t_p = one-way propagation time

t_d = processing delay

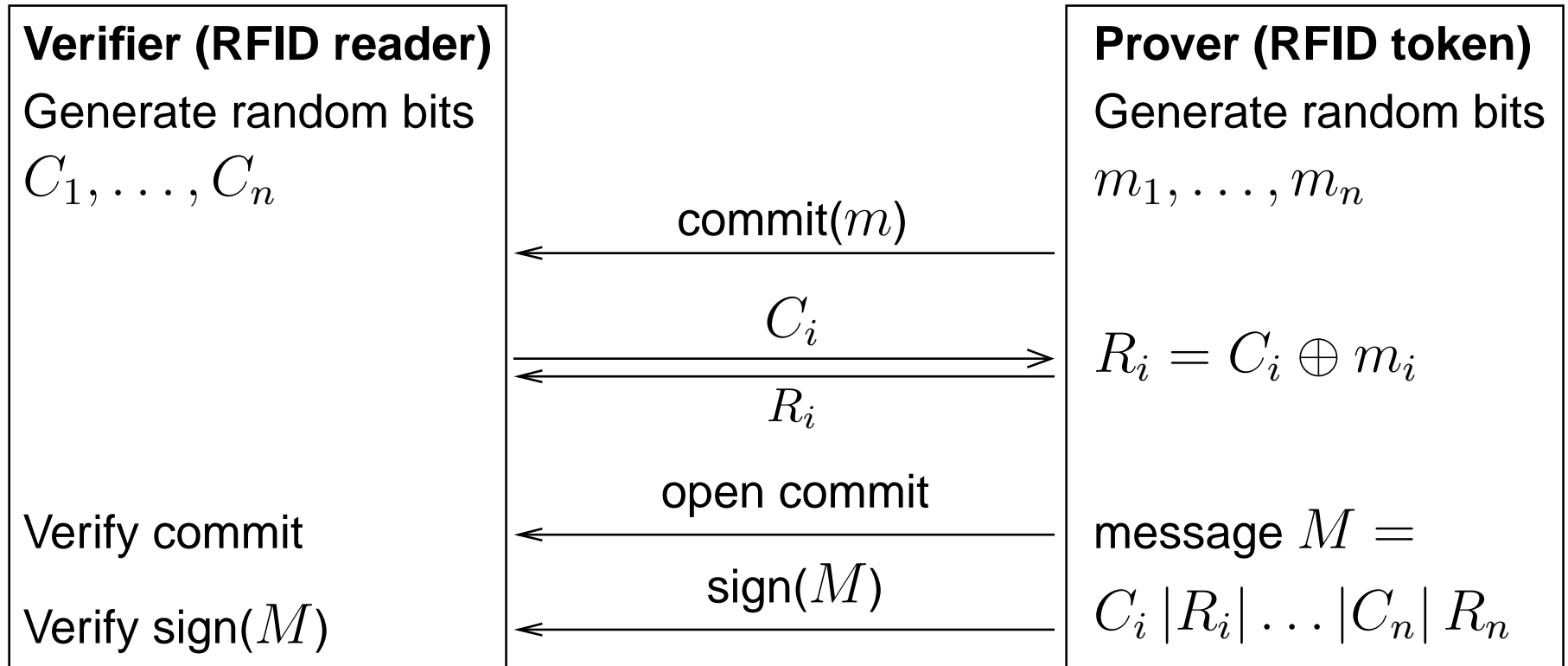
d = distance

v_p = signal propagation speed

Distance Bounding Protocols

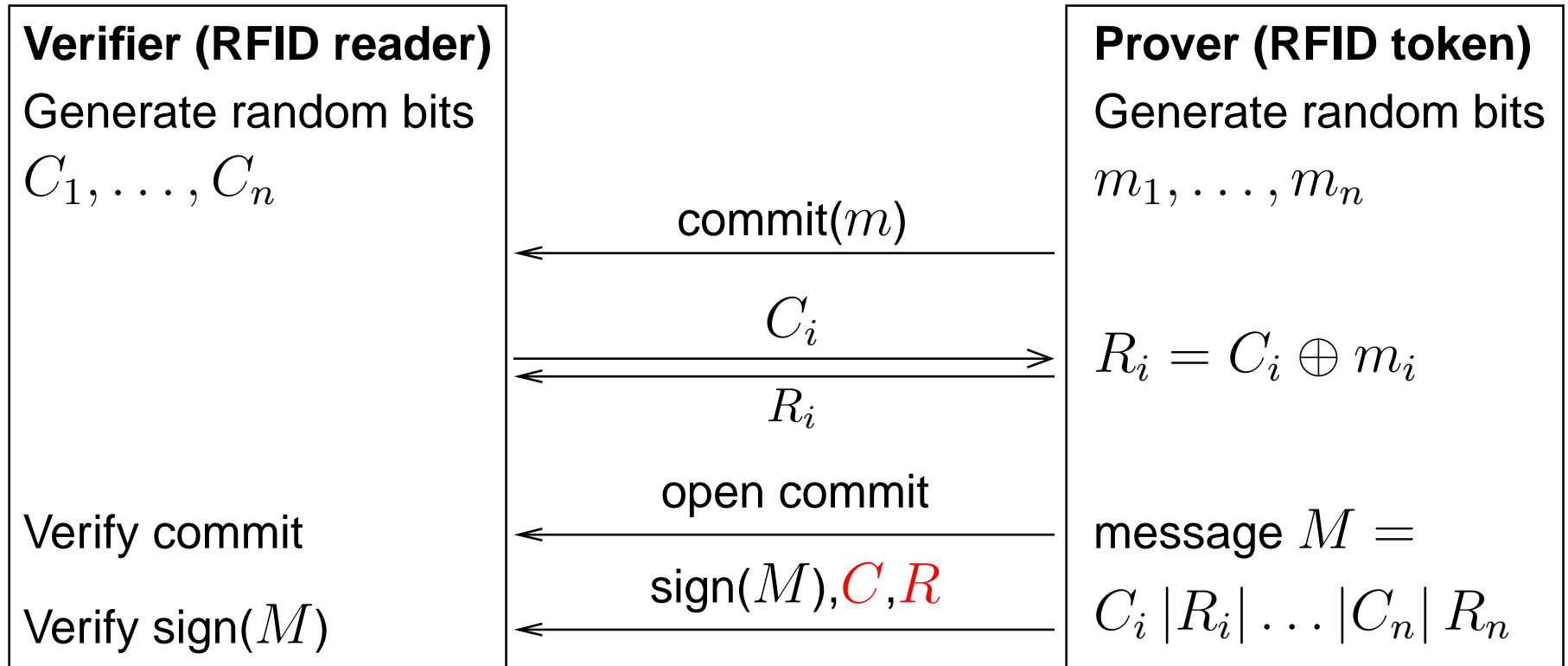
- Beth and Desmedt (1991)
- Brands and Chaum (1993)

Brands and Chaum



- Time round trip of single bit exchange
- Processing with variable delay done beforehand
- Minimal processing delay during bit exchange

Brands and Chaum



- Additional commit and sign operations
- Additional bits on slow channel
 - In presence of noise C and R need to be transmitted

Performance vs Brands and Chaum

For $EER = 10^{-4}$ and $\epsilon = 0.1$

Assume bit exchange rate = $f_{\text{carrier}}/4$

Standard	Time (B and C) $n = 70$	Time (Our protocol) $n = 360$
15693 'fast' 26.4 kbp/s, 13.56 MHz	5.3237 ms	0.1062 ms
15693 'long' 6.62 kbp/s, 13.56 MHz	21.1687 ms	0.1062 ms
14443 A/B 106 kbp/s, 13.56 MHz	1.3414 ms	0.1062 ms

Performance vs Brands and Chaum (2)

For $EER = 10^{-10}$ and $\epsilon = 0.05$

Assume bit exchange rate = $f_{\text{carrier}}/4$

Standard	Time (B and C) $n = 125$	Time (Our protocol) $n = 440$
15693 'fast' 26.4 kbp/s, 13.56 MHz	9.5066 ms	0.1298 ms
15693 'long' 6.62 kbp/s, 13.56 MHz	37.8012 ms	0.1298 ms
14443 A/B 106 kbp/s, 13.56 MHz	2.3954 ms	0.1298 ms

Positioning technology

Positioning Technology used today

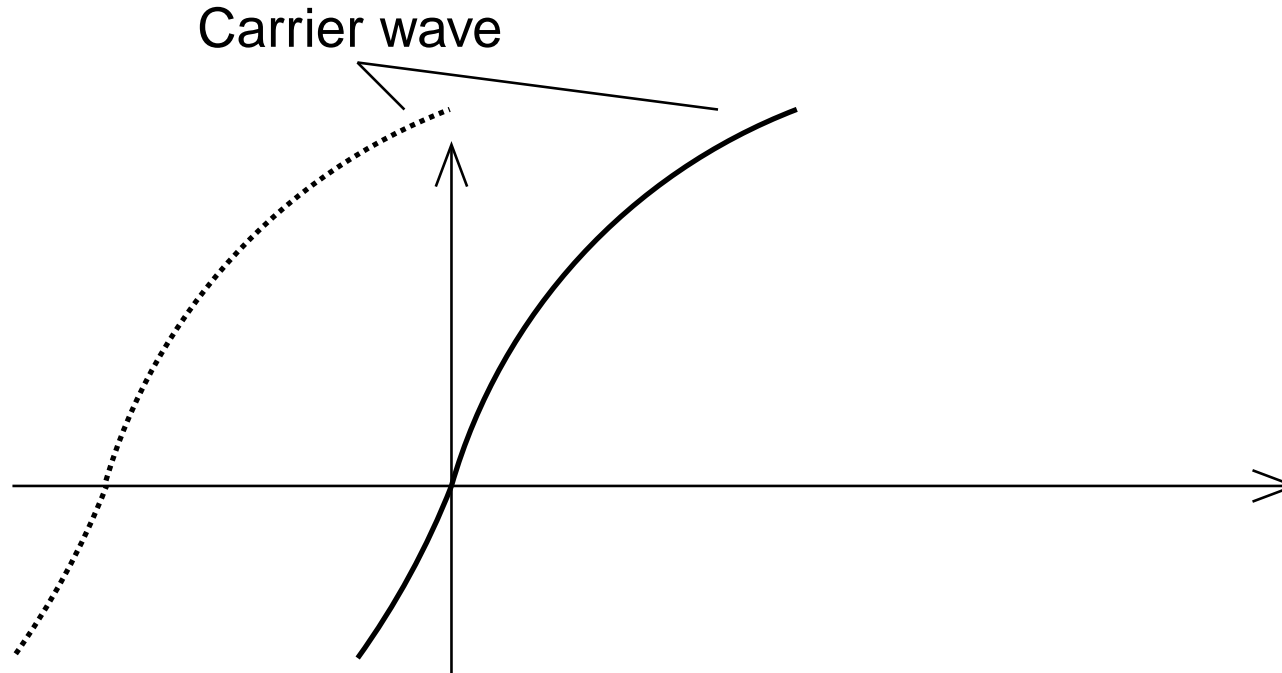
- Radio Frequency
 - Secure but complex
- Ultrasound
 - Appear closer by relaying data with faster RF link
- Received Signal Strength
 - Amplified signal appears closer

Resolution

Estimate $r \approx \frac{c}{B}$, where B is the channel bandwidth

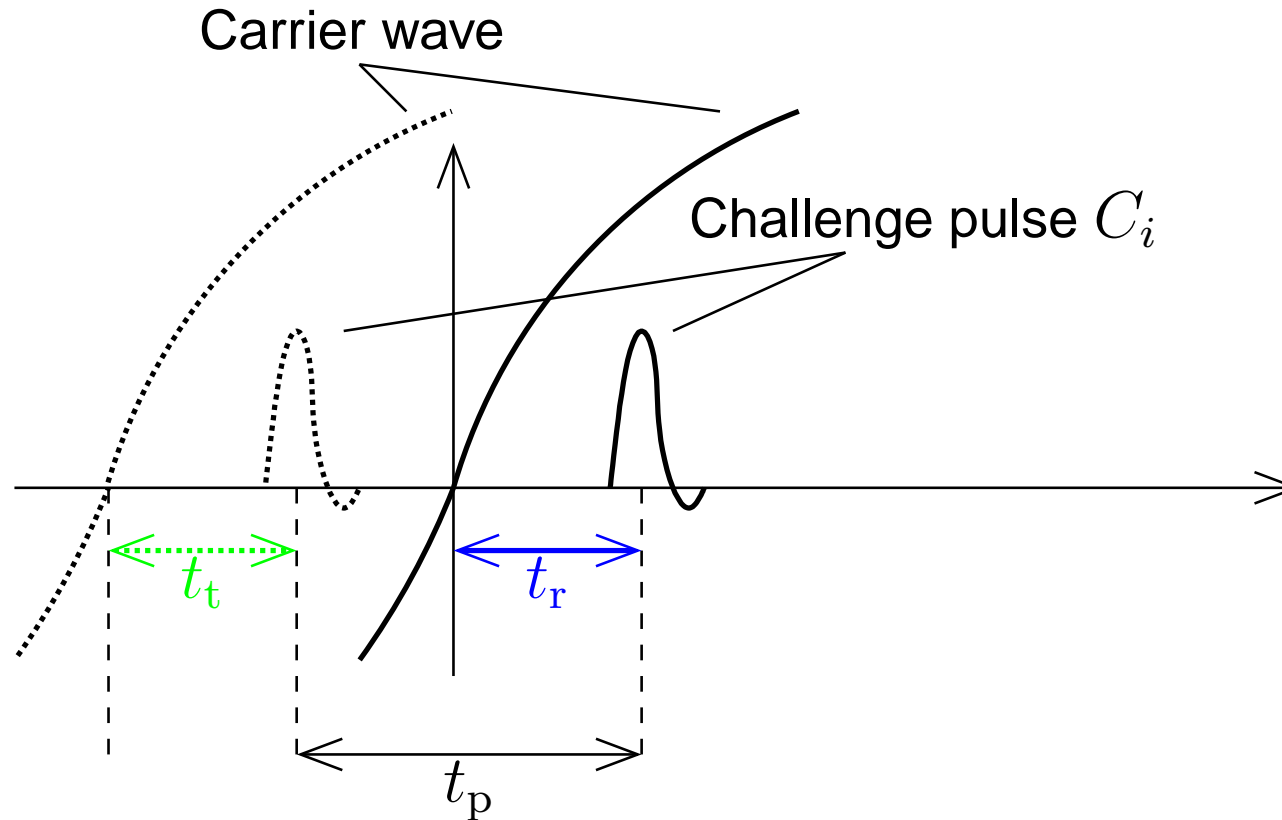
- RFID communication inadequate
 - e.g. for ISO 14443 at 106 kbp/s, $r \approx 3$ km
- Ultra Wideband Pulses
 - Higher bandwidth equals better resolution
- RFID implementation issues
 - Error free operation requires high resources
e.g. synchronization, bit placement
 - Crude implementation possible but would allow bit errors
 - Sufficient for bit exchange channel
 - Not to be used for normal communication

Proposed bit exchange channel



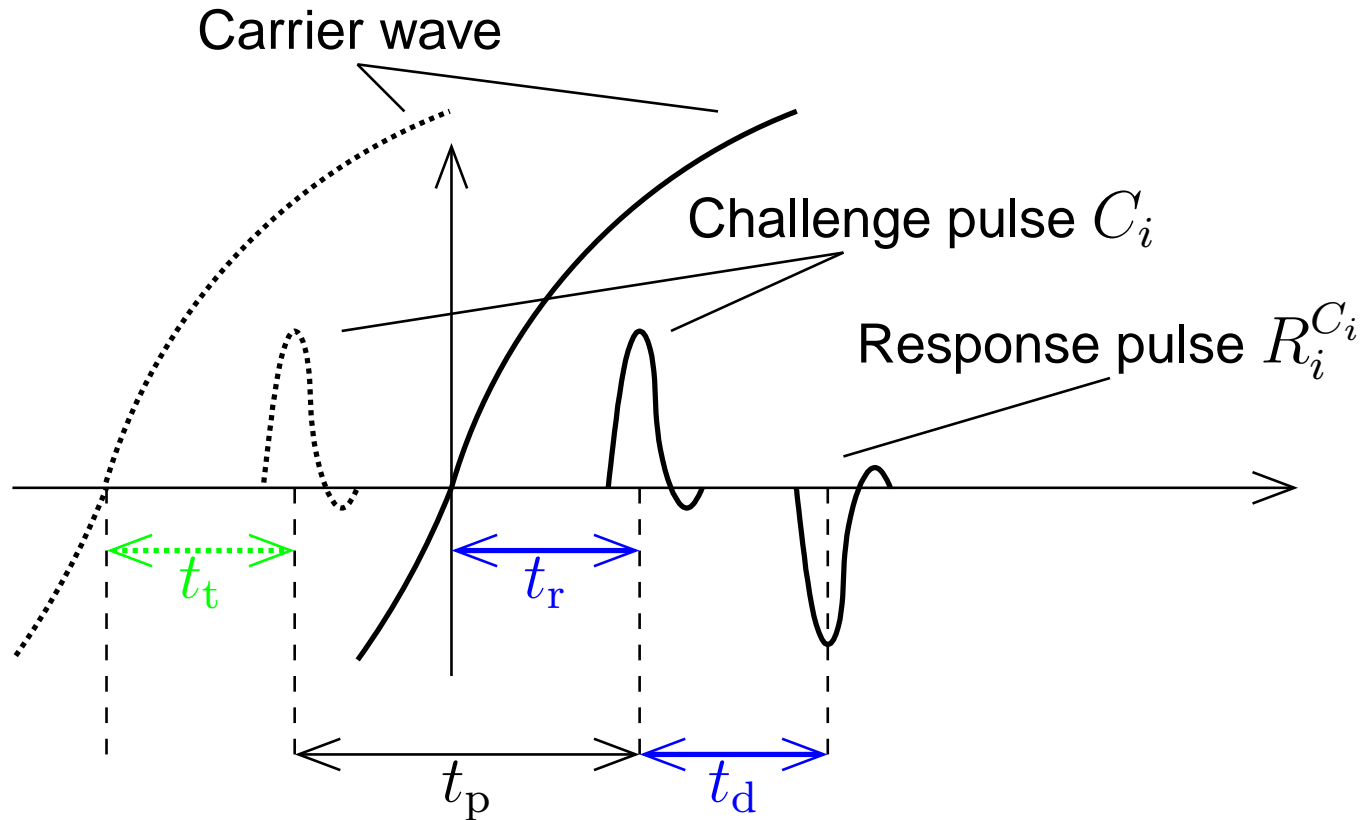
- Use carrier for loose synchronization
e.g. Zero crossing

Proposed bit exchange channel



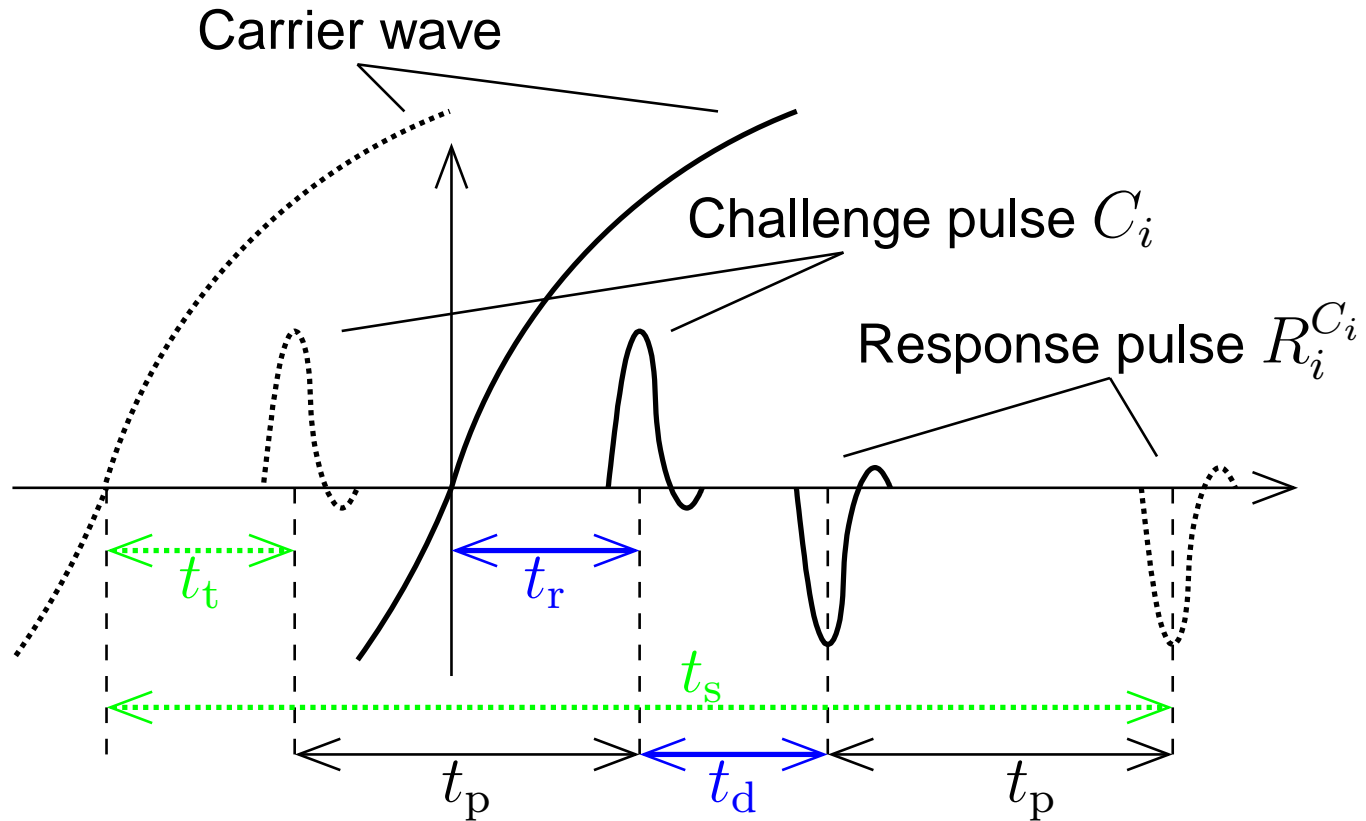
- Reader (Verifier) adjusts t_t to match sampling delay t_r in the token (Prover)

Proposed bit exchange channel



- t_d is a predictable hardware delay

Proposed bit exchange channel



$$d = c \cdot (t_s - t_t - t_d) / 2$$

Conclusion

- Few more bit exchanges to achieve same cryptographic security
 - Chance of attacker guessing correct response $\frac{3}{4}$ vs $\frac{1}{2}$
- Faster operation
 - Extra bits transmitted on faster bit exchange channel
 - Much less data transmitted on slow error corrected channel
- Practical implementation suited for RFID
 - Low power and processing requirements for Prover
 - Timing-sensitive measurements and adjustments done by the Verifier
 - Faster completion of protocol suited for RFID environment

Future work

- Practical implementation
 - Pseudorandom functions suited for RFID device
 - Rapid bit exchange channel
 - UWB antennas for card form factor
- Mutual distance bounding protocol
 - For applications where illegitimate reading attempts are more common e.g e-Passports