

# TCG TSS 2.0 Overview and Common Structures Specification

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Contact: [admin@trustedcomputinggroup.org](mailto:admin@trustedcomputinggroup.org)

- **Work in Progress:**

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## TCG Public Review

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## Revision History

Revision	Date	Description
Version 0.9 Rev 00		Draft for Public Review Complete
Version 0.9 Rev 02		Changed prefixes for MUAPI.

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# 1 Definitions and References

## 1.1 Acronyms

Term or Acronym	Definition
<b>Application Binary Interface (ABI)</b>	The ABI is the byte-wise layout of data types and function parameters in RAM as well as symbol definitions used to communicate between applications, shared objects and the kernel.
<b>Application Programming Interface (API)</b>	The API is the software interface defined by the functions and structures in a high-level programming language used to communicate between layers in the software stack.
<b>Caller</b>	The caller is the software that invokes a function call or that sends a TCTI command to the TAB/RM.
<b>Connection</b>	A “connection” to the TAB/RM corresponds to a TCTI context southbound from the SAPI to the TAB/RM.
<b>ESAPI</b>	TSS 2.0 Enhanced System API. This layer is intended to sit on top of the System API providing enhanced context management and cryptography.
<b>FAPI</b>	TSS 2.0 Feature API. This layer sits above the ESAPI and provides a high-level interface including a policy definition language and key store.
<b>Implementation</b>	An implementation is the source code and binary code that embodies a specification or parts of a specification.
<b>Marshal</b>	To marshal data is to convert data from C structures to marshaled data.
<b>Marshaled Data</b>	Marshaled data is the representation of data used to communicate with the TPM. In order to optimize data communication to and from the TPM, the smallest amount of data possible is sent to the TPM. For instance, if a structure starts with a size field and that field is set to 0, none of the other fields in the structure are sent to the TPM. Another example: if an input structure consists of a union of data structures, it’s marshalled representation will be the size of just the data structure selected from the union (actually the marshalled version of that structure itself). Also, the marshalled data must be in big-endian format since this is what the TPM expects.
<b>NV</b>	Non-volatile means that data is not lost when the system is powered down.
<b>PCR</b>	Platform Configuration Register (see TPM 2.0 Library Specification)
<b>RM</b>	The “Resource Manager” is software executing on a system with a TPM that ensures that the resources necessary to execute TPM commands are present in the TPM.
<b>SAPI</b>	TSS 2.0 System API. This layer is intended to sit on top of the TCTI providing marshaling/unmarshalling for TPM commands and responses.
<b>TAB</b>	The TPM Access Broker is software executing on a system with a TPM managing concurrent access from multiple applications.
<b>TPM Command Transmission Interface (TCTI)</b>	The TCTI is an IPC abstraction layer used to send commands to and receive responses from the TPM or the TAB/RM.

<b>TPM</b>	Trusted Platform Module
<b>TPM Resource</b>	Data managed by a TPM that can be referenced by a TPM handle. For TPM 2.0, this includes TPM objects (keys and data objects), TPM NV indices, TPM PCRs and TPM reserved handles and hierarchies.
<b>TSS</b>	TPM Software Stack
<b>Unmarshal</b>	To unmarshal data is to convert data from marshalled format to C structures.

## 1.2 TCG Software Stack 2.0 (TSS 2.0) Specification Library Structure

At the time of writing, the documents that are part of the specification of the TSS 2.0 are:

- [1] TCG TSS 2.0 Overview and Common Structures Specification
- [2] TCG TSS 2.0 TPM Command Transmission Interface (TCTI) API Specification
- [3] TCG TSS 2.0 Marshaling/Unmarshaling API Specification
- [4] TCG TSS 2.0 System API (SAPI) Specification
- [5] TCG TSS 2.0 Enhanced System API (ESAPI) Specification
- [6] TCG TSS 2.0 Feature API (FAPI) Specification
- [7] TCG TSS 2.0 TAB and Resource Manager Specification

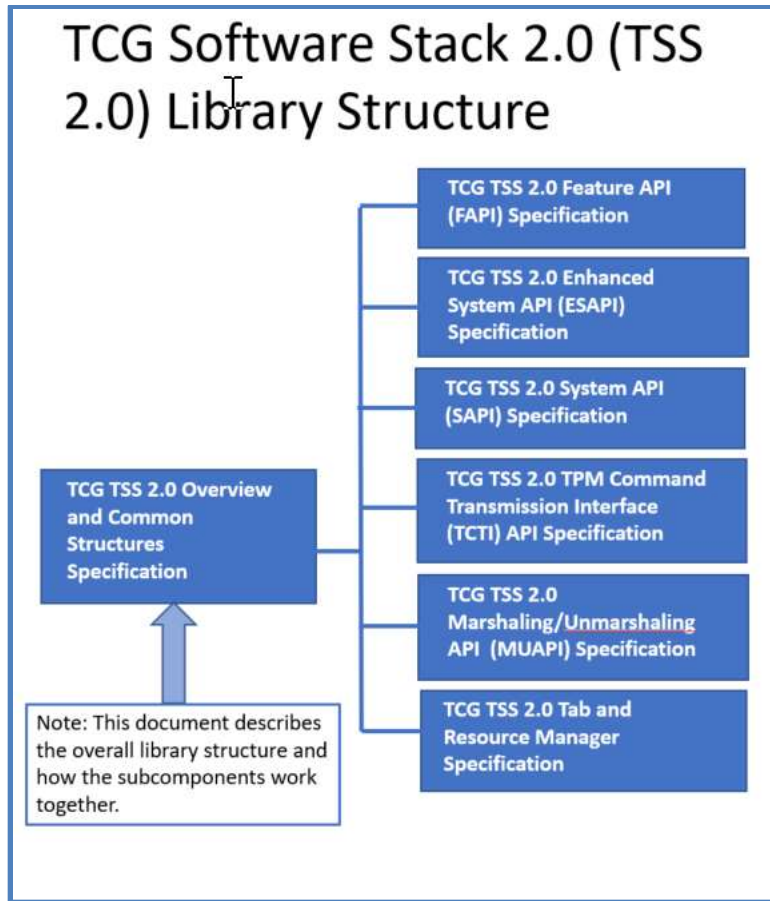


Figure 1: TSS 2.0 Specification Library

## 1.3 References

Documents change over time. The following rules apply to determining the edition of a reference needed for TSS 2.0. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

The following referenced documents are necessary or very useful for understanding the TPM 2.0 specification.

- [1] The Trusted Platform Module Library Specification, Family “2.0”  
**NOTE:** More information, the specification, and other documents can be found at <https://trustedcomputinggroup.org/tpm-library-specification/> and <http://www.trustedcomputinggroup.org/work-groups/trusted-platform-module/>.

- [i] [Part 1: Architecture](#)
- [ii] [Part 2: Structures](#)
- [iii] [Part 3: Commands](#)
- [iv] [Part 3: Commands – Code](#)
- [v] [Part 4: Supporting Routines](#)
- [vi] [Part 4: Supporting Routines – Code](#)

- [2] Errata for the Trusted Platform Module Library Specification, Family “2.0”

- [i] [Errata Version 1.1 for Trusted Platform Module Library Specification, Family “2.0”, Revision 01.38](#)
- [ii] [Errata Version 1.0 for Trusted Platform Module Library Specification, Family “2.0”, Revision 01.38](#)
  
- [3] IETF RFC 3447, Public-Key Cryptography Standards (PKCS) #1: RSA Cryptography Specifications Version 2.1
- [4] [NIST SP800-56A](#), *Recommendation for Pair-Wise Key Establishment Schemes Using Discrete Logarithm Cryptography (Revised)*
- [5] [NIST SP800-108](#), *Recommendation for Key Derivation Using Pseudorandom Functions (revised)*
- [6] [FIPS PUB 186-3](#), *Digital Signature Standard (DSS)*
- [7] ISO/IEC 9797-2, Information technology -- Security techniques -- Message Authentication Codes (MACs) -- Part 2: Mechanisms using a dedicated hash-function
- [8] IEEE Std 1363™-2000, *Standard Specifications for Public Key Cryptography*
- [9] IEEE Std 1363a™-2004 (Amendment to IEEE Std 1363™-2000), *IEEE Standard Specifications for Public Key Cryptography- Amendment 1: Additional Techniques*
- [10] ISO/IEC 10116:2006, *Information technology — Security techniques — Modes of operation for an n-bit block cipher*
- [11] GM/T 0003.1-2012: *Public Key Cryptographic Algorithm SM2 Based on Elliptic Curves Part 1: General*
- [12] GM/T 0003.2-2012: *Public Key Cryptographic Algorithm SM2 Based on Elliptic Curves Part 2: Digital Signature Algorithm*
- [13] GM/T 0003.3-2012: *Public Key Cryptographic Algorithm SM2 Based on Elliptic Curves Part 3: Key Exchange Protocol*
- [14] GM/T 0003.5-2012: *Public Key Cryptographic Algorithm SM2 Based on Elliptic Curves Part 5: Parameter definition*
- [15] GM/T 0004-2012: *SM3 Cryptographic Hash Algorithm*
- [16] GM/T 0002-2012: *SM4 Block Cipher Algorithm*
- [17] ISO/IEC 10118-3, Information technology — Security techniques — Hash-functions — Part 3: Dedicated hash functions
- [18] ISO/IEC 14888-3, Information technology -- Security techniques -- Digital signature with appendix -- Part 3: Discrete logarithm based mechanisms
- [19] ISO/IEC 15946-1, Information technology — Security techniques — Cryptographic techniques based on elliptic curves — Part 1: General
- [20] ISO/IEC 18033-3, Information technology — Security techniques — Encryption algorithms — Part 3: Block ciphers
- [21] TCG Algorithm Registry, Most recent version at the time of publication of this document → [https://trustedcomputinggroup.org/wp-content/uploads/TCG\\_Algorithm\\_Registry\\_Rev\\_1.24.pdf](https://trustedcomputinggroup.org/wp-content/uploads/TCG_Algorithm_Registry_Rev_1.24.pdf)  
All versions are available on <https://trustedcomputinggroup.org/tcg-algorithm-registry/>

## 1.4 TPM Specification Level Addressed by This Specification Version and Revision



This specification defines TSS 2.0 in alignment with Trusted Platform Module Library Specification, Family “2.0”, Level 00, Revision 01.38 – September 2016.

Future changes in the Trusted Platform Module Library Specification may require updates to this document and other documents in the TSS 2.0 library.

## 2 TSS Overview

The TSS is a software stack designed to isolate TPM application programmers from the low level details of interfacing to the TPM. The TSS consists of multiple layers allowing scalable TSS implementations to be tailored for both high end systems and resource constrained low end systems.

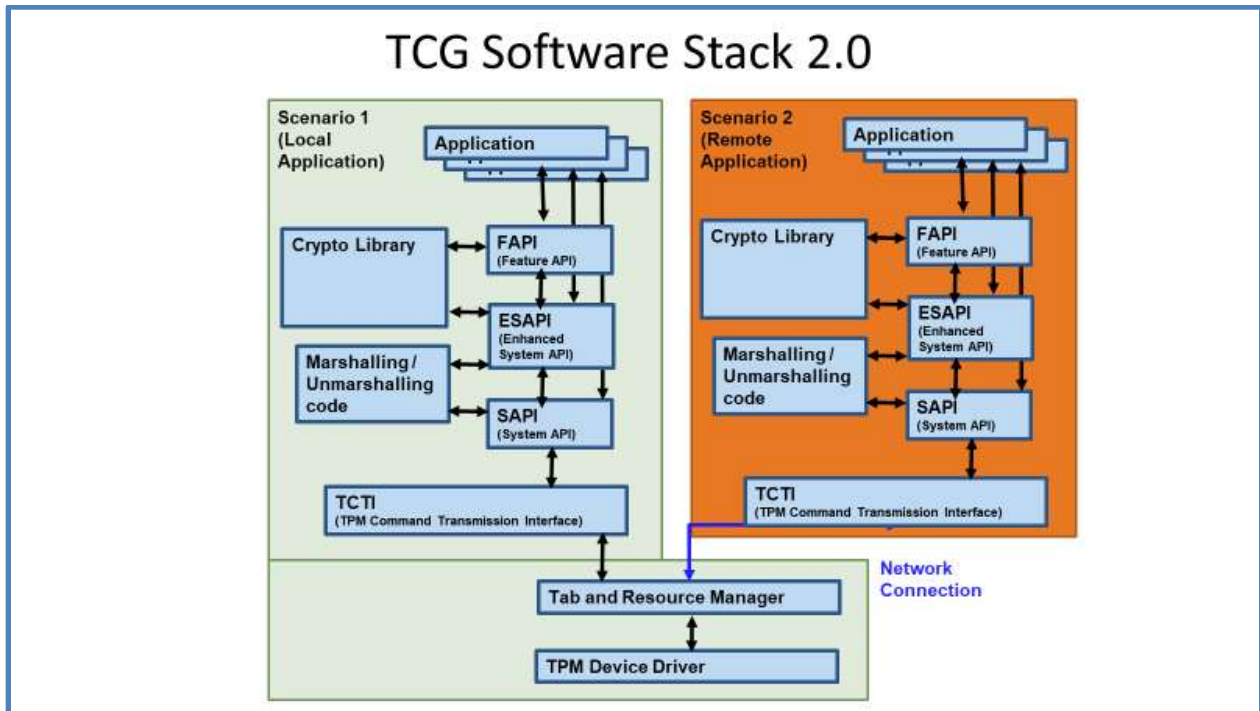


Figure 2: TCG Software Stack 2.0 (TSS 2.0)

The resource requirements for the various components are given in the following figure:

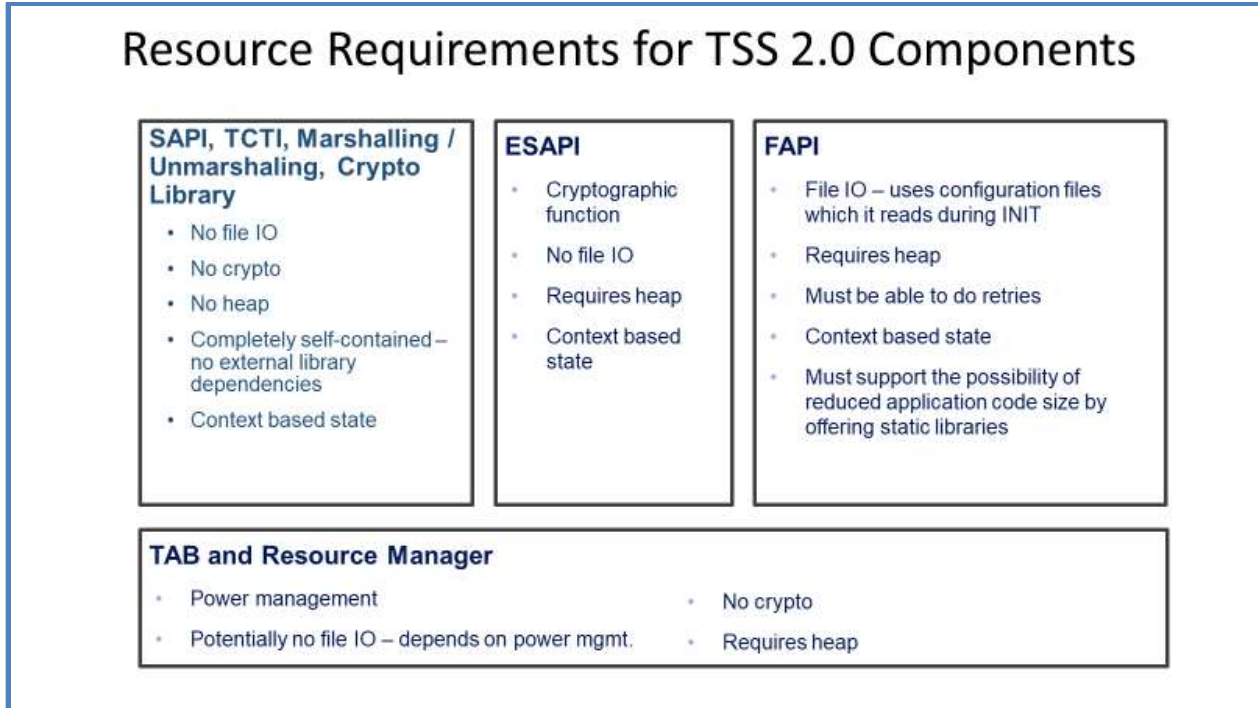


Figure 3: Resource Requirements for TSS 2.0 Components

## 2.1 TPM Device Driver

The TPM device driver is the OS-specific driver that handles all the handshaking with the TPM and reading and writing of data to the TPM.

## 2.2 TAB and Resource Manager

The resource manager manages the TPM context in a manner similar to a virtual memory manager. It swaps objects, sessions, and sequences in and out of the limited TPM onboard memory as needed. This layer is transparent to the upper layers of the TSS and is not mandatory. However, if not implemented, the upper layers will be responsible for TPM context management.

The TPM access broker (TAB) handles multi-process synchronization to the TPM. A process accessing the TPM can be guaranteed that it will be able to complete a TPM command without interference from other competing processes.

## 2.3 TPM Command Transmission Interface (TCTI)

The TPM command transmission interface (TCTI) handles all the communication to and from the lower layers of the stack. For instance, different interfaces are required for local HW TPMs, firmware TPMs, virtual TPMs, remote TPMs, and the TPM simulator. Also, there are two different interfaces to TPMs: the legacy TIS interface and the command/response buffer (CRB).

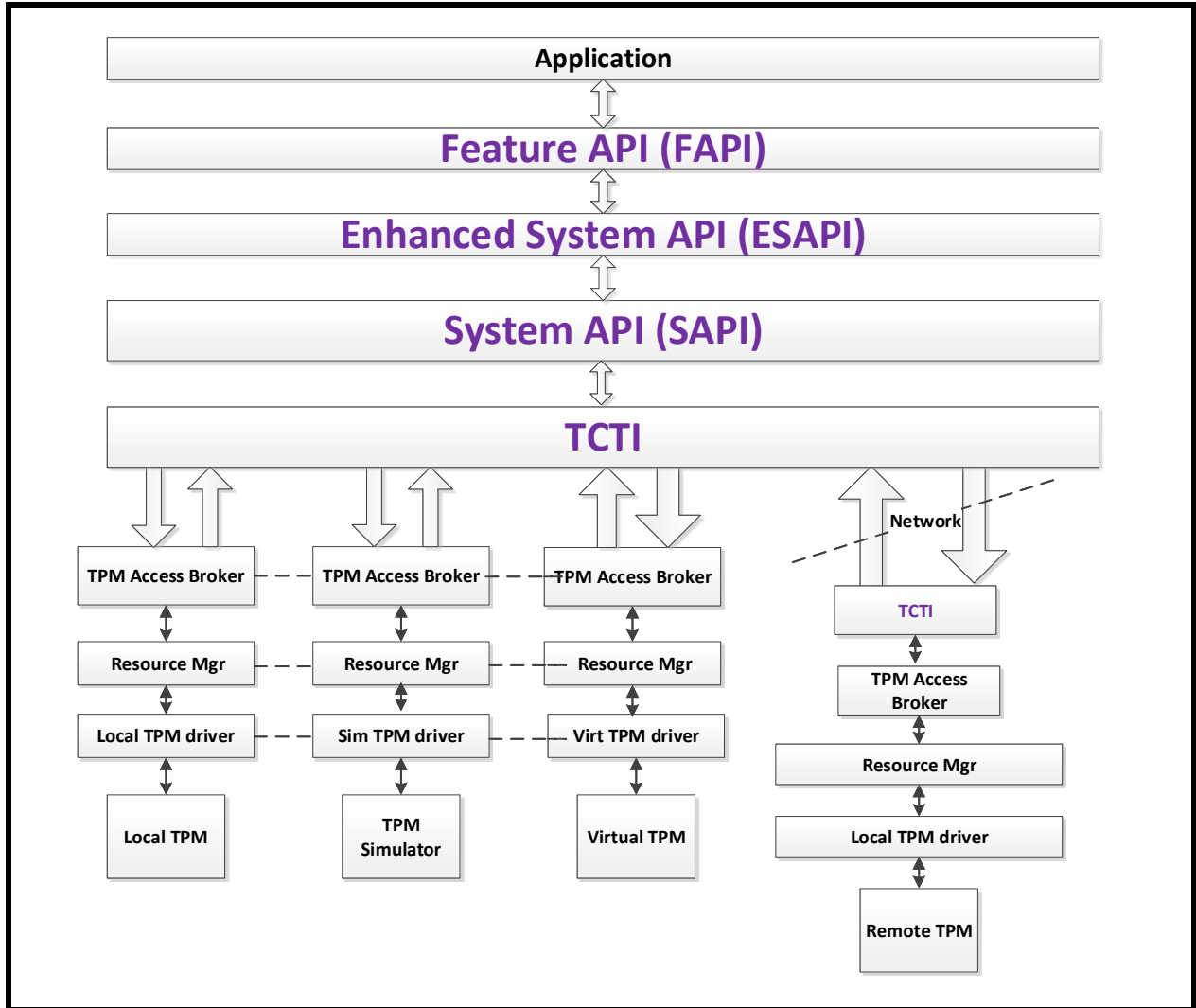


Figure 4: TCTI Allows Connection to Various Target TPMs

## 2.4 Marshaling/Unmarshaling (MUAPI)

The MUAPI builds TPM command byte streams (marshalling) and decomposes TPM response byte streams (unmarshalling). It is required by both the SAPI and ESAPI and is therefore kept in its own namespace with its own API.

## 2.5 System API (SAPI)

The System API is a layer of the overall TSS architecture that provides access to all the functionality of a TPM 2.0 implementation. It is designed to be used wherever low level calls to the TPM functions are made: firmware, BIOS, applications, OS, etc. The System API, as a low level interface, is targeted towards expert applications.

## 2.6 Enhanced System API (ESAPI)

The Enhanced System API (ESAPI) is an interface that is intended to sit directly above the System API. The primary purpose of the ESAPI is to reduce the complexity required of applications that desire to send

individual “system level” TPM calls to the TPM, but that also require cryptographic operations on the data being passed to and from the TPM. In particular, applications that wish to utilize secure sessions to perform Hash-based Message Authentication Code (HMAC) operations, parameter encryption, parameter decryption, TPM command audit and TPM policy operations could benefit from using the ESAPI. Additionally, context and object management are provided by the ESAPI.

While the ESAPI is significantly less complex to use than the System API for cryptographically protected communications with the TPM, it still requires in-depth understanding about the interface to a TPM 2.0. It is therefore envisioned that only expert applications will utilize the ESAPI and that typical applications would utilize a higher-level interface such as the Feature API. It is, however, expected that Feature API implementations would utilize the ESAPI as appropriate.

## **2.7 Feature API (FAPI)**

The feature/environment API provides a higher level software abstraction to application developers. For instance, an application may want to create a key without any knowledge of the low level details. This level of abstraction will be provided by the feature and application APIs.

## 3 Common Header File

tss2\_common.h

### 3.1 tss2\_common.h Prelude

```
#ifndef TSS2_COMMON_H
#define TSS2_COMMON_H

#define TSS2_API_VERSION_1_2_1_108

#include <stdint.h>
```

### 3.2 tss2\_common.h DLL Export Macros

While the TCG TSS specifications do not mandate distribution of a TSS2 implementation as either a static or shared library, the provided header files contain macros to facilitate implementation as a shared library on Microsoft Windows™. These macros are not provided for other platforms such as Linux because they are not needed to build shared libraries on unix-like platforms.

When built as a DLL on Microsoft Windows™ the library creator should define the macro `TSS2_DLL_EXPORTS` (with any value) to enable the export of symbols from the DLL. The application developer using the library does not have to define any macros in order to enable the import of symbols from the DLL for use in the application.

When built as a static library on Microsoft Windows™ both the library creator and the application developer must define the macro `TSS2_STATIC`.

On unix-like Oses these macros are not needed and have no effect.

```
/*
 * Macros to export function symbols on Microsoft Windows™
 */
#if !defined(TSS2_DLL_EXPORT)
  #if !defined(WIN32) || defined(TSS2_STATIC)
    #define TSS2_DLL_EXPORT
  #elif defined(TSS2_DLL_EXPORTS)
    #define TSS2_DLL_EXPORT __declspec(dllexport)
  #else
    #define TSS2_DLL_EXPORT __declspec(dllimport)
  #endif
#endif /* end TSS2_DLL_EXPORT */
```

### 3.3 tss2\_common.h Application Binary Interface (ABI) Negotiation

```
/*
 * ABI runtime negotiation definitions
 */
typedef struct {
  uint32_t tssCreator;
  uint32_t tssFamily;
  uint32_t tssLevel;
  uint32_t tssVersion;
} TSS2_ABI_VERSION;

#define TSS2_ABI_VERSION_CURRENT {1, 2, 1, 108}
```

### 3.4 tss2\_common.h Common Return Codes

```

/*
 * Return Codes
 */

/* The return type for all TSS2 functions */
typedef uint32_t TSS2_RC;

/* For return values other than SUCCESS, the second most significant
 * byte of the return value is a layer code indicating the software
 * layer that generated the error.
 */
#define TSS2_RC_LAYER_SHIFT      (16)
#define TSS2_RC_LAYER(layer)    ((TSS2_RC)((layer) <<
TSS2_RC_LAYER_SHIFT))
#define TSS2_RC_LAYER_MASK      TSS2_RC_LAYER(0xff)

/* These layer codes are reserved for software layers defined in the
TCG
 * specifications.
 */
#define TSS2_TPM_RC_LAYER        TSS2_RC_LAYER(0) /* base is a
TPM2_RC_* */
#define TSS2_FEATURE_RC_LAYER    TSS2_RC_LAYER(6) /* base is a
TSS2_BASE_RC_* */
#define TSS2_ESYS_RC_LAYER       TSS2_RC_LAYER(7) /* base is a
TSS2_BASE_RC_* */
#define TSS2_SYS_RC_LAYER        TSS2_RC_LAYER(8) /* base is a
TSS2_BASE_RC_* */
#define TSS2_MU_RC_LAYER         TSS2_RC_LAYER(9) /* base is a
TSS2_BASE_RC_* */
#define TSS2_TCTI_RC_LAYER       TSS2_RC_LAYER(10) /* base is a
TSS2_BASE_RC_* */
#define TSS2_RESMGR_RC_LAYER     TSS2_RC_LAYER(11) /* base is a
TSS2_BASE_RC_* */
#define TSS2_RESMGR_TPM_RC_LAYER TSS2_RC_LAYER(12) /* base is a
TPM_RC_* */

```

### 3.5 tss2\_common.h Base Return Codes

```

/* Base return codes.
 * These base codes indicate the error that occurred. They are
 * logical-ORed with a layer code to produce the TSS2 return value.
 */
#define TSS2_BASE_RC_GENERAL_FAILURE 1U /* Catch all for all
errors not otherwise specified */
#define TSS2_BASE_RC_NOT_IMPLEMENTED 2U /* If called
functionality isn't implemented */
#define TSS2_BASE_RC_BAD_CONTEXT     3U /* A context structure
is bad */
#define TSS2_BASE_RC_ABI_MISMATCH    4U /* Passed in ABI version
doesn't match called module's ABI version */
#define TSS2_BASE_RC_BAD_REFERENCE   5U /* A pointer is NULL
that isn't allowed to be NULL. */

```

```

#define TSS2_BASE_RC_INSUFFICIENT_BUFFER      6U /* A buffer isn't large
enough */
#define TSS2_BASE_RC_BAD_SEQUENCE            7U /* Function called in
the wrong order */
#define TSS2_BASE_RC_NO_CONNECTION           8U /* Fails to connect to
next lower layer */
#define TSS2_BASE_RC_TRY_AGAIN               9U /* Operation timed out;
function must be called again to be completed */
#define TSS2_BASE_RC_IO_ERROR               10U /* IO failure */
#define TSS2_BASE_RC_BAD_VALUE              11U /* A parameter has a bad
value */
#define TSS2_BASE_RC_NOT_PERMITTED          12U /* Operation not
permitted. */
#define TSS2_BASE_RC_INVALID_SESSIONS       13U /* The TPM command
doesn't use the number of sessions provided by the caller */
#define TSS2_BASE_RC_NO_DECRYPT_PARAM        14U /* A session with its
TPMA_SESSION_DECRYPT bit set was passed to a TPM command that doesn't
support encryption of the first command parameter. */
#define TSS2_BASE_RC_NO_ENCRYPT_PARAM        15U /* A session with its
TPMA_SESSION_ENCRYPT bit set was passed to a TPM command that doesn't
support encryption of the first response parameter. */
#define TSS2_BASE_RC_BAD_SIZE               16U /* If size of a
parameter is incorrect */
#define TSS2_BASE_RC_MALFORMED_RESPONSE     17U /* Response is malformed
*/
#define TSS2_BASE_RC_INSUFFICIENT_CONTEXT   18U /* Context not large
enough */
#define TSS2_BASE_RC_INSUFFICIENT_RESPONSE  19U /* Response is not long
enough */
#define TSS2_BASE_RC_INCOMPATIBLE_TCTI     20U /* Unknown or unusable
TCTI version */
#define TSS2_BASE_RC_NOT_SUPPORTED          21U /* Functionality not
supported. */
#define TSS2_BASE_RC_BAD_TCTI_STRUCTURE     22U /* TCTI context is bad.
*/
#define TSS2_BASE_RC_MEMORY                23U /* memory allocation
failed */
#define TSS2_BASE_RC_BAD_TR                 24U /* invalid ESYS_TR
handle */
#define TSS2_BASE_RC_MULTIPLE_DECRYPT_SESSIONS 25U /* More than one
session with TPMA_SESSION_DECRYPT bit set */
#define TSS2_BASE_RC_MULTIPLE_ENCRYPT_SESSIONS 26U /* More than one
session with TPMA_SESSION_ENCRYPT bit set */
#define TSS2_BASE_RC_RSP_AUTH_FAILED        27U /* Response HMAC from TPM did
not verify */

/* Base return codes in the range 0xf800 - 0xffff are reserved for
 * implementation-specific purposes.
 */
#define TSS2_LAYER_IMPLEMENTATION_SPECIFIC_OFFSET 0xf800

/* Success is the same for all software layers */
#define TSS2_RC_SUCCESS                      ((TSS2_RC) 0)

```

### 3.6 tss2\_common.h TCTI Response Codes

```

/* TCTI response codes */

```



```

#define TSS2_TCTI_RC_GENERAL_FAILURE \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_GENERAL_FAILURE))
#define TSS2_TCTI_RC_NOT_IMPLEMENTED
\
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_NOT_IMPLEMENTED))
#define TSS2_TCTI_RC_BAD_CONTEXT
\
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER | TSS2_BASE_RC_BAD_CONTEXT))
#define TSS2_TCTI_RC_ABI_MISMATCH \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_ABI_MISMATCH))
#define TSS2_TCTI_RC_BAD_REFERENCE \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_BAD_REFERENCE))
#define TSS2_TCTI_RC_INSUFFICIENT_BUFFER \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_INSUFFICIENT_BUFFER))
#define TSS2_TCTI_RC_BAD_SEQUENCE \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_BAD_SEQUENCE))
#define TSS2_TCTI_RC_NO_CONNECTION \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_NO_CONNECTION))
#define TSS2_TCTI_RC_TRY_AGAIN \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER | TSS2_BASE_RC_TRY_AGAIN))
#define TSS2_TCTI_RC_IO_ERROR \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER | TSS2_BASE_RC_IO_ERROR))
#define TSS2_TCTI_RC_BAD_VALUE \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER | TSS2_BASE_RC_BAD_VALUE))
#define TSS2_TCTI_RC_NOT_PERMITTED \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_NOT_PERMITTED))
#define TSS2_TCTI_RC_MALFORMED_RESPONSE \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_MALFORMED_RESPONSE))
#define TSS2_TCTI_RC_NOT_SUPPORTED \
    ((TSS2_RC) (TSS2_TCTI_RC_LAYER |
TSS2_BASE_RC_NOT_SUPPORTED))

```

### 3.7 tss2\_common.h SAPI (SYS) Error Codes

```

/* SAPI response codes */
#define TSS2_SYS_RC_GENERAL_FAILURE \
    ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_GENERAL_FAILURE))
#define TSS2_SYS_RC_ABI_MISMATCH \
    ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_ABI_MISMATCH))
#define TSS2_SYS_RC_BAD_REFERENCE \
    ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_BAD_REFERENCE))
#define TSS2_SYS_RC_INSUFFICIENT_BUFFER \
    ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_INSUFFICIENT_BUFFER))
#define TSS2_SYS_RC_BAD_SEQUENCE \

```

```

        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_BAD_SEQUENCE))
#define TSS2_SYS_RC_BAD_VALUE \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER | TSS2_BASE_RC_BAD_VALUE))
#define TSS2_SYS_RC_INVALID_SESSIONS \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_INVALID_SESSIONS))
#define TSS2_SYS_RC_NO_DECRYPT_PARAM \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_NO_DECRYPT_PARAM))
#define TSS2_SYS_RC_NO_ENCRYPT_PARAM \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_NO_ENCRYPT_PARAM))
#define TSS2_SYS_RC_BAD_SIZE \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER | TSS2_BASE_RC_BAD_SIZE))
#define TSS2_SYS_RC_MALFORMED_RESPONSE \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_MALFORMED_RESPONSE))
#define TSS2_SYS_RC_INSUFFICIENT_CONTEXT \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_INSUFFICIENT_CONTEXT))
#define TSS2_SYS_RC_INSUFFICIENT_RESPONSE \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_INSUFFICIENT_RESPONSE))
#define TSS2_SYS_RC_INCOMPATIBLE_TCTI \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_INCOMPATIBLE_TCTI))
#define TSS2_SYS_RC_BAD_TCTI_STRUCTURE \
        ((TSS2_RC) (TSS2_SYS_RC_LAYER |
TSS2_BASE_RC_BAD_TCTI_STRUCTURE))

```

### 3.8 tss2\_common.h MUAPI Error Codes

```

/* MUAPI response codes */
#define TSS2_MU_RC_GENERAL_FAILURE \
        ((TSS2_RC) (TSS2_MU_RC_LAYER |
TSS2_BASE_RC_GENERAL_FAILURE))
#define TSS2_MU_RC_BAD_REFERENCE \
        ((TSS2_RC) (TSS2_MU_RC_LAYER |
TSS2_BASE_RC_BAD_REFERENCE))
#define TSS2_MU_RC_BAD_SIZE \
        ((TSS2_RC) (TSS2_MU_RC_LAYER | TSS2_BASE_RC_BAD_SIZE))
#define TSS2_MU_RC_BAD_VALUE \
        ((TSS2_RC) (TSS2_MU_RC_LAYER | TSS2_BASE_RC_BAD_VALUE))
#define TSS2_MU_RC_INSUFFICIENT_BUFFER \
        ((TSS2_RC) (TSS2_MU_RC_LAYER |
TSS2_BASE_RC_INSUFFICIENT_BUFFER))

```

### 3.9 tss2\_common.h ESAPI Error Codes

```

/* ESAPI response codes */
#define TSS2_ESYS_RC_GENERAL_FAILURE \
        ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_GENERAL_FAILURE))
#define TSS2_ESYS_RC_NOT_IMPLEMENTED \
        ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_NOT_IMPLEMENTED))

```

```

#define TSS2_ESYS_RC_BAD_CONTEXT \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER | TSS2_BASE_RC_BAD_CONTEXT))
#define TSS2_ESYS_RC_ABI_MISMATCH \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_ABI_MISMATCH))
#define TSS2_ESYS_RC_BAD_REFERENCE \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_BAD_REFERENCE))
#define TSS2_ESYS_RC_BAD_SEQUENCE \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_BAD_SEQUENCE))
#define TSS2_ESYS_RC_TRY_AGAIN \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER | TSS2_BASE_RC_TRY_AGAIN))
#define TSS2_ESYS_RC_BAD_VALUE \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER | TSS2_BASE_RC_BAD_VALUE))
#define TSS2_ESYS_RC_NO_DECRYPT_PARAM \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_NO_DECRYPT_PARAM))
#define TSS2_ESYS_RC_NO_ENCRYPT_PARAM \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_NO_ENCRYPT_PARAM))
#define TSS2_ESYS_RC_MALFORMED_RESPONSE \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_MALFORMED_RESPONSE))
#define TSS2_ESYS_RC_INSUFFICIENT_RESPONSE \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_INSUFFICIENT_RESPONSE))
#define TSS2_ESYS_RC_INCOMPATIBLE_TCTI \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_INCOMPATIBLE_TCTI))
#define TSS2_ESYS_RC_BAD_TCTI_STRUCTURE \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_BAD_TCTI_STRUCTURE))
#define TSS2_ESYS_RC_MEMORY \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER | TSS2_BASE_RC_MEMORY))
#define TSS2_ESYS_RC_BAD_TR \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER | TSS2_BASE_RC_BAD_TR))
#define TSS2_ESYS_RC_MULTIPLE_DECRYPT_SESSIONS \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_MULTIPLE_DECRYPT_SESSIONS))
#define TSS2_ESYS_RC_MULTIPLE_ENCRYPT_SESSIONS \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_MULTIPLE_ENCRYPT_SESSIONS))
#define TSS2_ESYS_RC_RSP_AUTH_FAILED \
    ((TSS2_RC) (TSS2_ESYS_RC_LAYER |
TSS2_BASE_RC_RSP_AUTH_FAILED))

```

### 3.10 tss2\_common.h Postlude

```
#endif /* TSS2_COMMON_H */
```

## 4 TPM 2 Types Header File

tss2\_tpm2\_types.h

### 4.1 tss2\_tpm2\_types.h Prelude

```
#ifndef TSS2_TPM2_TYPES_H
#define TSS2_TPM2_TYPES_H

#include "tss2_common.h"

#ifndef TSS2_API_VERSION_1_2_1_108
#error Version mismatch among TSS2 header files.
#endif
```

### 4.2 tss2\_tpm2\_types.h ABI Constants

```
/*
 * ABI Constants
 */
```

The following set of ABI constants are the digest sizes of common algorithms.

```
#define TPM2_SHA_DIGEST_SIZE      20
#define TPM2_SHA1_DIGEST_SIZE     20
#define TPM2_SHA256_DIGEST_SIZE   32
#define TPM2_SHA384_DIGEST_SIZE   48
#define TPM2_SHA512_DIGEST_SIZE   64
#define TPM2_SM3_256_DIGEST_SIZE  32
```

The following set of ABI constants were chosen by the TSS Working Group. They represent reasonable, future-proof values.

```
#define TPM2_NUM_PCR_BANKS        3
#define TPM2_MAX_DIGEST_BUFFER    1024
#define TPM2_MAX_NV_BUFFER_SIZE   2048
#define TPM2_MAX_PCERS            32
#define TPM2_MAX_ALG_LIST_SIZE    128
#define TPM2_MAX_CAP_CC           256
#define TPM2_MAX_CAP_BUFFER       1024
#define TPM2_MAX_CONTEXT_SIZE     5120
```

The following set of ABI constants are parameters for cryptographic algorithms. These represent reasonable, future-proof values.

```
#define TPM2_MAX_SYM_BLOCK_SIZE   16
#define TPM2_MAX_SYM_DATA         256
#define TPM2_MAX_ECC_KEY_BYTES    128
#define TPM2_MAX_SYM_KEY_BYTES    32
#define TPM2_MAX_RSA_KEY_BYTES    512
```

The following set of ABI constants derived from the previous values or generic TPM constants.

```
#define TPM2_LABEL_MAX_BUFFER     32
#define TPM2_PCR_SELECT_MAX       ((TPM2_MAX_PCERS+7)/8)
#define TPM2_MAX_CAP_HANDLES      ((TPM2_MAX_CAP_BUFFER -
sizeof(TPM2_CAP) - sizeof(UINT32))/sizeof(TPM2_HANDLE))
#define TPM2_MAX_CAP_ALGS         ((TPM2_MAX_CAP_BUFFER -
sizeof(TPM2_CAP) - sizeof(UINT32))/sizeof(TPMS_ALG_PROPERTY))
#define TPM2_MAX_TPM_PROPERTIES   ((TPM2_MAX_CAP_BUFFER -
sizeof(TPM2_CAP) - sizeof(UINT32))/sizeof(TPMS_TAGGED_PROPERTY))
```

```
#define TPM2_MAX_PCR_PROPERTIES ((TPM2_MAX_CAP_BUFFER -
sizeof(TPM2_CAP) - sizeof(UINT32))/sizeof(TPMS_TAGGED_PCR_SELECT))
#define TPM2_MAX_ECC_CURVES ((TPM2_MAX_CAP_BUFFER -
sizeof(TPM2_CAP) - sizeof(UINT32))/sizeof(TPM2_ECC_CURVE))
#define TPM2_MAX_TAGGED_POLICIES ((TPM2_MAX_CAP_BUFFER -
sizeof(TPM2_CAP) - sizeof(UINT32))/sizeof(TPMS_TAGGED_POLICY))
#define TPM2_PRIVATE_VENDOR_SPECIFIC_BYTES ((TPM2_MAX_RSA_KEY_BYTES/2)
* (3 + 2))
```

### 4.3 tss2\_tpm2\_types.h Definition of Types and Associated Constants

```
/*
 * Type & Constant Definitions from the TPM Specification, Part 2
 * Level 00 Revision 01.38.
 */

/* Table 3 - Definition of Base Types */
#if defined(WIN32)
#include <Windows.h>
#else
typedef uint8_t      UINT8;
typedef uint8_t      BYTE;
typedef int8_t       INT8;
typedef uint16_t     UINT16;
typedef int16_t      INT16;
typedef uint32_t     UINT32;
typedef int32_t      INT32;
typedef uint64_t     UINT64;
typedef int64_t      INT64;
#endif

/* Table 5 - Definition of Types for Documentation Clarity */
typedef UINT32 TPM2_ALGORITHM_ID;
typedef UINT32 TPM2_MODIFIER_INDICATOR;
typedef UINT32 TPM2_AUTHORIZATION_SIZE;
typedef UINT32 TPM2_PARAMETER_SIZE;
typedef UINT16 TPM2_KEY_SIZE;
typedef UINT16 TPM2_KEY_BITS;

/* Table 6 - Definition of (UINT32) TPM2_SPEC Constants */
typedef UINT32 TPM2_SPEC;
#define TPM2_SPEC_FAMILY ((TPM2_SPEC) 0x322E3000)
#define TPM2_SPEC_LEVEL ((TPM2_SPEC) 00)
#define TPM2_SPEC_VERSION ((TPM2_SPEC) 138)
#define TPM2_SPEC_YEAR ((TPM2_SPEC) 2016)
#define TPM2_SPEC_DAY_OF_YEAR ((TPM2_SPEC) 273)

/* Table 7 - Definition of (UINT32) TPM2_GENERATED Constants */
typedef UINT32 TPM2_GENERATED;
#define TPM2_GENERATED_VALUE ((TPM2_GENERATED) 0xff544347)

/* Table 9 - Definition of (UINT16) TPM2_ALG_ID Constants */
typedef UINT16 TPM2_ALG_ID;
#define TPM2_ALG_ERROR ((TPM2_ALG_ID) 0x0000)
#define TPM2_ALG_RSA ((TPM2_ALG_ID) 0x0001)
#define TPM2_ALG_SHA ((TPM2_ALG_ID) 0x0004)
```

```

#define TPM2_ALG_SHA1                ((TPM2_ALG_ID) 0x0004)
#define TPM2_ALG_HMAC                 ((TPM2_ALG_ID) 0x0005)
#define TPM2_ALG_AES                  ((TPM2_ALG_ID) 0x0006)
#define TPM2_ALG_MGF1                 ((TPM2_ALG_ID) 0x0007)
#define TPM2_ALG_KEYEDHASH            ((TPM2_ALG_ID) 0x0008)
#define TPM2_ALG_XOR                  ((TPM2_ALG_ID) 0x000A)
#define TPM2_ALG_SHA256               ((TPM2_ALG_ID) 0x000B)
#define TPM2_ALG_SHA384               ((TPM2_ALG_ID) 0x000C)
#define TPM2_ALG_SHA512               ((TPM2_ALG_ID) 0x000D)
#define TPM2_ALG_NULL                  ((TPM2_ALG_ID) 0x0010)
#define TPM2_ALG_SM3_256              ((TPM2_ALG_ID) 0x0012)
#define TPM2_ALG_SM4                   ((TPM2_ALG_ID) 0x0013)
#define TPM2_ALG_RSASSA                ((TPM2_ALG_ID) 0x0014)
#define TPM2_ALG_RSAES                 ((TPM2_ALG_ID) 0x0015)
#define TPM2_ALG_RSAPSS                ((TPM2_ALG_ID) 0x0016)
#define TPM2_ALG_OAEP                  ((TPM2_ALG_ID) 0x0017)
#define TPM2_ALG_ECDSA                 ((TPM2_ALG_ID) 0x0018)
#define TPM2_ALG_ECDH                  ((TPM2_ALG_ID) 0x0019)
#define TPM2_ALG_ECDA                  ((TPM2_ALG_ID) 0x001A)
#define TPM2_ALG_SM2                   ((TPM2_ALG_ID) 0x001B)
#define TPM2_ALG_ECSCHNORR            ((TPM2_ALG_ID) 0x001C)
#define TPM2_ALG_ECMQV                 ((TPM2_ALG_ID) 0x001D)
#define TPM2_ALG_KDF1_SP800_56A       ((TPM2_ALG_ID) 0x0020)
#define TPM2_ALG_KDF2                 ((TPM2_ALG_ID) 0x0021)
#define TPM2_ALG_KDF1_SP800_108       ((TPM2_ALG_ID) 0x0022)
#define TPM2_ALG_ECC                   ((TPM2_ALG_ID) 0x0023)
#define TPM2_ALG_SYMCIPHER             ((TPM2_ALG_ID) 0x0025)
#define TPM2_ALG_CAMELLIA              ((TPM2_ALG_ID) 0x0026)
#define TPM2_ALG_CTR                    ((TPM2_ALG_ID) 0x0040)
#define TPM2_ALG_OFB                   ((TPM2_ALG_ID) 0x0041)
#define TPM2_ALG_CBC                    ((TPM2_ALG_ID) 0x0042)
#define TPM2_ALG_CFB                    ((TPM2_ALG_ID) 0x0043)
#define TPM2_ALG_ECB                    ((TPM2_ALG_ID) 0x0044)

/* Table 10 - Definition of (UINT16) { ECC } TPM2_ECC_CURVE Constants
*/
typedef UINT16 TPM2_ECC_CURVE;
#define TPM2_ECC_NONE                  ((TPM2_ECC_CURVE) 0x0000)
#define TPM2_ECC_NIST_P192             ((TPM2_ECC_CURVE) 0x0001)
#define TPM2_ECC_NIST_P224             ((TPM2_ECC_CURVE) 0x0002)
#define TPM2_ECC_NIST_P256             ((TPM2_ECC_CURVE) 0x0003)
#define TPM2_ECC_NIST_P384             ((TPM2_ECC_CURVE) 0x0004)
#define TPM2_ECC_NIST_P521             ((TPM2_ECC_CURVE) 0x0005)
#define TPM2_ECC_BN_P256               ((TPM2_ECC_CURVE) 0x0010)
#define TPM2_ECC_BN_P638               ((TPM2_ECC_CURVE) 0x0011)
#define TPM2_ECC_SM2_P256              ((TPM2_ECC_CURVE) 0x0020)

/* Table 12 - Definition of (UINT32) TPM2_CC Constants (Numeric Order)
*/
typedef UINT32 TPM2_CC;
#define TPM2_CC_FIRST                   ((TPM2_CC) 0x0000011F)
#define TPM2_CC_NV_UndefineSpaceSpecial ((TPM2_CC) 0x0000011F)
#define TPM2_CC_EvictControl            ((TPM2_CC) 0x00000120)
#define TPM2_CC_HierarchyControl        ((TPM2_CC) 0x00000121)
#define TPM2_CC_NV_UndefineSpace        ((TPM2_CC) 0x00000122)
#define TPM2_CC_ChangeEPS                ((TPM2_CC) 0x00000124)
#define TPM2_CC_ChangePPS                ((TPM2_CC) 0x00000125)

```

```

#define TPM2_CC_Clear (TPM2_CC) 0x00000126)
#define TPM2_CC_ClearControl (TPM2_CC) 0x00000127)
#define TPM2_CC_ClockSet (TPM2_CC) 0x00000128)
#define TPM2_CC_HierarchyChangeAuth (TPM2_CC) 0x00000129)
#define TPM2_CC_NV_DefineSpace (TPM2_CC) 0x0000012A)
#define TPM2_CC_PCR_Allocate (TPM2_CC) 0x0000012B)
#define TPM2_CC_PCR_SetAuthPolicy (TPM2_CC) 0x0000012C)
#define TPM2_CC_PP_Commands (TPM2_CC) 0x0000012D)
#define TPM2_CC_SetPrimaryPolicy (TPM2_CC) 0x0000012E)
#define TPM2_CC_FieldUpgradeStart (TPM2_CC) 0x0000012F)
#define TPM2_CC_ClockRateAdjust (TPM2_CC) 0x00000130)
#define TPM2_CC_CreatePrimary (TPM2_CC) 0x00000131)
#define TPM2_CC_NV_GlobalWriteLock (TPM2_CC) 0x00000132)
#define TPM2_CC_GetCommandAuditDigest (TPM2_CC) 0x00000133)
#define TPM2_CC_NV_Increment (TPM2_CC) 0x00000134)
#define TPM2_CC_NV_SetBits (TPM2_CC) 0x00000135)
#define TPM2_CC_NV_Extend (TPM2_CC) 0x00000136)
#define TPM2_CC_NV_Write (TPM2_CC) 0x00000137)
#define TPM2_CC_NV_WriteLock (TPM2_CC) 0x00000138)
#define TPM2_CC_DictionaryAttackLockReset (TPM2_CC) 0x00000139)
#define TPM2_CC_DictionaryAttackParameters (TPM2_CC) 0x0000013A)
#define TPM2_CC_NV_ChangeAuth (TPM2_CC) 0x0000013B)
#define TPM2_CC_PCR_Event (TPM2_CC) 0x0000013C)
#define TPM2_CC_PCR_Reset (TPM2_CC) 0x0000013D)
#define TPM2_CC_SequenceComplete (TPM2_CC) 0x0000013E)
#define TPM2_CC_SetAlgorithmSet (TPM2_CC) 0x0000013F)
#define TPM2_CC_SetCommandCodeAuditStatus (TPM2_CC) 0x00000140)
#define TPM2_CC_FieldUpgradeData (TPM2_CC) 0x00000141)
#define TPM2_CC_IncrementalSelfTest (TPM2_CC) 0x00000142)
#define TPM2_CC_SelfTest (TPM2_CC) 0x00000143)
#define TPM2_CC_Startup (TPM2_CC) 0x00000144)
#define TPM2_CC_Shutdown (TPM2_CC) 0x00000145)
#define TPM2_CC_StirRandom (TPM2_CC) 0x00000146)
#define TPM2_CC_ActivateCredential (TPM2_CC) 0x00000147)
#define TPM2_CC_Certify (TPM2_CC) 0x00000148)
#define TPM2_CC_PolicyNV (TPM2_CC) 0x00000149)
#define TPM2_CC_CertifyCreation (TPM2_CC) 0x0000014A)
#define TPM2_CC_Duplicate (TPM2_CC) 0x0000014B)
#define TPM2_CC_GetTime (TPM2_CC) 0x0000014C)
#define TPM2_CC_GetSessionAuditDigest (TPM2_CC) 0x0000014D)
#define TPM2_CC_NV_Read (TPM2_CC) 0x0000014E)
#define TPM2_CC_NV_ReadLock (TPM2_CC) 0x0000014F)
#define TPM2_CC_ObjectChangeAuth (TPM2_CC) 0x00000150)
#define TPM2_CC_PolicySecret (TPM2_CC) 0x00000151)
#define TPM2_CC_Rewrap (TPM2_CC) 0x00000152)
#define TPM2_CC_Create (TPM2_CC) 0x00000153)
#define TPM2_CC_ECDH_ZGen (TPM2_CC) 0x00000154)
#define TPM2_CC_HMAC (TPM2_CC) 0x00000155)
#define TPM2_CC_Import (TPM2_CC) 0x00000156)
#define TPM2_CC_Load (TPM2_CC) 0x00000157)
#define TPM2_CC_Quote (TPM2_CC) 0x00000158)
#define TPM2_CC_RSA_Decrypt (TPM2_CC) 0x00000159)
#define TPM2_CC_HMAC_Start (TPM2_CC) 0x0000015B)
#define TPM2_CC_SequenceUpdate (TPM2_CC) 0x0000015C)
#define TPM2_CC_Sign (TPM2_CC) 0x0000015D)
#define TPM2_CC_Unseal (TPM2_CC) 0x0000015E)
#define TPM2_CC_PolicySigned (TPM2_CC) 0x00000160)

```

```

#define TPM2_CC_ContextLoad          ((TPM2_CC) 0x00000161)
#define TPM2_CC_ContextSave          ((TPM2_CC) 0x00000162)
#define TPM2_CC_ECDH_KeyGen          ((TPM2_CC) 0x00000163)
#define TPM2_CC_EncryptDecrypt       ((TPM2_CC) 0x00000164)
#define TPM2_CC_FlushContext         ((TPM2_CC) 0x00000165)
#define TPM2_CC_LoadExternal          ((TPM2_CC) 0x00000167)
#define TPM2_CC_MakeCredential        ((TPM2_CC) 0x00000168)
#define TPM2_CC_NV_ReadPublic         ((TPM2_CC) 0x00000169)
#define TPM2_CC_PolicyAuthorize       ((TPM2_CC) 0x0000016A)
#define TPM2_CC_PolicyAuthValue       ((TPM2_CC) 0x0000016B)
#define TPM2_CC_PolicyCommandCode    ((TPM2_CC) 0x0000016C)
#define TPM2_CC_PolicyCounterTimer    ((TPM2_CC) 0x0000016D)
#define TPM2_CC_PolicyCpHash          ((TPM2_CC) 0x0000016E)
#define TPM2_CC_PolicyLocality        ((TPM2_CC) 0x0000016F)
#define TPM2_CC_PolicyNameHash        ((TPM2_CC) 0x00000170)
#define TPM2_CC_PolicyOR              ((TPM2_CC) 0x00000171)
#define TPM2_CC_PolicyTicket          ((TPM2_CC) 0x00000172)
#define TPM2_CC_ReadPublic            ((TPM2_CC) 0x00000173)
#define TPM2_CC_RSA_Encrypt           ((TPM2_CC) 0x00000174)
#define TPM2_CC_StartAuthSession      ((TPM2_CC) 0x00000176)
#define TPM2_CC_VerifySignature       ((TPM2_CC) 0x00000177)
#define TPM2_CC_ECC_Parameters        ((TPM2_CC) 0x00000178)
#define TPM2_CC_FirmwareRead          ((TPM2_CC) 0x00000179)
#define TPM2_CC_GetCapability          ((TPM2_CC) 0x0000017A)
#define TPM2_CC_GetRandom              ((TPM2_CC) 0x0000017B)
#define TPM2_CC_GetTestResult         ((TPM2_CC) 0x0000017C)
#define TPM2_CC_Hash                   ((TPM2_CC) 0x0000017D)
#define TPM2_CC_PCR_Read               ((TPM2_CC) 0x0000017E)
#define TPM2_CC_PolicyPCR              ((TPM2_CC) 0x0000017F)
#define TPM2_CC_PolicyRestart         ((TPM2_CC) 0x00000180)
#define TPM2_CC_ReadClock              ((TPM2_CC) 0x00000181)
#define TPM2_CC_PCR_Extend              ((TPM2_CC) 0x00000182)
#define TPM2_CC_PCR_SetAuthValue       ((TPM2_CC) 0x00000183)
#define TPM2_CC_NV_Certify             ((TPM2_CC) 0x00000184)
#define TPM2_CC_EventSequenceComplete ((TPM2_CC) 0x00000185)
#define TPM2_CC_HashSequenceStart      ((TPM2_CC) 0x00000186)
#define TPM2_CC_PolicyPhysicalPresence ((TPM2_CC) 0x00000187)
#define TPM2_CC_PolicyDuplicationSelect ((TPM2_CC) 0x00000188)
#define TPM2_CC_PolicyGetDigest        ((TPM2_CC) 0x00000189)
#define TPM2_CC_TestParms              ((TPM2_CC) 0x0000018A)
#define TPM2_CC_Commit                 ((TPM2_CC) 0x0000018B)
#define TPM2_CC_PolicyPassword         ((TPM2_CC) 0x0000018C)
#define TPM2_CC_ZGen_2Phase            ((TPM2_CC) 0x0000018D)
#define TPM2_CC_EC_Ephemeral           ((TPM2_CC) 0x0000018E)
#define TPM2_CC_PolicyNvWritten         ((TPM2_CC) 0x0000018F)
#define TPM2_CC_PolicyTemplate         ((TPM2_CC) 0x00000190)
#define TPM2_CC_CreateLoaded           ((TPM2_CC) 0x00000191)
#define TPM2_CC_PolicyAuthorizeNV      ((TPM2_CC) 0x00000192)
#define TPM2_CC_EncryptDecrypt2        ((TPM2_CC) 0x00000193)
#define TPM2_CC_LAST                    ((TPM2_CC) 0x00000193)
#define TPM2_CC_VEND                    ((TPM2_CC) 0x20000000)
#define TPM2_CC_Vendor_TCG_Test        ((TPM2_CC)
(TPM2_CC_VEND+0x0000))

```

```

/* Table 16 - Definition of (UINT32) TPM2_RC Constants (Actions) */
typedef UINT32 TPM2_RC;
#define TPM2_RC_SUCCESS                ((TPM2_RC) 0x000)

```



```

#define TPM2_RC_BAD_TAG                ((TPM2_RC) 0x01E)
#define TPM2_RC_VER1                    ((TPM2_RC) 0x100)
#define TPM2_RC_INITIALIZE              ((TPM2_RC) (TPM2_RC_VER1 + 0x000))
#define TPM2_RC_FAILURE                 ((TPM2_RC) (TPM2_RC_VER1 + 0x001))
#define TPM2_RC_SEQUENCE                ((TPM2_RC) (TPM2_RC_VER1 + 0x003))
#define TPM2_RC_PRIVATE                 ((TPM2_RC) (TPM2_RC_VER1 + 0x00B))
#define TPM2_RC_HMAC                    ((TPM2_RC) (TPM2_RC_VER1 + 0x019))
#define TPM2_RC_DISABLED                ((TPM2_RC) (TPM2_RC_VER1 + 0x020))
#define TPM2_RC_EXCLUSIVE                ((TPM2_RC) (TPM2_RC_VER1 + 0x021))
#define TPM2_RC_AUTH_TYPE               ((TPM2_RC) (TPM2_RC_VER1 + 0x024))
#define TPM2_RC_AUTH_MISSING            ((TPM2_RC) (TPM2_RC_VER1 + 0x025))
#define TPM2_RC_POLICY                  ((TPM2_RC) (TPM2_RC_VER1 + 0x026))
#define TPM2_RC_PCR                     ((TPM2_RC) (TPM2_RC_VER1 + 0x027))
#define TPM2_RC_PCR_CHANGED              ((TPM2_RC) (TPM2_RC_VER1 + 0x028))
#define TPM2_RC_UPGRADE                  ((TPM2_RC) (TPM2_RC_VER1 + 0x02D))
#define TPM2_RC_TOO_MANY_CONTEXTS       ((TPM2_RC) (TPM2_RC_VER1 + 0x02E))
#define TPM2_RC_AUTH_UNAVAILABLE         ((TPM2_RC) (TPM2_RC_VER1 + 0x02F))
#define TPM2_RC_REBOOT                   ((TPM2_RC) (TPM2_RC_VER1 + 0x030))
#define TPM2_RC_UNBALANCED               ((TPM2_RC) (TPM2_RC_VER1 + 0x031))
#define TPM2_RC_COMMAND_SIZE             ((TPM2_RC) (TPM2_RC_VER1 + 0x042))
#define TPM2_RC_COMMAND_CODE             ((TPM2_RC) (TPM2_RC_VER1 + 0x043))
#define TPM2_RC_AUTHSIZE                 ((TPM2_RC) (TPM2_RC_VER1 + 0x044))
#define TPM2_RC_AUTH_CONTEXT             ((TPM2_RC) (TPM2_RC_VER1 + 0x045))
#define TPM2_RC_NV_RANGE                 ((TPM2_RC) (TPM2_RC_VER1 + 0x046))
#define TPM2_RC_NV_SIZE                  ((TPM2_RC) (TPM2_RC_VER1 + 0x047))
#define TPM2_RC_NV_LOCKED                ((TPM2_RC) (TPM2_RC_VER1 + 0x048))
#define TPM2_RC_NV_AUTHORIZATION         ((TPM2_RC) (TPM2_RC_VER1 + 0x049))
#define TPM2_RC_NV_UNINITIALIZED         ((TPM2_RC) (TPM2_RC_VER1 + 0x04A))
#define TPM2_RC_NV_SPACE                 ((TPM2_RC) (TPM2_RC_VER1 + 0x04B))
#define TPM2_RC_NV_DEFINED               ((TPM2_RC) (TPM2_RC_VER1 + 0x04C))
#define TPM2_RC_BAD_CONTEXT              ((TPM2_RC) (TPM2_RC_VER1 + 0x050))
#define TPM2_RC_CPHASH                   ((TPM2_RC) (TPM2_RC_VER1 + 0x051))
#define TPM2_RC_PARENT                   ((TPM2_RC) (TPM2_RC_VER1 + 0x052))
#define TPM2_RC_NEEDS_TEST                ((TPM2_RC) (TPM2_RC_VER1 + 0x053))
#define TPM2_RC_NO_RESULT                 ((TPM2_RC) (TPM2_RC_VER1 + 0x054))
#define TPM2_RC_SENSITIVE                 ((TPM2_RC) (TPM2_RC_VER1 + 0x055))
#define TPM2_RC_MAX_FM0                  ((TPM2_RC) (TPM2_RC_VER1 + 0x07F))
#define TPM2_RC_FMT1                     ((TPM2_RC) 0x080)
#define TPM2_RC_ASYMMETRIC               ((TPM2_RC) (TPM2_RC_FMT1 + 0x001))
#define TPM2_RC_ATTRIBUTES                ((TPM2_RC) (TPM2_RC_FMT1 + 0x002))
#define TPM2_RC_HASH                      ((TPM2_RC) (TPM2_RC_FMT1 + 0x003))
#define TPM2_RC_VALUE                     ((TPM2_RC) (TPM2_RC_FMT1 + 0x004))
#define TPM2_RC_HIERARCHY                 ((TPM2_RC) (TPM2_RC_FMT1 + 0x005))
#define TPM2_RC_KEY_SIZE                  ((TPM2_RC) (TPM2_RC_FMT1 + 0x007))
#define TPM2_RC_MGF                       ((TPM2_RC) (TPM2_RC_FMT1 + 0x008))
#define TPM2_RC_MODE                      ((TPM2_RC) (TPM2_RC_FMT1 + 0x009))
#define TPM2_RC_TYPE                      ((TPM2_RC) (TPM2_RC_FMT1 + 0x00A))
#define TPM2_RC_HANDLE                    ((TPM2_RC) (TPM2_RC_FMT1 + 0x00B))
#define TPM2_RC_KDF                       ((TPM2_RC) (TPM2_RC_FMT1 + 0x00C))
#define TPM2_RC_RANGE                     ((TPM2_RC) (TPM2_RC_FMT1 + 0x00D))
#define TPM2_RC_AUTH_FAIL                 ((TPM2_RC) (TPM2_RC_FMT1 + 0x00E))
#define TPM2_RC_NONCE                     ((TPM2_RC) (TPM2_RC_FMT1 + 0x00F))
#define TPM2_RC_PP                        ((TPM2_RC) (TPM2_RC_FMT1 + 0x010))
#define TPM2_RC_SCHEME                     ((TPM2_RC) (TPM2_RC_FMT1 + 0x012))
#define TPM2_RC_SIZE                      ((TPM2_RC) (TPM2_RC_FMT1 + 0x015))
#define TPM2_RC_SYMMETRIC                 ((TPM2_RC) (TPM2_RC_FMT1 + 0x016))
#define TPM2_RC_TAG                       ((TPM2_RC) (TPM2_RC_FMT1 + 0x017))

```

```

#define TPM2_RC_SELECTOR ((TPM2_RC) (TPM2_RC_FMT1 + 0x018))
#define TPM2_RC_INSUFFICIENT ((TPM2_RC) (TPM2_RC_FMT1 + 0x01A))
#define TPM2_RC_SIGNATURE ((TPM2_RC) (TPM2_RC_FMT1 + 0x01B))
#define TPM2_RC_KEY ((TPM2_RC) (TPM2_RC_FMT1 + 0x01C))
#define TPM2_RC_POLICY_FAIL ((TPM2_RC) (TPM2_RC_FMT1 + 0x01D))
#define TPM2_RC_INTEGRITY ((TPM2_RC) (TPM2_RC_FMT1 + 0x01F))
#define TPM2_RC_TICKET ((TPM2_RC) (TPM2_RC_FMT1 + 0x020))
#define TPM2_RC_RESERVED_BITS ((TPM2_RC) (TPM2_RC_FMT1 + 0x021))
#define TPM2_RC_BAD_AUTH ((TPM2_RC) (TPM2_RC_FMT1 + 0x022))
#define TPM2_RC_EXPIRED ((TPM2_RC) (TPM2_RC_FMT1 + 0x023))
#define TPM2_RC_POLICY_CC ((TPM2_RC) (TPM2_RC_FMT1 + 0x024))
#define TPM2_RC_BINDING ((TPM2_RC) (TPM2_RC_FMT1 + 0x025))
#define TPM2_RC_CURVE ((TPM2_RC) (TPM2_RC_FMT1 + 0x026))
#define TPM2_RC_ECC_POINT ((TPM2_RC) (TPM2_RC_FMT1 + 0x027))
#define TPM2_RC_WARN ((TPM2_RC) 0x900)
#define TPM2_RC_CONTEXT_GAP ((TPM2_RC) (TPM2_RC_WARN + 0x001))
#define TPM2_RC_OBJECT_MEMORY ((TPM2_RC) (TPM2_RC_WARN + 0x002))
#define TPM2_RC_SESSION_MEMORY ((TPM2_RC) (TPM2_RC_WARN + 0x003))
#define TPM2_RC_MEMORY ((TPM2_RC) (TPM2_RC_WARN + 0x004))
#define TPM2_RC_SESSION_HANDLES ((TPM2_RC) (TPM2_RC_WARN + 0x005))
#define TPM2_RC_OBJECT_HANDLES ((TPM2_RC) (TPM2_RC_WARN + 0x006))
#define TPM2_RC_LOCALITY ((TPM2_RC) (TPM2_RC_WARN + 0x007))
#define TPM2_RC_YIELDED ((TPM2_RC) (TPM2_RC_WARN + 0x008))
#define TPM2_RC_CANCELED ((TPM2_RC) (TPM2_RC_WARN + 0x009))
#define TPM2_RC_TESTING ((TPM2_RC) (TPM2_RC_WARN + 0x00A))
#define TPM2_RC_REFERENCE_H0 ((TPM2_RC) (TPM2_RC_WARN + 0x010))
#define TPM2_RC_REFERENCE_H1 ((TPM2_RC) (TPM2_RC_WARN + 0x011))
#define TPM2_RC_REFERENCE_H2 ((TPM2_RC) (TPM2_RC_WARN + 0x012))
#define TPM2_RC_REFERENCE_H3 ((TPM2_RC) (TPM2_RC_WARN + 0x013))
#define TPM2_RC_REFERENCE_H4 ((TPM2_RC) (TPM2_RC_WARN + 0x014))
#define TPM2_RC_REFERENCE_H5 ((TPM2_RC) (TPM2_RC_WARN + 0x015))
#define TPM2_RC_REFERENCE_H6 ((TPM2_RC) (TPM2_RC_WARN + 0x016))
#define TPM2_RC_REFERENCE_S0 ((TPM2_RC) (TPM2_RC_WARN + 0x018))
#define TPM2_RC_REFERENCE_S1 ((TPM2_RC) (TPM2_RC_WARN + 0x019))
#define TPM2_RC_REFERENCE_S2 ((TPM2_RC) (TPM2_RC_WARN + 0x01A))
#define TPM2_RC_REFERENCE_S3 ((TPM2_RC) (TPM2_RC_WARN + 0x01B))
#define TPM2_RC_REFERENCE_S4 ((TPM2_RC) (TPM2_RC_WARN + 0x01C))
#define TPM2_RC_REFERENCE_S5 ((TPM2_RC) (TPM2_RC_WARN + 0x01D))
#define TPM2_RC_REFERENCE_S6 ((TPM2_RC) (TPM2_RC_WARN + 0x01E))
#define TPM2_RC_NV_RATE ((TPM2_RC) (TPM2_RC_WARN + 0x020))
#define TPM2_RC_LOCKOUT ((TPM2_RC) (TPM2_RC_WARN + 0x021))
#define TPM2_RC_RETRY ((TPM2_RC) (TPM2_RC_WARN + 0x022))
#define TPM2_RC_NV_UNAVAILABLE ((TPM2_RC) (TPM2_RC_WARN + 0x023))
#define TPM2_RC_NOT_USED ((TPM2_RC) (TPM2_RC_WARN + 0x7F))
#define TPM2_RC_H ((TPM2_RC) 0x000)
#define TPM2_RC_P ((TPM2_RC) 0x040)
#define TPM2_RC_S ((TPM2_RC) 0x800)
#define TPM2_RC_1 ((TPM2_RC) 0x100)
#define TPM2_RC_2 ((TPM2_RC) 0x200)
#define TPM2_RC_3 ((TPM2_RC) 0x300)
#define TPM2_RC_4 ((TPM2_RC) 0x400)
#define TPM2_RC_5 ((TPM2_RC) 0x500)
#define TPM2_RC_6 ((TPM2_RC) 0x600)
#define TPM2_RC_7 ((TPM2_RC) 0x700)
#define TPM2_RC_8 ((TPM2_RC) 0x800)
#define TPM2_RC_9 ((TPM2_RC) 0x900)
#define TPM2_RC_A ((TPM2_RC) 0xA00)

```

```

#define TPM2_RC_B                ((TPM2_RC) 0xB00)
#define TPM2_RC_C                ((TPM2_RC) 0xC00)
#define TPM2_RC_D                ((TPM2_RC) 0xD00)
#define TPM2_RC_E                ((TPM2_RC) 0xE00)
#define TPM2_RC_F                ((TPM2_RC) 0xF00)
#define TPM2_RC_N_MASK          ((TPM2_RC) 0xF00)

/* Table 17 - Definition of (INT8) TPM2_CLOCK_ADJUST Constants */
typedef INT8 TPM2_CLOCK_ADJUST;
#define TPM2_CLOCK_COARSE_SLOWER ((TPM2_CLOCK_ADJUST) -3)
#define TPM2_CLOCK_MEDIUM_SLOWER ((TPM2_CLOCK_ADJUST) -2)
#define TPM2_CLOCK_FINE_SLOWER   ((TPM2_CLOCK_ADJUST) -1)
#define TPM2_CLOCK_NO_CHANGE     ((TPM2_CLOCK_ADJUST) 0)
#define TPM2_CLOCK_FINE_FASTER   ((TPM2_CLOCK_ADJUST) 1)
#define TPM2_CLOCK_MEDIUM_FASTER ((TPM2_CLOCK_ADJUST) 2)
#define TPM2_CLOCK_COARSE_FASTER ((TPM2_CLOCK_ADJUST) 3)

/* Table 18 - Definition of (UINT16) TPM2_EO Constants */
typedef UINT16 TPM2_EO;
#define TPM2_EO_EQ                ((TPM2_EO) 0x0000)
#define TPM2_EO_NEQ              ((TPM2_EO) 0x0001)
#define TPM2_EO_SIGNED_GT        ((TPM2_EO) 0x0002)
#define TPM2_EO_UNSIGNED_GT      ((TPM2_EO) 0x0003)
#define TPM2_EO_SIGNED_LT        ((TPM2_EO) 0x0004)
#define TPM2_EO_UNSIGNED_LT      ((TPM2_EO) 0x0005)
#define TPM2_EO_SIGNED_GE        ((TPM2_EO) 0x0006)
#define TPM2_EO_UNSIGNED_GE      ((TPM2_EO) 0x0007)
#define TPM2_EO_SIGNED_LE        ((TPM2_EO) 0x0008)
#define TPM2_EO_UNSIGNED_LE      ((TPM2_EO) 0x0009)
#define TPM2_EO_BITSET           ((TPM2_EO) 0x000A)
#define TPM2_EO_BITCLEAR         ((TPM2_EO) 0x000B)

/* Table 19 - Definition of (UINT16) TPM2_ST Constants */
typedef UINT16 TPM2_ST;
#define TPM2_ST_RSP_COMMAND      ((TPM2_ST) 0x00C4)
#define TPM2_ST_NULL             ((TPM2_ST) 0x8000)
#define TPM2_ST_NO_SESSIONS     ((TPM2_ST) 0x8001)
#define TPM2_ST_SESSIONS        ((TPM2_ST) 0x8002)
#define TPM2_ST_ATTEST_NV        ((TPM2_ST) 0x8014)
#define TPM2_ST_ATTEST_COMMAND_AUDIT ((TPM2_ST) 0x8015)
#define TPM2_ST_ATTEST_SESSION_AUDIT ((TPM2_ST) 0x8016)
#define TPM2_ST_ATTEST_CERTIFY   ((TPM2_ST) 0x8017)
#define TPM2_ST_ATTEST_QUOTE     ((TPM2_ST) 0x8018)
#define TPM2_ST_ATTEST_TIME      ((TPM2_ST) 0x8019)
#define TPM2_ST_ATTEST_CREATION  ((TPM2_ST) 0x801A)
#define TPM2_ST_CREATION         ((TPM2_ST) 0x8021)
#define TPM2_ST_VERIFIED         ((TPM2_ST) 0x8022)
#define TPM2_ST_AUTH_SECRET      ((TPM2_ST) 0x8023)
#define TPM2_ST_HASHCHECK        ((TPM2_ST) 0x8024)
#define TPM2_ST_AUTH_SIGNED      ((TPM2_ST) 0x8025)
#define TPM2_ST_FU_MANIFEST      ((TPM2_ST) 0x8029)

/* Table 20 - Definition of (UINT16) TPM2_SU Constants */
typedef UINT16 TPM2_SU;
#define TPM2_SU_CLEAR            ((TPM2_SU) 0x0000)
#define TPM2_SU_STATE           ((TPM2_SU) 0x0001)

```

```

/* Table 21 - Definition of (UINT8) TPM2_SE Constants */
typedef UINT8 TPM2_SE;
#define TPM2_SE_HMAC      ((TPM2_SE) 0x00)
#define TPM2_SE_POLICY    ((TPM2_SE) 0x01)
#define TPM2_SE_TRIAL     ((TPM2_SE) 0x03)

/* Table 22 - Definition of (UINT32) TPM2_CAP Constants */
typedef UINT32 TPM2_CAP;
#define TPM2_CAP_FIRST      ((TPM2_CAP) 0x00000000)
#define TPM2_CAP_ALGS      ((TPM2_CAP) 0x00000000)
#define TPM2_CAP_HANDLES    ((TPM2_CAP) 0x00000001)
#define TPM2_CAP_COMMANDS   ((TPM2_CAP) 0x00000002)
#define TPM2_CAP_PP_COMMANDS ((TPM2_CAP) 0x00000003)
#define TPM2_CAP_AUDIT_COMMANDS ((TPM2_CAP) 0x00000004)
#define TPM2_CAP_PCERS      ((TPM2_CAP) 0x00000005)
#define TPM2_CAP_TPM_PROPERTIES ((TPM2_CAP) 0x00000006)
#define TPM2_CAP_PCR_PROPERTIES ((TPM2_CAP) 0x00000007)
#define TPM2_CAP_ECC_CURVES ((TPM2_CAP) 0x00000008)
#define TPM2_CAP_AUTH_POLICIES ((TPM2_CAP) 0x00000009)
#define TPM2_CAP_LAST       ((TPM2_CAP) 0x00000009)
#define TPM2_CAP_VENDOR_PROPERTY ((TPM2_CAP) 0x00000100)

/* Table 23 - Definition of (UINT32) TPM2_PT Constants */
typedef UINT32 TPM2_PT;
#define TPM2_PT_NONE        ((TPM2_PT) 0x00000000)
#define TPM2_PT_GROUP       ((TPM2_PT) 0x00000100)
#define TPM2_PT_FIXED       ((TPM2_PT) (TPM2_PT_GROUP * 1))
#define TPM2_PT_FAMILY_INDICATOR ((TPM2_PT) (TPM2_PT_FIXED + 0))
#define TPM2_PT_LEVEL       ((TPM2_PT) (TPM2_PT_FIXED + 1))
#define TPM2_PT_REVISION    ((TPM2_PT) (TPM2_PT_FIXED + 2))
#define TPM2_PT_DAY_OF_YEAR ((TPM2_PT) (TPM2_PT_FIXED + 3))
#define TPM2_PT_YEAR        ((TPM2_PT) (TPM2_PT_FIXED + 4))
#define TPM2_PT_MANUFACTURER ((TPM2_PT) (TPM2_PT_FIXED + 5))
#define TPM2_PT_VENDOR_STRING_1 ((TPM2_PT) (TPM2_PT_FIXED + 6))
#define TPM2_PT_VENDOR_STRING_2 ((TPM2_PT) (TPM2_PT_FIXED + 7))
#define TPM2_PT_VENDOR_STRING_3 ((TPM2_PT) (TPM2_PT_FIXED + 8))
#define TPM2_PT_VENDOR_STRING_4 ((TPM2_PT) (TPM2_PT_FIXED + 9))
#define TPM2_PT_VENDOR_TPM_TYPE ((TPM2_PT) (TPM2_PT_FIXED +
10))
#define TPM2_PT_FIRMWARE_VERSION_1 ((TPM2_PT) (TPM2_PT_FIXED +
11))
#define TPM2_PT_FIRMWARE_VERSION_2 ((TPM2_PT) (TPM2_PT_FIXED +
12))
#define TPM2_PT_INPUT_BUFFER ((TPM2_PT) (TPM2_PT_FIXED +
13))
#define TPM2_PT_HR_TRANSIENT_MIN ((TPM2_PT) (TPM2_PT_FIXED +
14))
#define TPM2_PT_HR_PERSISTENT_MIN ((TPM2_PT) (TPM2_PT_FIXED +
15))
#define TPM2_PT_HR_LOADED_MIN ((TPM2_PT) (TPM2_PT_FIXED +
16))
#define TPM2_PT_ACTIVE_SESSIONS_MAX ((TPM2_PT) (TPM2_PT_FIXED +
17))
#define TPM2_PT_PCR_COUNT ((TPM2_PT) (TPM2_PT_FIXED +
18))
#define TPM2_PT_PCR_SELECT_MIN ((TPM2_PT) (TPM2_PT_FIXED +
19))

```

```

#define TPM2_PT_CONTEXT_GAP_MAX          ((TPM2_PT_FIXED + 20))
#define TPM2_PT_NV_COUNTERS_MAX         ((TPM2_PT_FIXED + 22))
#define TPM2_PT_NV_INDEX_MAX           ((TPM2_PT_FIXED + 23))
#define TPM2_PT_MEMORY                 ((TPM2_PT_FIXED + 24))
#define TPM2_PT_CLOCK_UPDATE           ((TPM2_PT_FIXED + 25))
#define TPM2_PT_CONTEXT_HASH           ((TPM2_PT_FIXED + 26))
#define TPM2_PT_CONTEXT_SYM            ((TPM2_PT_FIXED + 27))
#define TPM2_PT_CONTEXT_SYM_SIZE      ((TPM2_PT_FIXED + 28))
#define TPM2_PT_ORDERLY_COUNT         ((TPM2_PT_FIXED + 29))
#define TPM2_PT_MAX_COMMAND_SIZE      ((TPM2_PT_FIXED + 30))
#define TPM2_PT_MAX_RESPONSE_SIZE     ((TPM2_PT_FIXED + 31))
#define TPM2_PT_MAX_DIGEST            ((TPM2_PT_FIXED + 32))
#define TPM2_PT_MAX_OBJECT_CONTEXT    ((TPM2_PT_FIXED + 33))
#define TPM2_PT_MAX_SESSION_CONTEXT   ((TPM2_PT_FIXED + 34))
#define TPM2_PT_PS_FAMILY_INDICATOR   ((TPM2_PT_FIXED + 35))
#define TPM2_PT_PS_LEVEL              ((TPM2_PT_FIXED + 36))
#define TPM2_PT_PS_REVISION           ((TPM2_PT_FIXED + 37))
#define TPM2_PT_PS_DAY_OF_YEAR        ((TPM2_PT_FIXED + 38))
#define TPM2_PT_PS_YEAR               ((TPM2_PT_FIXED + 39))
#define TPM2_PT_SPLIT_MAX             ((TPM2_PT_FIXED + 40))
#define TPM2_PT_TOTAL_COMMANDS        ((TPM2_PT_FIXED + 41))
#define TPM2_PT_LIBRARY_COMMANDS     ((TPM2_PT_FIXED + 42))
#define TPM2_PT_VENDOR_COMMANDS      ((TPM2_PT_FIXED + 43))
#define TPM2_PT_NV_BUFFER_MAX         ((TPM2_PT_FIXED + 44))
#define TPM2_PT_MODES                 ((TPM2_PT_FIXED + 45))
#define TPM2_PT_MAX_CAP_BUFFER        ((TPM2_PT_FIXED + 46))
#define TPM2_PT_VAR                   ((TPM2_PT_GROUP * 2))
#define TPM2_PT_PERMANENT              ((TPM2_PT_VAR + 0))
#define TPM2_PT_STARTUP_CLEAR          ((TPM2_PT_VAR + 1))
#define TPM2_PT_HR_NV_INDEX           ((TPM2_PT_VAR + 2))
#define TPM2_PT_HR_LOADED              ((TPM2_PT_VAR + 3))

```

```

#define TPM2_PT_HR_LOADED_AVAIL      ((TPM2_PT) (TPM2_PT_VAR + 4))
#define TPM2_PT_HR_ACTIVE            ((TPM2_PT) (TPM2_PT_VAR + 5))
#define TPM2_PT_HR_ACTIVE_AVAIL     ((TPM2_PT) (TPM2_PT_VAR + 6))
#define TPM2_PT_HR_TRANSIENT_AVAIL  ((TPM2_PT) (TPM2_PT_VAR + 7))
#define TPM2_PT_HR_PERSISTENT       ((TPM2_PT) (TPM2_PT_VAR + 8))
#define TPM2_PT_HR_PERSISTENT_AVAIL ((TPM2_PT) (TPM2_PT_VAR + 9))
#define TPM2_PT_NV_COUNTERS         ((TPM2_PT) (TPM2_PT_VAR + 10))
#define TPM2_PT_NV_COUNTERS_AVAIL   ((TPM2_PT) (TPM2_PT_VAR + 11))
#define TPM2_PT_ALGORITHM_SET       ((TPM2_PT) (TPM2_PT_VAR + 12))
#define TPM2_PT_LOADED_CURVES       ((TPM2_PT) (TPM2_PT_VAR + 13))
#define TPM2_PT_LOCKOUT_COUNTER     ((TPM2_PT) (TPM2_PT_VAR + 14))
#define TPM2_PT_MAX_AUTH_FAIL       ((TPM2_PT) (TPM2_PT_VAR + 15))
#define TPM2_PT_LOCKOUT_INTERVAL    ((TPM2_PT) (TPM2_PT_VAR + 16))
#define TPM2_PT_LOCKOUT_RECOVERY    ((TPM2_PT) (TPM2_PT_VAR + 17))
#define TPM2_PT_NV_WRITE_RECOVERY   ((TPM2_PT) (TPM2_PT_VAR + 18))
#define TPM2_PT_AUDIT_COUNTER_0     ((TPM2_PT) (TPM2_PT_VAR + 19))
#define TPM2_PT_AUDIT_COUNTER_1     ((TPM2_PT) (TPM2_PT_VAR + 20))

```

/\* Table 24 - Definition of (UINT32) TPM2\_PT\_PCR Constants \*/

```

typedef UINT32 TPM2_PT_PCR;
#define TPM2_PT_PCR_FIRST           ((TPM2_PT_PCR) 0x00000000)
#define TPM2_PT_PCR_SAVE           ((TPM2_PT_PCR) 0x00000000)
#define TPM2_PT_PCR_EXTEND_L0      ((TPM2_PT_PCR) 0x00000001)
#define TPM2_PT_PCR_RESET_L0      ((TPM2_PT_PCR) 0x00000002)
#define TPM2_PT_PCR_EXTEND_L1     ((TPM2_PT_PCR) 0x00000003)
#define TPM2_PT_PCR_RESET_L1     ((TPM2_PT_PCR) 0x00000004)
#define TPM2_PT_PCR_EXTEND_L2     ((TPM2_PT_PCR) 0x00000005)
#define TPM2_PT_PCR_RESET_L2     ((TPM2_PT_PCR) 0x00000006)
#define TPM2_PT_PCR_EXTEND_L3     ((TPM2_PT_PCR) 0x00000007)
#define TPM2_PT_PCR_RESET_L3     ((TPM2_PT_PCR) 0x00000008)
#define TPM2_PT_PCR_EXTEND_L4     ((TPM2_PT_PCR) 0x00000009)
#define TPM2_PT_PCR_RESET_L4     ((TPM2_PT_PCR) 0x0000000A)
#define TPM2_PT_PCR_NO_INCREMENT  ((TPM2_PT_PCR) 0x00000011)
#define TPM2_PT_PCR_DRTM_RESET    ((TPM2_PT_PCR) 0x00000012)
#define TPM2_PT_PCR_POLICY        ((TPM2_PT_PCR) 0x00000013)
#define TPM2_PT_PCR_AUTH          ((TPM2_PT_PCR) 0x00000014)
#define TPM2_PT_PCR_LAST          ((TPM2_PT_PCR) 0x00000014)

```

/\* Table 25 - Definition of (UINT32) TPM2\_PS Constants \*/

```

typedef UINT32 TPM2_PS;
#define TPM2_PS_MAIN               ((TPM2_PS) 0x00000000)
#define TPM2_PS_PC                 ((TPM2_PS) 0x00000001)
#define TPM2_PS_PDA                ((TPM2_PS) 0x00000002)
#define TPM2_PS_CELL_PHONE        ((TPM2_PS) 0x00000003)
#define TPM2_PS_SERVER             ((TPM2_PS) 0x00000004)
#define TPM2_PS_PERIPHERAL        ((TPM2_PS) 0x00000005)
#define TPM2_PS_TSS                ((TPM2_PS) 0x00000006)
#define TPM2_PS_STORAGE            ((TPM2_PS) 0x00000007)
#define TPM2_PS_AUTHENTICATION     ((TPM2_PS) 0x00000008)
#define TPM2_PS_EMBEDDED           ((TPM2_PS) 0x00000009)
#define TPM2_PS_HARDCOPY           ((TPM2_PS) 0x0000000A)
#define TPM2_PS_INFRASTRUCTURE    ((TPM2_PS) 0x0000000B)
#define TPM2_PS_VIRTUALIZATION    ((TPM2_PS) 0x0000000C)
#define TPM2_PS_TNC                ((TPM2_PS) 0x0000000D)
#define TPM2_PS_MULTI_TENANT      ((TPM2_PS) 0x0000000E)
#define TPM2_PS_TC                 ((TPM2_PS) 0x0000000F)

```

```

/* Table 26 - Definition of Types for Handles */
typedef UINT32 TPM2_HANDLE;

/* Table 27 - Definition of (UINT8) TPM2_HT Constants */
typedef UINT8 TPM2_HT;
#define TPM2_HT_PCR (TPM2_HT) 0x00)
#define TPM2_HT_NV_INDEX (TPM2_HT) 0x01)
#define TPM2_HT_HMAC_SESSION (TPM2_HT) 0x02)
#define TPM2_HT_LOADED_SESSION (TPM2_HT) 0x02)
#define TPM2_HT_POLICY_SESSION (TPM2_HT) 0x03)
#define TPM2_HT_SAVED_SESSION (TPM2_HT) 0x03)
#define TPM2_HT_PERMANENT (TPM2_HT) 0x40)
#define TPM2_HT_TRANSIENT (TPM2_HT) 0x80)
#define TPM2_HT_PERSISTENT (TPM2_HT) 0x81)

/* Table 28 - Definition of (TPM2_HANDLE) TPM2_RH Constants */
typedef TPM2_HANDLE TPM2_RH;
#define TPM2_RH_FIRST (TPM2_RH) 0x40000000)
#define TPM2_RH_SRK (TPM2_RH) 0x40000000)
#define TPM2_RH_OWNER (TPM2_RH) 0x40000001)
#define TPM2_RH_REVOKE (TPM2_RH) 0x40000002)
#define TPM2_RH_TRANSPORT (TPM2_RH) 0x40000003)
#define TPM2_RH_OPERATOR (TPM2_RH) 0x40000004)
#define TPM2_RH_ADMIN (TPM2_RH) 0x40000005)
#define TPM2_RH_EK (TPM2_RH) 0x40000006)
#define TPM2_RH_NULL (TPM2_RH) 0x40000007)
#define TPM2_RH_UNASSIGNED (TPM2_RH) 0x40000008)
#define TPM2_RS_PW (TPM2_RH) 0x40000009)
#define TPM2_RH_LOCKOUT (TPM2_RH) 0x4000000A)
#define TPM2_RH_ENDORSEMENT (TPM2_RH) 0x4000000B)
#define TPM2_RH_PLATFORM (TPM2_RH) 0x4000000C)
#define TPM2_RH_PLATFORM_NV (TPM2_RH) 0x4000000D)
#define TPM2_RH_AUTH_00 (TPM2_RH) 0x40000010)
#define TPM2_RH_AUTH_FF (TPM2_RH) 0x4000010F)
#define TPM2_RH_LAST (TPM2_RH) 0x4000010F)

/* Table 29 - Definition of (TPM2_HANDLE) TPM2_HC Constants */
typedef TPM2_HANDLE TPM2_HC;
#define TPM2_HR_HANDLE_MASK (TPM2_HC) 0x00FFFFFF)
#define TPM2_HR_RANGE_MASK (TPM2_HC) 0xFF000000)
#define TPM2_HR_SHIFT (TPM2_HC) 24)
#define TPM2_HR_PCR (TPM2_HC) (TPM2_HT_PCR <<
TPM2_HR_SHIFT))
#define TPM2_HR_HMAC_SESSION (TPM2_HC) (TPM2_HT_HMAC_SESSION <<
TPM2_HR_SHIFT))
#define TPM2_HR_POLICY_SESSION (TPM2_HC) (TPM2_HT_POLICY_SESSION
<< TPM2_HR_SHIFT))
#define TPM2_HR_TRANSIENT (TPM2_HC) (TPM2_HT_TRANSIENT <<
TPM2_HR_SHIFT))
#define TPM2_HR_PERSISTENT (TPM2_HC) (TPM2_HT_PERSISTENT <<
TPM2_HR_SHIFT))
#define TPM2_HR_NV_INDEX (TPM2_HC) (TPM2_HT_NV_INDEX <<
TPM2_HR_SHIFT))
#define TPM2_HR_PERMANENT (TPM2_HC) (TPM2_HT_PERMANENT <<
TPM2_HR_SHIFT))
#define TPM2_PCR_FIRST (TPM2_HC) (TPM2_HR_PCR + 0))

```

```

#define TPM2_PCR_LAST                ((TPM2_HC) (TPM2_PCR_FIRST +
TPM2_MAX_PCERS-1))
#define TPM2_HMAC_SESSION_FIRST      ((TPM2_HC) (TPM2_HR_HMAC_SESSION+
0))
#define TPM2_HMAC_SESSION_LAST       ((TPM2_HC)
(TPM2_HMAC_SESSION_FIRST+0x00ffffffe))
#define TPM2_LOADED_SESSION_FIRST    ((TPM2_HC) TPM2_HMAC_SESSION_FIRST)
#define TPM2_LOADED_SESSION_LAST     ((TPM2_HC) TPM2_HMAC_SESSION_LAST)
#define TPM2_POLICY_SESSION_FIRST    ((TPM2_HC) (TPM2_HR_POLICY_SESSION+
0))
#define TPM2_POLICY_SESSION_LAST     ((TPM2_HC)
(TPM2_POLICY_SESSION_FIRST + 0x00ffffffe))
#define TPM2_TRANSIENT_FIRST         ((TPM2_HC) (TPM2_HR_TRANSIENT +0))
#define TPM2_ACTIVE_SESSION_FIRST    ((TPM2_HC)
TPM2_POLICY_SESSION_FIRST)
#define TPM2_ACTIVE_SESSION_LAST     ((TPM2_HC)
TPM2_POLICY_SESSION_LAST)
#define TPM2_TRANSIENT_LAST          ((TPM2_HC)
(TPM2_TRANSIENT_FIRST+0x00ffffffe))
#define TPM2_PERSISTENT_FIRST        ((TPM2_HC) (TPM2_HR_PERSISTENT+0))
#define TPM2_PERSISTENT_LAST         ((TPM2_HC)
(TPM2_PERSISTENT_FIRST+0x00FFFFFF))
#define TPM2_PLATFORM_PERSISTENT     ((TPM2_HC) (TPM2_PERSISTENT_FIRST +
0x00800000))
#define TPM2_NV_INDEX_FIRST          ((TPM2_HC) (TPM2_HR_NV_INDEX + 0))
#define TPM2_NV_INDEX_LAST           ((TPM2_HC) (TPM2_NV_INDEX_FIRST +
0x00FFFFFF))
#define TPM2_PERMANENT_FIRST          ((TPM2_HC) TPM2_RH_FIRST)
#define TPM2_PERMANENT_LAST          ((TPM2_HC) TPM2_RH_LAST)

/* Table 30 - Definition of (UINT32) TPMA_ALGORITHM Bits */
typedef UINT32 TPMA_ALGORITHM;
#define TPMA_ALGORITHM_ASYMMETRIC    ((TPMA_ALGORITHM) 0x00000001)
#define TPMA_ALGORITHM_SYMMETRIC    ((TPMA_ALGORITHM) 0x00000002)
#define TPMA_ALGORITHM_HASH          ((TPMA_ALGORITHM) 0x00000004)
#define TPMA_ALGORITHM_OBJECT        ((TPMA_ALGORITHM) 0x00000008)
#define TPMA_ALGORITHM_RESERVED1_MASK ((TPMA_ALGORITHM) 0x000000f0)
#define TPMA_ALGORITHM_SIGNING       ((TPMA_ALGORITHM) 0x00000100)
#define TPMA_ALGORITHM_ENCRYPTING     ((TPMA_ALGORITHM) 0x00000200)
#define TPMA_ALGORITHM_METHOD        ((TPMA_ALGORITHM) 0x00000400)
#define TPMA_ALGORITHM_RESERVED2_MASK ((TPMA_ALGORITHM) 0xffff800)

/* Table 31 - Definition of (UINT32) TPMA_OBJECT Bits */
typedef UINT32 TPMA_OBJECT;
#define TPMA_OBJECT_RESERVED1_MASK   ((TPMA_OBJECT)
0x00000001)
#define TPMA_OBJECT_FIXEDTPM        ((TPMA_OBJECT)
0x00000002)
#define TPMA_OBJECT_STCLEAR          ((TPMA_OBJECT)
0x00000004)
#define TPMA_OBJECT_RESERVED2_MASK   ((TPMA_OBJECT)
0x00000008)
#define TPMA_OBJECT_FIXEDPARENT      ((TPMA_OBJECT)
0x00000010)
#define TPMA_OBJECT_SENSITIVEDATAORIGIN ((TPMA_OBJECT)
0x00000020)

```



```

#define TPMA_OBJECT_USERWITHAUTH          ((TPMA_OBJECT)
0x00000040)
#define TPMA_OBJECT_ADMINWITHPOLICY      ((TPMA_OBJECT)
0x00000080)
#define TPMA_OBJECT_RESERVED3_MASK      ((TPMA_OBJECT)
0x00000300)
#define TPMA_OBJECT_NODA                  ((TPMA_OBJECT)
0x00000400)
#define TPMA_OBJECT_ENCRYPTEDDUPLICATION  ((TPMA_OBJECT)
0x00000800)
#define TPMA_OBJECT_RESERVED4_MASK      ((TPMA_OBJECT)
0x0000f000)
#define TPMA_OBJECT_RESTRICTED           ((TPMA_OBJECT)
0x00010000)
#define TPMA_OBJECT_DECRYPT               ((TPMA_OBJECT)
0x00020000)
#define TPMA_OBJECT_SIGN_ENCRYPT         ((TPMA_OBJECT)
0x00040000)
#define TPMA_OBJECT_RESERVED5_MASK      ((TPMA_OBJECT)
0xfff80000)

/* Table 32 - Definition of (UINT8) TPMA_SESSION Bits */
typedef UINT8 TPMA_SESSION;
#define TPMA_SESSION_CONTINUESESSION    ((TPMA_SESSION) 0x01)
#define TPMA_SESSION_AUDITEXCLUSIVE     ((TPMA_SESSION) 0x02)
#define TPMA_SESSION_AUDITRESET        ((TPMA_SESSION) 0x04)
#define TPMA_SESSION_RESERVED1_MASK    ((TPMA_SESSION) 0x18)
#define TPMA_SESSION_DECRYPT            ((TPMA_SESSION) 0x20)
#define TPMA_SESSION_ENCRYPT            ((TPMA_SESSION) 0x40)
#define TPMA_SESSION_AUDIT             ((TPMA_SESSION) 0x80)

/* Table 33 - Definition of (UINT8) TPMA_LOCALITY Bits */
typedef UINT8 TPMA_LOCALITY;
#define TPMA_LOCALITY_TPM2_LOC_ZERO     ((TPMA_LOCALITY) 0x01)
#define TPMA_LOCALITY_TPM2_LOC_ONE      ((TPMA_LOCALITY) 0x02)
#define TPMA_LOCALITY_TPM2_LOC_TWO      ((TPMA_LOCALITY) 0x04)
#define TPMA_LOCALITY_TPM2_LOC_THREE    ((TPMA_LOCALITY) 0x08)
#define TPMA_LOCALITY_TPM2_LOC_FOUR     ((TPMA_LOCALITY) 0x10)
#define TPMA_LOCALITY_EXTENDED_MASK     ((TPMA_LOCALITY) 0xe0)
#define TPMA_LOCALITY_EXTENDED_SHIFT    (5)

/* Table 34 - Definition of (UINT32) TPMA_PERMANENT Bits */
typedef UINT32 TPMA_PERMANENT;
#define TPMA_PERMANENT_OWNERAUTHSET     ((TPMA_PERMANENT)
0x00000001)
#define TPMA_PERMANENT_ENDORSEMENTAUTHSET ((TPMA_PERMANENT)
0x00000002)
#define TPMA_PERMANENT_LOCKOUTAUTHSET   ((TPMA_PERMANENT)
0x00000004)
#define TPMA_PERMANENT_RESERVED1_MASK   ((TPMA_PERMANENT)
0x000000f8)
#define TPMA_PERMANENT_DISABLECLEAR     ((TPMA_PERMANENT)
0x00000100)
#define TPMA_PERMANENT_INLOCKOUT        ((TPMA_PERMANENT)
0x00000200)
#define TPMA_PERMANENT_TPMGENERATEDEPS  ((TPMA_PERMANENT)
0x00000400)

```

```

#define TPMA_PERMANENT_RESERVED2_MASK      ((TPMA_PERMANENT)
0xfffff800)

/* Table 35 - Definition of (UINT32) TPMA_STARTUP_CLEAR Bits */
typedef UINT32 TPMA_STARTUP_CLEAR;
#define TPMA_STARTUP_CLEAR_PHENABLE      ((TPMA_STARTUP_CLEAR)
0x00000001)
#define TPMA_STARTUP_CLEAR_SHENABLE      ((TPMA_STARTUP_CLEAR)
0x00000002)
#define TPMA_STARTUP_CLEAR_EHENABLE      ((TPMA_STARTUP_CLEAR)
0x00000004)
#define TPMA_STARTUP_CLEAR_PHENABLENV    ((TPMA_STARTUP_CLEAR)
0x00000008)
#define TPMA_STARTUP_CLEAR_RESERVED1_MASK ((TPMA_STARTUP_CLEAR)
0x7fffffff0)
#define TPMA_STARTUP_CLEAR_ORDERLY      ((TPMA_STARTUP_CLEAR)
0x80000000)

/* Table 36 - Definition of (UINT32) TPMA_MEMORY Bits */
typedef UINT32 TPMA_MEMORY;
#define TPMA_MEMORY_SHAREDGRAM           ((TPMA_MEMORY) 0x00000001)
#define TPMA_MEMORY_SHAREDNV            ((TPMA_MEMORY) 0x00000002)
#define TPMA_MEMORY_OBJECTCOPIEDTORAM   ((TPMA_MEMORY) 0x00000004)
#define TPMA_MEMORY_RESERVED1_MASK      ((TPMA_MEMORY) 0xffffffff8)

/* Table 37 - Definition of (TPM2_CC) TPMA_CC Bits */
typedef TPM2_CC TPMA_CC;
#define TPMA_CC_COMMANDINDEX_MASK      ((TPMA_CC) 0x0000ffff)
#define TPMA_CC_COMMANDINDEX_SHIFT     (0)
#define TPMA_CC_RESERVED1_MASK         ((TPMA_CC) 0x003f0000)
#define TPMA_CC_NV                      ((TPMA_CC) 0x00400000)
#define TPMA_CC_EXTENSIVE                ((TPMA_CC) 0x00800000)
#define TPMA_CC_FLUSHED                 ((TPMA_CC) 0x01000000)
#define TPMA_CC_CHANDLES_MASK           ((TPMA_CC) 0x0e000000)
#define TPMA_CC_CHANDLES_SHIFT          (25)
#define TPMA_CC_RHANDLE                 ((TPMA_CC) 0x10000000)
#define TPMA_CC_V                       ((TPMA_CC) 0x20000000)
#define TPMA_CC_RES_MASK                 ((TPMA_CC) 0xc0000000)
#define TPMA_CC_RES_SHIFT                (30)

/* Table 38 - Definition of (UINT32) TPMA_MODES Bits */
typedef UINT32 TPMA_MODES;
#define TPMA_MODES_FIPS_140_2           ((TPMA_MODES) 0x00000001)
#define TPMA_MODES_RESERVED1_MASK      ((TPMA_MODES) 0xffffffffe)

/* Table 39 - Definition of (BYTE) TPMI_YES_NO Type */
typedef BYTE TPMI_YES_NO;

/* Table 40 - Definition of (TPM2_HANDLE) TPMI_DH_OBJECT Type */
typedef TPM2_HANDLE TPMI_DH_OBJECT;

/* Table 41 - Definition of (TPM2_HANDLE) TPMI_DH_PARENT Type */
typedef TPM2_HANDLE TPMI_DH_PARENT;

/* Table 42 - Definition of (TPM2_HANDLE) TPMI_DH_PERSISTENT Type */
typedef TPM2_HANDLE TPMI_DH_PERSISTENT;

```

```
/* Table 43 - Definition of (TPM2_HANDLE) TPMI_DH_ENTITY Type */
typedef TPM2_HANDLE TPMI_DH_ENTITY;

/* Table 44 - Definition of (TPM2_HANDLE) TPMI_DH_PCR Type */
typedef TPM2_HANDLE TPMI_DH_PCR;

/* Table 45 - Definition of (TPM2_HANDLE) TPMI_SH_AUTH_SESSION Type */
typedef TPM2_HANDLE TPMI_SH_AUTH_SESSION;

/* Table 46 - Definition of (TPM2_HANDLE) TPMI_SH_HMAC Type */
typedef TPM2_HANDLE TPMI_SH_HMAC;

/* Table 47 - Definition of (TPM2_HANDLE) TPMI_SH_POLICY Type */
typedef TPM2_HANDLE TPMI_SH_POLICY;

/* Table 48 - Definition of (TPM2_HANDLE) TPMI_DH_CONTEXT Type */
typedef TPM2_HANDLE TPMI_DH_CONTEXT;

/* Table 49 - Definition of (TPM2_HANDLE) TPMI_RH_HIERARCHY Type */
typedef TPM2_HANDLE TPMI_RH_HIERARCHY;

/* Table 50 - Definition of (TPM2_HANDLE) TPMI_RH_ENABLES Type */
typedef TPM2_HANDLE TPMI_RH_ENABLES;

/* Table 51 - Definition of (TPM2_HANDLE) TPMI_RH_HIERARCHY_AUTH Type
*/
typedef TPM2_HANDLE TPMI_RH_HIERARCHY_AUTH;

/* Table 52 - Definition of (TPM2_HANDLE) TPMI_RH_PLATFORM Type */
typedef TPM2_HANDLE TPMI_RH_PLATFORM;

/* Table 53 - Definition of (TPM2_HANDLE) TPMI_RH_OWNER Type */
typedef TPM2_HANDLE TPMI_RH_OWNER;

/* Table 54 - Definition of (TPM2_HANDLE) TPMI_RH_ENDORSEMENT Type */
typedef TPM2_HANDLE TPMI_RH_ENDORSEMENT;

/* Table 55 - Definition of (TPM2_HANDLE) TPMI_RH_PROVISION Type */
typedef TPM2_HANDLE TPMI_RH_PROVISION;

/* Table 56 - Definition of (TPM2_HANDLE) TPMI_RH_CLEAR Type */
typedef TPM2_HANDLE TPMI_RH_CLEAR;

/* Table 57 - Definition of (TPM2_HANDLE) TPMI_RH_NV_AUTH Type */
typedef TPM2_HANDLE TPMI_RH_NV_AUTH;

/* Table 58 - Definition of (TPM2_HANDLE) TPMI_RH_LOCKOUT Type */
typedef TPM2_HANDLE TPMI_RH_LOCKOUT;

/* Table 59 - Definition of (TPM2_HANDLE) TPMI_RH_NV_INDEX Type */
typedef TPM2_HANDLE TPMI_RH_NV_INDEX;

/* Table 60 - Definition of (TPM2_ALG_ID) TPMI_ALG_HASH Type */
typedef TPM2_ALG_ID TPMI_ALG_HASH;

/* Table 61 - Definition of (TPM2_ALG_ID) TPMI_ALG_ASYM Type */
typedef TPM2_ALG_ID TPMI_ALG_ASYM;
```

```

/* Table 62 - Definition of (TPM2_ALG_ID) TPMI_ALG_SYM Type */
typedef TPM2_ALG_ID  TPMI_ALG_SYM;

/* Table 63 - Definition of (TPM2_ALG_ID) TPMI_ALG_SYM_OBJECT Type */
typedef TPM2_ALG_ID  TPMI_ALG_SYM_OBJECT;

/* Table 64 - Definition of (TPM2_ALG_ID) TPMI_ALG_SYM_MODE Type */
typedef TPM2_ALG_ID  TPMI_ALG_SYM_MODE;

/* Table 65 - Definition of (TPM2_ALG_ID) TPMI_ALG_KDF Type */
typedef TPM2_ALG_ID  TPMI_ALG_KDF;

/* Table 66 - Definition of (TPM2_ALG_ID) TPMI_ALG_SIG_SCHEME Type */
typedef TPM2_ALG_ID  TPMI_ALG_SIG_SCHEME;

/* Table 67 - Definition of (TPM2_ALG_ID){ECC} TPMI_ECC_KEY_EXCHANGE
Type */
typedef TPM2_ALG_ID  TPMI_ECC_KEY_EXCHANGE;

/* Table 68 - Definition of (TPM2_ST) TPMI_ST_COMMAND_TAG Type */
typedef TPM2_ST  TPMI_ST_COMMAND_TAG;

/* Table 69 - Definition of TPMS_EMPTY Structure */
typedef struct {
    UINT8  empty[1];
} TPMS_EMPTY;

/* Table 70 - Definition of TPMS_ALGORITHM_DESCRIPTION Structure */
typedef struct {
    TPM2_ALG_ID  alg;
    TPMA_ALGORITHM  attributes;
} TPMS_ALGORITHM_DESCRIPTION;

/* Table 71 - Definition of TPMU_HA Union */
typedef union {
    BYTE  sha [TPM2_SHA_DIGEST_SIZE];          /* TPM2_ALG_SHA */
    BYTE  sha1 [TPM2_SHA1_DIGEST_SIZE];        /* TPM2_ALG_SHA1
*/
    BYTE  sha256 [TPM2_SHA256_DIGEST_SIZE];    /* TPM2_ALG_SHA256
*/
    BYTE  sha384 [TPM2_SHA384_DIGEST_SIZE];    /* TPM2_ALG_SHA384
*/
    BYTE  sha512 [TPM2_SHA512_DIGEST_SIZE];    /* TPM2_ALG_SHA512
*/
    BYTE  sm3_256 [TPM2_SM3_256_DIGEST_SIZE]; /*
TPM2_ALG_SM3_256 */
    TPMS_EMPTY  null;                          /* TPM2_ALG_NULL
*/
} TPMU_HA;

/* Table 72 - Definition of TPMT_HA Structure */
typedef struct {
    TPMI_ALG_HASH  hashAlg;
    TPMU_HA  digest;
} TPMT_HA;

```

```

/* Table 73 - Definition of TPM2B_DIGEST Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[sizeof(TPMU_HA)];
} TPM2B_DIGEST;

/* Table 74 - Definition of TPM2B_DATA Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[sizeof(TPMT_HA)];
} TPM2B_DATA;

/* Table 75 - Definition of Types for TPM2B_NONCE */
typedef TPM2B_DIGEST  TPM2B_NONCE;

/* Table 76 - Definition of Types for TPM2B_AUTH */
typedef TPM2B_DIGEST  TPM2B_AUTH;

/* Table 77 - Definition of Types for TPM2B_OPERAND */
typedef TPM2B_DIGEST  TPM2B_OPERAND;

/* Table 78 - Definition of TPM2B_EVENT Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[1024];
} TPM2B_EVENT;

/* Table 79 - Definition of TPM2B_MAX_BUFFER Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[TPM2_MAX_DIGEST_BUFFER];
} TPM2B_MAX_BUFFER;

/* Table 80 - Definition of TPM2B_MAX_NV_BUFFER Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[TPM2_MAX_NV_BUFFER_SIZE];
} TPM2B_MAX_NV_BUFFER;

/* Table 81 - Definition of Types for TPM2B_TIMEOUT */
typedef TPM2B_DIGEST  TPM2B_TIMEOUT;

/* Table 82 - Definition of TPM2B_IV Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[TPM2_MAX_SYM_BLOCK_SIZE];
} TPM2B_IV;

/* Table 83 - Definition of TPMU_NAME Union */
typedef union {
    TPMT_HA      digest;
    TPM2_HANDLE  handle;
} TPMU_NAME;

/* Table 84 - Definition of TPM2B_NAME Structure */
typedef struct {
    UINT16  size;

```

```

    BYTE    name[sizeof(TPMU_NAME)];
} TPM2B_NAME;

/* Table 85 - Definition of TPMS_PCR_SELECT Structure */
typedef struct {
    UINT8    sizeofSelect;
    BYTE     pcrSelect[TPM2_PCR_SELECT_MAX];
} TPMS_PCR_SELECT;

/* Table 86 - Definition of TPMS_PCR_SELECTION Structure */
typedef struct {
    TPMI_ALG_HASH    hash;
    UINT8            sizeofSelect;
    BYTE             pcrSelect[TPM2_PCR_SELECT_MAX];
} TPMS_PCR_SELECTION;

/* Table 89 - Definition of TPMT_TK_CREATION Structure */
typedef struct {
    TPM2_ST            tag;
    TPMI_RH_HIERARCHY hierarchy;
    TPM2B_DIGEST      digest;
} TPMT_TK_CREATION;

/* Table 90 - Definition of TPMT_TK_VERIFIED Structure */
typedef struct {
    TPM2_ST            tag;
    TPMI_RH_HIERARCHY hierarchy;
    TPM2B_DIGEST      digest;
} TPMT_TK_VERIFIED;

/* Table 91 - Definition of TPMT_TK_AUTH Structure */
typedef struct {
    TPM2_ST            tag;
    TPMI_RH_HIERARCHY hierarchy;
    TPM2B_DIGEST      digest;
} TPMT_TK_AUTH;

/* Table 92 - Definition of TPMT_TK_HASHCHECK Structure */
typedef struct {
    TPM2_ST            tag;
    TPMI_RH_HIERARCHY hierarchy;
    TPM2B_DIGEST      digest;
} TPMT_TK_HASHCHECK;

/* Table 93 - Definition of TPMS_ALG_PROPERTY Structure */
typedef struct {
    TPM2_ALG_ID    alg;
    TPMA_ALGORITHM algProperties;
} TPMS_ALG_PROPERTY;

/* Table 94 - Definition of TPMS_TAGGED_PROPERTY Structure */
typedef struct {
    TPM2_PT    property;
    UINT32     value;
} TPMS_TAGGED_PROPERTY;

/* Table 95 - Definition of TPMS_TAGGED_PCR_SELECT Structure */

```

```

typedef struct {
    TPM2_PT_PCR      tag;
    UINT8           sizeofSelect;
    BYTE            pcrSelect[TPM2_PCR_SELECT_MAX];
} TPMS_TAGGED_PCR_SELECT;

/* Table 96 - Definition of TPMS_TAGGED_POLICY Structure */
typedef struct {
    TPM2_HANDLE      handle;
    TPMT_HA          policyHash;
} TPMS_TAGGED_POLICY;

/* Table 97 - Definition of TPML_CC Structure */
typedef struct {
    UINT32          count;
    TPM2_CC         commandCodes[TPM2_MAX_CAP_CC];
} TPML_CC;

/* Table 98 - Definition of TPML_CCA Structure */
typedef struct {
    UINT32          count;
    TPMA_CC         commandAttributes[TPM2_MAX_CAP_CC];
} TPML_CCA;

/* Table 99 - Definition of TPML_ALG Structure */
typedef struct {
    UINT32          count;
    TPM2_ALG_ID     algorithms[TPM2_MAX_ALG_LIST_SIZE];
} TPML_ALG;

/* Table 100 - Definition of TPML_HANDLE Structure */
typedef struct {
    UINT32          count;
    TPM2_HANDLE     handle[TPM2_MAX_CAP_HANDLES];
} TPML_HANDLE;

/* Table 101 - Definition of TPML_DIGEST Structure */
typedef struct {
    UINT32          count;
    TPM2B_DIGEST    digests[8];
} TPML_DIGEST;

/* Table 102 - Definition of TPML_DIGEST_VALUES Structure */
typedef struct {
    UINT32          count;
    TPMT_HA          digests[TPM2_NUM_PCR_BANKS];
} TPML_DIGEST_VALUES;

/* Table 103 - Definition of TPML_PCR_SELECTION Structure */
typedef struct {
    UINT32          count;
    TPMS_PCR_SELECTION pcrSelections[TPM2_NUM_PCR_BANKS];
} TPML_PCR_SELECTION;

/* Table 104 - Definition of TPML_ALG_PROPERTY Structure */
typedef struct {
    UINT32          count;

```

```

    TPMS_ALG_PROPERTY    algProperties[TPM2_MAX_CAP_ALGS];
} TPML_ALG_PROPERTY;

/* Table 105 - Definition of TPML_TAGGED_TPM_PROPERTY Structure */
typedef struct {
    UINT32                count;
    TPMS_TAGGED_PROPERTY  tpmProperty[TPM2_MAX_TPM_PROPERTIES];
} TPML_TAGGED_TPM_PROPERTY;

/* Table 106 - Definition of TPML_TAGGED_PCR_PROPERTY Structure */
typedef struct {
    UINT32                count;
    TPMS_TAGGED_PCR_SELECT pcrProperty[TPM2_MAX_PCR_PROPERTIES];
} TPML_TAGGED_PCR_PROPERTY;

/* Table 107 - Definition of {ECC} TPML_ECC_CURVE Structure */
typedef struct {
    UINT32                count;
    TPM2_ECC_CURVE        eccCurves[TPM2_MAX_ECC_CURVES];
} TPML_ECC_CURVE;

/* Table 108 - Definition of TPML_TAGGED_POLICY Structure */
typedef struct {
    UINT32                count;
    TPMS_TAGGED_POLICY    policies[TPM2_MAX_TAGGED_POLICIES];
} TPML_TAGGED_POLICY;

/* Table 109 - Definition of TPMU_CAPABILITIES Union */
typedef union {
    TPML_ALG_PROPERTY        algorithms;        /* TPM2_CAP_ALGS */
    TPML_HANDLE              handles;          /* TPM2_CAP_HANDLES */
    TPML_CCA                 command;          /* TPM2_CAP_COMMANDS */
    /*
    TPML_CC                 ppCommands;        /*
TPM2_CAP_PP_COMMANDS */
    TPML_CC                 auditCommands;    /*
TPM2_CAP_AUDIT_COMMANDS */
    TPML_PCR_SELECTION       assignedPCR;     /* TPM2_CAP_PCRS */
    TPML_TAGGED_TPM_PROPERTY tpmProperties;    /*
TPM2_CAP_TPM_PROPERTIES */
    TPML_TAGGED_PCR_PROPERTY pcrProperties;    /*
TPM2_CAP_PCR_PROPERTIES */
    TPML_ECC_CURVE           eccCurves;      /* TPM2_CAP_ECC_CURVES */
    /*
    TPML_TAGGED_POLICY       authPolicies;    /*
TPM2_CAP_AUTH_POLICIES */
} TPMU_CAPABILITIES;

/* Table 110 - Definition of TPMS_CAPABILITY_DATA Structure */
typedef struct {
    TPM2_CAP                capability;
    TPMU_CAPABILITIES        data;
} TPMS_CAPABILITY_DATA;

/* Table 111 - Definition of TPMS_CLOCK_INFO Structure */
typedef struct {
    UINT64                  clock;

```



```

    UINT32          resetCount;
    UINT32          restartCount;
    TPMI_YES_NO    safe;
} TPMS_CLOCK_INFO;

/* Table 112 - Definition of TPMS_TIME_INFO Structure */
typedef struct {
    UINT64          time;
    TPMS_CLOCK_INFO clockInfo;
} TPMS_TIME_INFO;

/* Table 113 - Definition of TPMS_TIME_ATTEST_INFO Structure */
typedef struct {
    TPMS_TIME_INFO time;
    UINT64          firmwareVersion;
} TPMS_TIME_ATTEST_INFO;

/* Table 114 - Definition of TPMS_CERTIFY_INFO Structure */
typedef struct {
    TPM2B_NAME name;
    TPM2B_NAME qualifiedName;
} TPMS_CERTIFY_INFO;

/* Table 115 - Definition of TPMS_QUOTE_INFO Structure */
typedef struct {
    TPML_PCR_SELECTION pcrSelect;
    TPM2B_DIGEST       pcrDigest;
} TPMS_QUOTE_INFO;

/* Table 116 - Definition of TPMS_COMMAND_AUDIT_INFO Structure */
typedef struct {
    UINT64          auditCounter;
    TPM2_ALG_ID     digestAlg;
    TPM2B_DIGEST    auditDigest;
    TPM2B_DIGEST    commandDigest;
} TPMS_COMMAND_AUDIT_INFO;

/* Table 117 - Definition of TPMS_SESSION_AUDIT_INFO Structure */
typedef struct {
    TPMI_YES_NO     exclusiveSession;
    TPM2B_DIGEST    sessionDigest;
} TPMS_SESSION_AUDIT_INFO;

/* Table 118 - Definition of TPMS_CREATION_INFO Structure */
typedef struct {
    TPM2B_NAME      objectName;
    TPM2B_DIGEST    creationHash;
} TPMS_CREATION_INFO;

/* Table 119 - Definition of TPMS_NV_CERTIFY_INFO Structure */
typedef struct {
    TPM2B_NAME      indexName;
    UINT16          offset;
    TPM2B_MAX_NV_BUFFER nvContents;
} TPMS_NV_CERTIFY_INFO;

/* Table 120 - Definition of (TPM2_ST) TPMS_ST_ATTEST Type */

```

```

typedef TPM2_ST  TPMS_ATTEST;

/* Table 121 - Definition of TPMU_ATTEST Union */
typedef union {
    TPMS_CERTIFY_INFO          certify;          /*
TPM2_ST_ATTEST_CERTIFY */
    TPMS_CREATION_INFO        creation;         /*
TPM2_ST_ATTEST_CREATION */
    TPMS_QUOTE_INFO           quote;           /*
TPM2_ST_ATTEST_QUOTE */
    TPMS_COMMAND_AUDIT_INFO    commandAudit;    /*
TPM2_ST_ATTEST_COMMAND_AUDIT */
    TPMS_SESSION_AUDIT_INFO    sessionAudit;    /*
TPM2_ST_ATTEST_SESSION_AUDIT */
    TPMS_TIME_ATTEST_INFO      time;           /* TPM2_ST_ATTEST_TIME
*/
    TPMS_NV_CERTIFY_INFO       nv;             /* TPM2_ST_ATTEST_NV
*/
} TPMU_ATTEST;

/* Table 122 - Definition of TPMS_ATTEST Structure */
typedef struct {
    TPM2_GENERATED             magic;
    TPMS_ATTEST                type;
    TPM2B_NAME                  qualifiedSigner;
    TPM2B_DATA                  extraData;
    TPMS_CLOCK_INFO            clockInfo;
    UINT64                      firmwareVersion;
    TPMU_ATTEST                 attested;
} TPMS_ATTEST;

/* Table 123 - Definition of TPM2B_ATTEST Structure */
typedef struct {
    UINT16  size;
    BYTE    attestationData[sizeof(TPMS_ATTEST)];
} TPM2B_ATTEST;

/* Table 124 - Definition of TPMS_AUTH_COMMAND Structure */
typedef struct {
    TPMS_AUTH_SESSION_HANDLE sessionHandle;
    TPM2B_NONCE               nonce;
    TPMA_SESSION              sessionAttributes;
    TPM2B_AUTH                hmac;
} TPMS_AUTH_COMMAND;

/* Table 125 - Definition of TPMS_AUTH_RESPONSE Structure */
typedef struct {
    TPM2B_NONCE               nonce;
    TPMA_SESSION              sessionAttributes;
    TPM2B_AUTH                hmac;
} TPMS_AUTH_RESPONSE;

/* Table 126 - Definition of { !ALG.S } (TPM2_KEY_BITS)
TPMI_!ALG.S_KEY_BITS Type */
typedef TPM2_KEY_BITS  TPMI_AES_KEY_BITS;
typedef TPM2_KEY_BITS  TPMI_SM4_KEY_BITS;
typedef TPM2_KEY_BITS  TPMI_CAMELLIA_KEY_BITS;

```

```

/* Table 127 - Definition of TPMU_SYM_KEY_BITS Union */
typedef union {
    TPMI_AES_KEY_BITS        aes;           /* TPM2_ALG_AES */
    TPMI_SM4_KEY_BITS        sm4;          /* TPM2_ALG_SM4 */
    TPMI_CAMELLIA_KEY_BITS   camellia;     /* TPM2_ALG_CAMELLIA */
    TPM2_KEY_BITS            sym;
    TPMI_ALG_HASH            exclusiveOr;  /* TPM2_ALG_XOR */
    TPMS_EMPTY               null;        /* TPM2_ALG_NULL */
} TPMU_SYM_KEY_BITS;

/* Table 128 - Definition of TPMU_SYM_MODE Union */
typedef union {
    TPMI_ALG_SYM_MODE        aes;           /* TPM2_ALG_AES */
    TPMI_ALG_SYM_MODE        sm4;          /* TPM2_ALG_SM4 */
    TPMI_ALG_SYM_MODE        camellia;     /* TPM2_ALG_CAMELLIA */
    TPMI_ALG_SYM_MODE        sym;
    TPMS_EMPTY               exclusiveOr;  /* TPM2_ALG_XOR */
    TPMS_EMPTY               null;        /* TPM2_ALG_NULL */
} TPMU_SYM_MODE;

/* Table 130 - Definition of TPMT_SYM_DEF Structure */
typedef struct {
    TPMI_ALG_SYM              algorithm;
    TPMU_SYM_KEY_BITS         keyBits;
    TPMU_SYM_MODE             mode;
} TPMT_SYM_DEF;

/* Table 131 - Definition of TPMT_SYM_DEF_OBJECT Structure */
typedef struct {
    TPMI_ALG_SYM_OBJECT       algorithm;
    TPMU_SYM_KEY_BITS         keyBits;
    TPMU_SYM_MODE             mode;
} TPMT_SYM_DEF_OBJECT;

/* Table 132 - Definition of TPM2B_SYM_KEY Structure */
typedef struct {
    UINT16    size;
    BYTE      buffer[TPM2_MAX_SYM_KEY_BYTES];
} TPM2B_SYM_KEY;

/* Table 133 - Definition of TPMS_SYMCIPHER_PARMS Structure */
typedef struct {
    TPMT_SYM_DEF_OBJECT       sym;
} TPMS_SYMCIPHER_PARMS;

/* Table 134 - Definition of TPM2B_LABEL Structure */
typedef struct {
    UINT16    size;
    BYTE      buffer[TPM2_LABEL_MAX_BUFFER];
} TPM2B_LABEL;

/* Table 135 - Definition of TPMS_DERIVE Structure */
typedef struct {
    TPM2B_LABEL    label;
    TPM2B_LABEL    context;
} TPMS_DERIVE;

```

```

/* Table 136 - Definition of TPM2B_DERIVE Structure */
typedef struct {
    UINT16 size;
    BYTE buffer[ sizeof(TPMS_DERIVE) ];
} TPM2B_DERIVE;

/* Table 137 - Definition of TPMU_SENSITIVE_CREATE Union */
typedef union {
    BYTE create[TPM2_MAX_SYM_DATA];
    TPMS_DERIVE derive;
} TPMU_SENSITIVE_CREATE;

/* Table 138 - Definition of TPM2B_SENSITIVE_DATA Structure */
typedef struct {
    UINT16 size;
    BYTE buffer[ sizeof(TPMU_SENSITIVE_CREATE) ];
} TPM2B_SENSITIVE_DATA;

/* Table 139 - Definition of TPMS_SENSITIVE_CREATE Structure */
typedef struct {
    TPM2B_AUTH userAuth;
    TPM2B_SENSITIVE_DATA data;
} TPMS_SENSITIVE_CREATE;

/* Table 140 - Definition of TPM2B_SENSITIVE_CREATE Structure */
typedef struct {
    UINT16 size;
    TPMS_SENSITIVE_CREATE sensitive;
} TPM2B_SENSITIVE_CREATE;

/* Table 141 - Definition of TPMS_SCHEME_HASH Structure */
typedef struct {
    TPMI_ALG_HASH hashAlg;
} TPMS_SCHEME_HASH;

/* Table 142 - Definition of { ECC } TPMS_SCHEME_ECDSA Structure */
typedef struct {
    TPMI_ALG_HASH hashAlg;
    UINT16 count;
} TPMS_SCHEME_ECDSA;

/* Table 143 - Definition of (TPM2_ALG_ID) TPMI_ALG_KEYEDHASH_SCHEME
Type */
typedef TPM2_ALG_ID TPMI_ALG_KEYEDHASH_SCHEME;

/* Table 144 - Definition of Types for HMAC_SIG_SCHEME */
typedef TPMS_SCHEME_HASH TPMS_SCHEME_HMAC;

/* Table 145 - Definition of TPMS_SCHEME_XOR Structure */
typedef struct {
    TPMI_ALG_HASH hashAlg;
    TPMI_ALG_KDF kdf;
} TPMS_SCHEME_XOR;

/* Table 146 - Definition of TPMU_SCHEME_KEYEDHASH Union */
typedef union {

```

```

    TPMS_SCHEME_HMAC      hmac;          /* TPM2_ALG_HMAC */
    TPMS_SCHEME_XOR       exclusiveOr;   /* TPM2_ALG_XOR */
    TPMS_EMPTY            null;          /* TPM2_ALG_NULL */
} TPMU_SCHEME_KEYEDHASH;

/* Table 147 - Definition of TPMT_KEYEDHASH_SCHEME Structure */
typedef struct {
    TPMS_SCHEME_KEYEDHASH_SCHEME  scheme;
    TPMU_SCHEME_KEYEDHASH         details;
} TPMT_KEYEDHASH_SCHEME;

/* Table 148 - Definition of {RSA} Types for RSA Signature Schemes */
typedef TPMS_SCHEME_HASH TPMS_SIG_SCHEME_RSASSA;
typedef TPMS_SCHEME_HASH TPMS_SIG_SCHEME_RSAPSS;

/* Table 149 - Definition of {ECC} Types for ECC Signature Schemes */
typedef TPMS_SCHEME_HASH TPMS_SIG_SCHEME_ECDSA;
typedef TPMS_SCHEME_HASH TPMS_SIG_SCHEME_SM2;
typedef TPMS_SCHEME_HASH TPMS_SIG_SCHEME_ECSCNORR;
typedef TPMS_SCHEME_ECDA A TPMS_SIG_SCHEME_ECDA A;

/* Table 150 - Definition of TPMU_SIG_SCHEME Union */
typedef union {
    TPMS_SIG_SCHEME_RSASSA      rsassa;      /* TPM2_ALG_RSASSA */
    TPMS_SIG_SCHEME_RSAPSS     rsapss;      /* TPM2_ALG_RSAPSS */
    TPMS_SIG_SCHEME_ECDSA      ecdsa;       /* TPM2_ALG_ECDSA */
    TPMS_SIG_SCHEME_ECDA A     ecdaa;       /* TPM2_ALG_ECDA A */
    TPMS_SIG_SCHEME_SM2        sm2;         /* TPM2_ALG_SM2 */
    TPMS_SIG_SCHEME_ECSCNORR   ecschnorr;   /* TPM2_ALG_ECSCNORR */
    TPMS_SCHEME_HMAC           hmac;        /* TPM2_ALG_HMAC */
    TPMS_SCHEME_HASH           any;
    TPMS_EMPTY                 null;        /* TPM2_ALG_NULL */
} TPMU_SIG_SCHEME;

/* Table 151 - Definition of TPMT_SIG_SCHEME Structure */
typedef struct {
    TPMS_SIG_SCHEME             scheme;
    TPMU_SIG_SCHEME            details;
} TPMT_SIG_SCHEME;

/* Table 152 - Definition of Types for {RSA} Encryption Schemes */
typedef TPMS_SCHEME_HASH TPMS_ENC_SCHEME_OAEP;
typedef TPMS_EMPTY TPMS_ENC_SCHEME_RSAES;

/* Table 153 - Definition of Types for {ECC} ECC Key Exchange */
typedef TPMS_SCHEME_HASH TPMS_KEY_SCHEME_ECDH;
typedef TPMS_SCHEME_HASH TPMS_KEY_SCHEME_ECMQV;

/* Table 154 - Definition of Types for KDF Schemes */
typedef TPMS_SCHEME_HASH TPMS_SCHEME_MGF1;
typedef TPMS_SCHEME_HASH TPMS_SCHEME_KDF1_SP800_56A;
typedef TPMS_SCHEME_HASH TPMS_SCHEME_KDF2;
typedef TPMS_SCHEME_HASH TPMS_SCHEME_KDF1_SP800_108;

/* Table 155 - Definition of TPMU_KDF_SCHEME Union */
typedef union {
    TPMS_SCHEME_MGF1           mgf1;          /* TPM2_ALG_MGF1 */

```

```

    TPMS_SCHEME_KDF1_SP800_56A  kdf1_sp800_56a;  /*
TPM2_ALG_KDF1_SP800_56A */
    TPMS_SCHEME_KDF2            kdf2;           /* TPM2_ALG_KDF2 */
    TPMS_SCHEME_KDF1_SP800_108  kdf1_sp800_108; /*
TPM2_ALG_KDF1_SP800_108 */
    TPMS_EMPTY                  null;           /* TPM2_ALG_NULL */
} TPMU_KDF_SCHEME;

/* Table 156 - Definition of TPMT_KDF_SCHEME Structure */
typedef struct {
    TPMT_ALG_KDF            scheme;
    TPMU_KDF_SCHEME        details;
} TPMT_KDF_SCHEME;

/* Table 157 - Definition of (TPM2_ALG_ID) TPMT_ALG_ASYM_SCHEME Type */
typedef TPM2_ALG_ID  TPMT_ALG_ASYM_SCHEME;

/* Table 158 - Definition of TPMU_ASYM_SCHEME Union */
typedef union {
    TPMS_KEY_SCHEME_ECDH      ecdh;           /* TPM2_ALG_ECDH */
    TPMS_KEY_SCHEME_ECMQV     ecmqv;         /* TPM2_ALG_ECMQV */
    TPMS_SIG_SCHEME_RSASSA    rsassa;        /* TPM2_ALG_RSASSA */
    TPMS_SIG_SCHEME_RSAPSS    rsapss;        /* TPM2_ALG_RSAPSS */
    TPMS_SIG_SCHEME_ECDSA     ecdsa;         /* TPM2_ALG_ECDSA */
    TPMS_SIG_SCHEME_ECDA     ecdaa;         /* TPM2_ALG_ECDA     */
    TPMS_SIG_SCHEME_SM2       sm2;          /* TPM2_ALG_SM2     */
    TPMS_SIG_SCHEME_ECSCHNORR  ecschnorr;    /* TPM2_ALG_ECSCHNORR */
    TPMS_ENC_SCHEME_RSAES     rsaes;         /* TPM2_ALG_RSAES   */
    TPMS_ENC_SCHEME_OAEP      oaep;          /* TPM2_ALG_OAEP    */
    TPMS_SCHEME_HASH          anySig;
    TPMS_EMPTY                null;          /* TPM2_ALG_NULL    */
} TPMU_ASYM_SCHEME;

/* Table 159 - Definition of TPMT_ASYM_SCHEME Structure */
typedef struct {
    TPMT_ALG_ASYM_SCHEME    scheme;
    TPMU_ASYM_SCHEME        details;
} TPMT_ASYM_SCHEME;

/* Table 160 - Definition of (TPM2_ALG_ID) { RSA } TPMT_ALG_RSA_SCHEME
Type */
typedef TPM2_ALG_ID  TPMT_ALG_RSA_SCHEME;

/* Table 161 - Definition of { RSA } TPMT_RSA_SCHEME Structure */
typedef struct {
    TPMT_ALG_RSA_SCHEME     scheme;
    TPMU_ASYM_SCHEME        details;
} TPMT_RSA_SCHEME;

/* Table 162 - Definition of (TPM2_ALG_ID) { RSA } TPMT_ALG_RSA_DECRYPT
Type */
typedef TPM2_ALG_ID  TPMT_ALG_RSA_DECRYPT;

/* Table 163 - Definition of { RSA } TPMT_RSA_DECRYPT Structure */
typedef struct {
    TPMT_ALG_RSA_DECRYPT     scheme;
    TPMU_ASYM_SCHEME        details;
} TPMT_RSA_DECRYPT;

```

```

} TPMT_RSA_DECRYPT;

/* Table 164 - Definition of { RSA } TPM2B_PUBLIC_KEY_RSA Structure */
typedef struct {
    UINT16 size;
    BYTE buffer[ TPM2_MAX_RSA_KEY_BYTES ];
} TPM2B_PUBLIC_KEY_RSA;

/* Table 165 - Definition of { RSA } (TPM2_KEY_BITS) TPMT_RSA_KEY_BITS
Type */
typedef TPM2_KEY_BITS TPMT_RSA_KEY_BITS;

/* Table 166 - Definition of { RSA } TPM2B_PRIVATE_KEY_RSA Structure */
typedef struct {
    UINT16 size;
    BYTE buffer[TPM2_MAX_RSA_KEY_BYTES/2];
} TPM2B_PRIVATE_KEY_RSA;

/* Table 167 - Definition of TPM2B_ECC_PARAMETER Structure */
typedef struct {
    UINT16 size;
    BYTE buffer[TPM2_MAX_ECC_KEY_BYTES];
} TPM2B_ECC_PARAMETER;

/* Table 168 - Definition of { ECC } TPMS_ECC_POINT Structure */
typedef struct {
    TPM2B_ECC_PARAMETER x;
    TPM2B_ECC_PARAMETER y;
} TPMS_ECC_POINT;

/* Table 169 - Definition of { ECC } TPM2B_ECC_POINT Structure */
typedef struct {
    UINT16 size;
    TPMS_ECC_POINT point;
} TPM2B_ECC_POINT;

/* Table 170 - Definition of (TPM2_ALG_ID) { ECC } TPMT_ALG_ECC_SCHEME
Type */
typedef TPM2_ALG_ID TPMT_ALG_ECC_SCHEME;

/* Table 171 - Definition of { ECC } (TPM2_ECC_CURVE) TPMT_ECC_CURVE
Type */
typedef TPM2_ECC_CURVE TPMT_ECC_CURVE;

/* Table 172 - Definition of (TPMT_SIG_SCHEME) { ECC } TPMT_ECC_SCHEME
Structure */
typedef struct {
    TPMT_ALG_ECC_SCHEME scheme;
    TPMU_ASYM_SCHEME details;
} TPMT_ECC_SCHEME;

/* Table 173 - Definition of { ECC } TPMS_ALGORITHM_DETAIL_ECC
Structure */
typedef struct {
    TPM2_ECC_CURVE curveID;
    UINT16 keySize;
    TPMT_KDF_SCHEME kdf;

```

```

    TPMT_ECC_SCHEME          sign;
    TPM2B_ECC_PARAMETER      p;
    TPM2B_ECC_PARAMETER      a;
    TPM2B_ECC_PARAMETER      b;
    TPM2B_ECC_PARAMETER      gX;
    TPM2B_ECC_PARAMETER      gY;
    TPM2B_ECC_PARAMETER      n;
    TPM2B_ECC_PARAMETER      h;
} TPMS_ALGORITHM_DETAIL_ECC;

/* Table 174 - Definition of { RSA } TPMS_SIGNATURE_RSA Structure */
typedef struct {
    TPMT_ALG_HASH            hash;
    TPM2B_PUBLIC_KEY_RSA     sig;
} TPMS_SIGNATURE_RSA;

/* Table 175 - Definition of Types for {RSA} Signature */
typedef TPMS_SIGNATURE_RSA TPMS_SIGNATURE_RSASSA;
typedef TPMS_SIGNATURE_RSA TPMS_SIGNATURE_RSAPSS;

/* Table 176 - Definition of { ECC } TPMS_SIGNATURE_ECC Structure */
typedef struct {
    TPMT_ALG_HASH            hash;
    TPM2B_ECC_PARAMETER      signatureR;
    TPM2B_ECC_PARAMETER      signatureS;
} TPMS_SIGNATURE_ECC;

/* Table 177 - Definition of Types for {ECC} TPMS_SIGNATURE_ECC */
typedef TPMS_SIGNATURE_ECC TPMS_SIGNATURE_ECDSA;
typedef TPMS_SIGNATURE_ECC TPMS_SIGNATURE_ECDA;
typedef TPMS_SIGNATURE_ECC TPMS_SIGNATURE_SM2;
typedef TPMS_SIGNATURE_ECC TPMS_SIGNATURE_EC Schnorr;

/* Table 178 - Definition of TPMU_SIGNATURE Union */
typedef union {
    TPMS_SIGNATURE_RSASSA     rsassa;        /* TPM2_ALG_RSASSA */
    TPMS_SIGNATURE_RSAPSS     rsapss;        /* TPM2_ALG_RSAPSS */
    TPMS_SIGNATURE_ECDSA      ecdsa;         /* TPM2_ALG_ECDSA */
    TPMS_SIGNATURE_ECDA       ecdaa;         /* TPM2_ALG_ECDA */
    TPMS_SIGNATURE_SM2        sm2;           /* TPM2_ALG_SM2 */
    TPMS_SIGNATURE_EC Schnorr     ecschnorr; /* TPM2_ALG_EC Schnorr */
    TPMT_HA                    hmac;         /* TPM2_ALG_HMAC */
    TPMS_SCHEME_HASH           any;
    TPMS_EMPTY                  null;        /* TPM2_ALG_NULL */
} TPMU_SIGNATURE;

/* Table 179 - Definition of TPMT_SIGNATURE Structure */
typedef struct {
    TPMT_ALG_SIG_SCHEME       sigAlg;
    TPMU_SIGNATURE             signature;
} TPMT_SIGNATURE;

/* Table 180 - Definition of TPMU_ENCRYPTED_SECRET Union */
typedef union {
    BYTE    ecc[sizeof(TPMS_ECC_POINT)];     /* TPM2_ALG_ECC */
    BYTE    rsa[TPM2_MAX_RSA_KEY_BYTES];    /* TPM2_ALG_RSA */
}

```



```

    BYTE    symmetric[sizeof(TPM2B_DIGEST)];    /* TPM2_ALG_SYMCIPHER
*/
    BYTE    keyedHash[sizeof(TPM2B_DIGEST)];    /* TPM2_ALG_KEYEDHASH
*/
} TPMU_ENCRYPTED_SECRET;

/* Table 181 - Definition of TPM2B_ENCRYPTED_SECRET Structure */
typedef struct {
    UINT16  size;
    BYTE    secret[sizeof(TPMU_ENCRYPTED_SECRET)];
} TPM2B_ENCRYPTED_SECRET;

/* Table 182 - Definition of (TPM2_ALG_ID) TPMT_ALG_PUBLIC Type */
typedef TPM2_ALG_ID  TPMT_ALG_PUBLIC;

/* Table 183 - Definition of TPMU_PUBLIC_ID Union */
typedef union {
    TPM2B_DIGEST          keyedHash;    /* TPM2_ALG_KEYEDHASH */
    TPM2B_DIGEST          sym;          /* TPM2_ALG_SYMCIPHER */
    TPM2B_PUBLIC_KEY_RSA  rsa;          /* TPM2_ALG_RSA */
    TPMS_ECC_POINT        ecc;          /* TPM2_ALG_ECC */
    TPMS_DERIVE           derive;
} TPMU_PUBLIC_ID;

/* Table 184 - Definition of TPMS_KEYEDHASH_PARMS Structure */
typedef struct {
    TPMT_KEYEDHASH_SCHEME  scheme;
} TPMS_KEYEDHASH_PARMS;

/* Table 185 - Definition of TPMS_ASYM_PARMS Structure */
typedef struct {
    TPMT_SYM_DEF_OBJECT    symmetric;
    TPMT_ASYM_SCHEME       scheme;
} TPMS_ASYM_PARMS;

/* Table 186 - Definition of { RSA } TPMS_RSA_PARMS Structure */
typedef struct {
    TPMT_SYM_DEF_OBJECT    symmetric;
    TPMT_RSA_SCHEME        scheme;
    TPMT_RSA_KEY_BITS      keyBits;
    UINT32                 exponent;
} TPMS_RSA_PARMS;

/* Table 187 - Definition of { ECC } TPMS_ECC_PARMS Structure */
typedef struct {
    TPMT_SYM_DEF_OBJECT    symmetric;
    TPMT_ECC_SCHEME        scheme;
    TPMT_ECC_CURVE         curveID;
    TPMT_KDF_SCHEME        kdf;
} TPMS_ECC_PARMS;

/* Table 188 - Definition of TPMU_PUBLIC_PARMS Union */
typedef union {
    TPMS_KEYEDHASH_PARMS  keyedHashDetail;    /* TPM2_ALG_KEYEDHASH
*/
    TPMS_SYMCIPHER_PARMS  symDetail;          /* TPM2_ALG_SYMCIPHER
*/
}

```

```

    TPMS_RSA_PARMS          rsaDetail;          /* TPM2_ALG_RSA */
    TPMS_ECC_PARMS          eccDetail;          /* TPM2_ALG_ECC */
    TPMS_ASYM_PARMS         asymDetail;
} TPMU_PUBLIC_PARMS;

/* Table 189 - Definition of TPMT_PUBLIC_PARMS Structure */
typedef struct {
    TPMI_ALG_PUBLIC          type;
    TPMU_PUBLIC_PARMS       parameters;
} TPMT_PUBLIC_PARMS;

/* Table 190 - Definition of TPMT_PUBLIC Structure */
typedef struct {
    TPMI_ALG_PUBLIC          type;
    TPMI_ALG_HASH            nameAlg;
    TPMA_OBJECT              objectAttributes;
    TPM2B_DIGEST             authPolicy;
    TPMU_PUBLIC_PARMS       parameters;
    TPMU_PUBLIC_ID           unique;
} TPMT_PUBLIC;

/* Table 191 - Definition of TPM2B_PUBLIC Structure */
typedef struct {
    UINT16                   size;
    TPMT_PUBLIC              publicArea;
} TPM2B_PUBLIC;

/* Table 192 - Definition of TPM2B_TEMPLATE Structure */
typedef struct {
    UINT16                   size;
    BYTE                     buffer[sizeof(TPMT_PUBLIC)];
} TPM2B_TEMPLATE;

/* Table 193 - Definition of TPM2B_PRIVATE_VENDOR_SPECIFIC Structure */
typedef struct {
    UINT16                   size;
    BYTE                     buffer[TPM2_PRIVATE_VENDOR_SPECIFIC_BYTES];
} TPM2B_PRIVATE_VENDOR_SPECIFIC;

/* Table 194 - Definition of TPMU_SENSITIVE_COMPOSITE Union */
typedef union {
    TPM2B_PRIVATE_KEY_RSA    rsa;          /* TPM2_ALG_RSA */
    TPM2B_ECC_PARAMETER      ecc;          /* TPM2_ALG_ECC */
    TPM2B_SENSITIVE_DATA     bits;         /* TPM2_ALG_KEYEDHASH */
    TPM2B_SYM_KEY             sym;         /* TPM2_ALG_SYMCIPHER */
    TPM2B_PRIVATE_VENDOR_SPECIFIC any;
} TPMU_SENSITIVE_COMPOSITE;

/* Table 195 - Definition of TPMT_SENSITIVE Structure */
typedef struct {
    TPMI_ALG_PUBLIC          sensitiveType;
    TPM2B_AUTH               authValue;
    TPM2B_DIGEST             seedValue;
    TPMU_SENSITIVE_COMPOSITE sensitive;
} TPMT_SENSITIVE;

/* Table 196 - Definition of TPM2B_SENSITIVE Structure */

```

```

typedef struct {
    UINT16      size;
    TPMT_SENSITIVE sensitiveArea;
} TPM2B_SENSITIVE;

/* Table 197 - Definition of _PRIVATE Structure */
typedef struct {
    TPM2B_DIGEST      integrityOuter;
    TPM2B_DIGEST      integrityInner;
    TPM2B_SENSITIVE   sensitive;
} _PRIVATE;

/* Table 198 - Definition of TPM2B_PRIVATE Structure */
typedef struct {
    UINT16  size;
    BYTE    buffer[sizeof(_PRIVATE)];
} TPM2B_PRIVATE;

/* Table 199 - Definition of TPMS_ID_OBJECT Structure */
typedef struct {
    TPM2B_DIGEST      integrityHMAC;
    TPM2B_DIGEST      encIdentity;
} TPMS_ID_OBJECT;

/* Table 200 - Definition of TPM2B_ID_OBJECT Structure */
typedef struct {
    UINT16  size;
    BYTE    credential[sizeof(TPMS_ID_OBJECT)];
} TPM2B_ID_OBJECT;

/* Table 201 - Definition of (UINT32) TPM2_NV_INDEX Bits */
typedef UINT32 TPM2_NV_INDEX;
#define TPM2_NV_INDEX_INDEX_MASK      ((TPM2_NV_INDEX) 0x00ffffff)
#define TPM2_NV_INDEX_INDEX_SHIFT    (0)
#define TPM2_NV_INDEX_RH_NV_MASK     ((TPM2_NV_INDEX) 0xff000000)
#define TPM2_NV_INDEX_RH_NV_SHIFT    (24)

/* Table 202 - Definition of TPM2_NT Constants */
typedef UINT8 TPM2_NT;
#define TPM2_NT_ORDINARY      ((TPM2_NT) 0x0)
#define TPM2_NT_COUNTER      ((TPM2_NT) 0x1)
#define TPM2_NT_BITS         ((TPM2_NT) 0x2)
#define TPM2_NT_EXTEND       ((TPM2_NT) 0x4)
#define TPM2_NT_PIN_FAIL     ((TPM2_NT) 0x8)
#define TPM2_NT_PIN_PASS     ((TPM2_NT) 0x9)

/* Table 203 - Definition of TPMS_NV_PIN_COUNTER_PARAMETERS Structure */
typedef struct {
    UINT32  pinCount;
    UINT32  pinLimit;
} TPMS_NV_PIN_COUNTER_PARAMETERS;

/* Table 204 - Definition of (UINT32) TPMA_NV Bits */
typedef UINT32 TPMA_NV;
#define TPMA_NV_PPWRITE      ((TPMA_NV) 0x00000001)
#define TPMA_NV_OWNERWRITE  ((TPMA_NV) 0x00000002)

```

```

#define TPMA_NV_AUTHWRITE          ((TPMA_NV) 0x00000004)
#define TPMA_NV_POLICYWRITE        ((TPMA_NV) 0x00000008)
#define TPMA_NV_TPM2_NT_MASK      ((TPMA_NV) 0x000000f0)
#define TPMA_NV_TPM2_NT_SHIFT     (4)
#define TPMA_NV_RESERVED1_MASK    ((TPMA_NV) 0x00000300)
#define TPMA_NV_POLICY_DELETE     ((TPMA_NV) 0x00000400)
#define TPMA_NV_WRITELOCKED       ((TPMA_NV) 0x00000800)
#define TPMA_NV_WRITEALL          ((TPMA_NV) 0x00001000)
#define TPMA_NV_WRITEDEFINE       ((TPMA_NV) 0x00002000)
#define TPMA_NV_WRITE_STCLEAR     ((TPMA_NV) 0x00004000)
#define TPMA_NV_GLOBALLOCK        ((TPMA_NV) 0x00008000)
#define TPMA_NV_PPREAD            ((TPMA_NV) 0x00010000)
#define TPMA_NV_OWNERREAD         ((TPMA_NV) 0x00020000)
#define TPMA_NV_AUTHREAD          ((TPMA_NV) 0x00040000)
#define TPMA_NV_POLICYREAD        ((TPMA_NV) 0x00080000)
#define TPMA_NV_RESERVED2_MASK    ((TPMA_NV) 0x01f00000)
#define TPMA_NV_NO_DA             ((TPMA_NV) 0x02000000)
#define TPMA_NV_ORDERLY           ((TPMA_NV) 0x04000000)
#define TPMA_NV_CLEAR_STCLEAR     ((TPMA_NV) 0x08000000)
#define TPMA_NV_READLOCKED        ((TPMA_NV) 0x10000000)
#define TPMA_NV_WRITTEN           ((TPMA_NV) 0x20000000)
#define TPMA_NV_PLATFORMCREATE    ((TPMA_NV) 0x40000000)
#define TPMA_NV_READ_STCLEAR      ((TPMA_NV) 0x80000000)

/* Table 205 - Definition of TPMS_NV_PUBLIC Structure */
typedef struct {
    TPMI_RH_NV_INDEX    nvIndex;
    TPMI_ALG_HASH       nameAlg;
    TPMA_NV             attributes;
    TPM2B_DIGEST        authPolicy;
    UINT16              dataSize;
} TPMS_NV_PUBLIC;

/* Table 206 - Definition of TPM2B_NV_PUBLIC Structure */
typedef struct {
    UINT16              size;
    TPMS_NV_PUBLIC     nvPublic;
} TPM2B_NV_PUBLIC;

/* Table 207 - Definition of TPM2B_CONTEXT_SENSITIVE Structure */
typedef struct {
    UINT16              size;
    BYTE               buffer[TPM2_MAX_CONTEXT_SIZE];
} TPM2B_CONTEXT_SENSITIVE;

/* Table 208 - Definition of TPMS_CONTEXT_DATA Structure */
typedef struct {
    TPM2B_DIGEST        integrity;
    TPM2B_CONTEXT_SENSITIVE encrypted;
} TPMS_CONTEXT_DATA;

/* Table 209 - Definition of TPM2B_CONTEXT_DATA Structure */
typedef struct {
    UINT16              size;
    BYTE               buffer[sizeof(TPMS_CONTEXT_DATA)];
} TPM2B_CONTEXT_DATA;

```

```
/* Table 210 - Definition of TPMS_CONTEXT Structure */
typedef struct {
    UINT64          sequence;
    TPMI_DH_CONTEXT savedHandle;
    TPMI_RH_HIERARCHY hierarchy;
    TPM2B_CONTEXT_DATA contextBlob;
} TPMS_CONTEXT;

/* Table 212 - Definition of TPMS_CREATION_DATA Structure */
typedef struct {
    TPML_PCR_SELECTION pcrSelect;
    TPM2B_DIGEST       pcrDigest;
    TPMA_LOCALITY      locality;
    TPM2_ALG_ID        parentNameAlg;
    TPM2B_NAME         parentName;
    TPM2B_NAME         parentQualifiedName;
    TPM2B_DATA         outsideInfo;
} TPMS_CREATION_DATA;

/* Table 213 - Definition of TPM2B_CREATION_DATA Structure */
typedef struct {
    UINT16          size;
    TPMS_CREATION_DATA creationData;
} TPM2B_CREATION_DATA;
```

#### 4.4 tss2\_tpm2\_types.h Postlude

```
#endif /* TSS2_TPM2_TYPES_H */
```